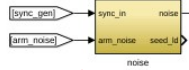


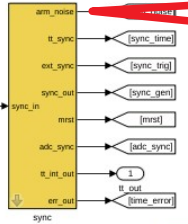
VLA DTS Interface
2 IF x 2 pol (8-bit mode)
4 IF x 2 pol (3-bit mode)



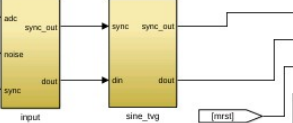
Noise Test Generators



**Input selection
& bit stats**



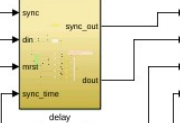
Delay Compensation



Timestamp Generation



**Sine-wave
signal injection**



**Autocorrelation
(full bit width)**

**Delay / Phase
tracking**

**LO offset
removal**

**1 MHz PFB
channelization**

**Bandpass
Equalization &
8-bit Quantize**

**Channel
Selection**

Packetization

**100 GbE
Outputs**

**cross-pol/IF
correlation**

**8-bit Arbitrary
Test Vector
Insertion**

