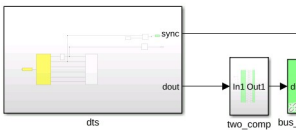


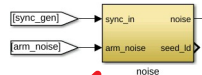
VLA DTS Interface

2 IF x 2 pol (8-bit mode)
4 IF x 2 pol (3-bit mode)

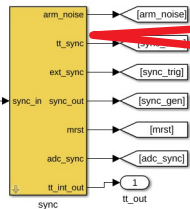
4 inputs (2 IFs x dual pol)
Each 8 parallel words x 12 bits
(96 bits per input)
(384 bits total)



Noise Test Generators



Input selection & bit stats



Timestamp Generation

Delay Compensation

LO offset removal

1 MHz PFB channelization

Bandpass Equalization & 8-bit Quantize

Channel Selection

Packetization

100 GbE Output

