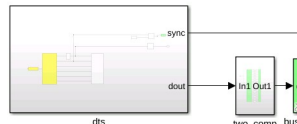


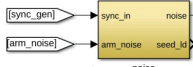
VLA DTS Interface

2 IF x 2 pol (8-bit mode)
4 IF x 2 pol (3-bit mode)

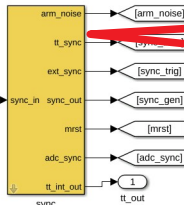
4 inputs (2 IFs x dual pol)
Each 8 parallel words x 12 bits
(96 bits per input)
(384 bits total)



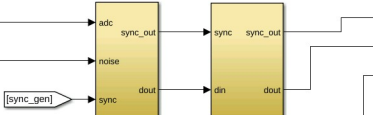
Noise Test Generators



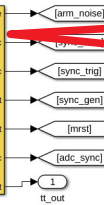
Input selection & bit stats



Delay Compensation



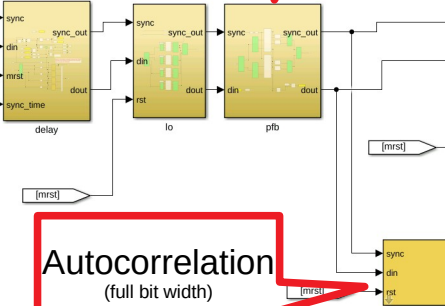
Timestamp Generation



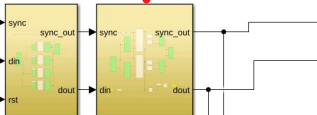
Sine-wave signal injection



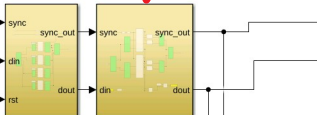
Autocorrelation (full bit width)



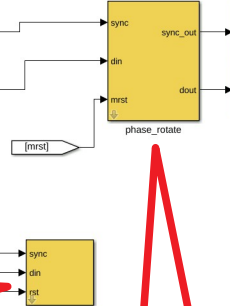
LO offset removal



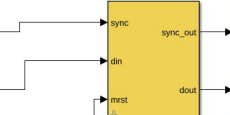
1 MHz PFB channelization



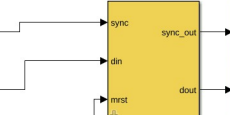
Delay / Phase tracking



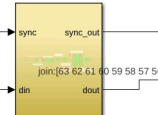
Bandpass Equalization & 8-bit Quantize



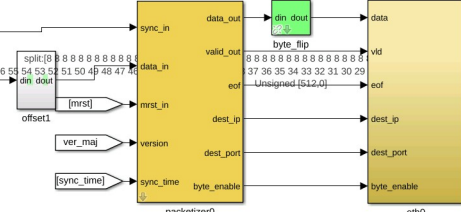
8-bit Arbitrary Test Vector Insertion



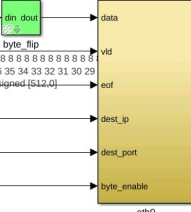
Channel Selection



Packetization



100 GbE Output



cross-pol/IF correlation

