

VLA DTS Interface

2 IF x 2 pol (8-bit mode)
4 IF x 2 pol (3-bit mode)

4 inputs (2 IFs x dual pol)
Each 8 parallel words x 12 bits
(96 bits per input)
(384 bits total)

Timestamp Generation

LO offset removal

Bandpass Equalization & 8-bit Quantize

Channel Selection

Packetization

100 GbE Outputs

Delay Compensation

1 MHz PFB channelization

Autocorrelation (full bit width)

Delay / Phase tracking

8-bit Arbitrary Test Vector Insertion

cross-pol/IF correlation

Noise Test Generators

Input selection & bit stats

Sine-wave signal injection

