

Course : Computer Organization – ENCM 369

Lab # : Lab 6

Group Submission for : B04

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## Exercise A: 12-bit two's complement and RISC-V instructions

Here is a RISC-V assembly language instruction to allocate 28 words on the stack:

```
addi    sp, sp, -112
```

Answer the following questions:

1. What is the machine code for the instruction? Show how you obtained your answer, and express your answer in base two.
2. Suppose before the instruction is run,  $sp = 0x7fff\_ed80$ . A 32-bit adder will compute the new value for  $sp$ . What are the two 32-bit inputs to the adder, and what is the 32-bit output of the adder? Show how you obtained your answers, and express all your answers in base two.

### Exercise A

addi sp, sp, -112

machine code

-112	sp	func3	sp	op
1111-1001-0000	x2	0	x2	19
1111-1001-0000	00010	000	00010	001 0011

112 in hex =  $16 \overline{) 112} \begin{matrix} 0 \\ 7 \\ 0 \end{matrix} = 70$

$0x7fff\_ed80$   
 $- \quad \quad \quad 70$   
 $0x7fff\_ed10$   
 $= \quad 0111 \quad 1111 \quad 1111 \quad 1111 \quad 1110 \quad 1101 \quad 0001 \quad 0000$

### Exercise B

1) b4, b3

2's comp

1)  $\begin{matrix} 1011 & 0100 \\ 1011 & 0011 \\ 0110 & 0111 \end{matrix}$

carryout = 1

2) d0, e0

2)  $\begin{matrix} 1101 & 0000 \\ 1110 & 0000 \\ 1011 & 0000 \end{matrix}$

carryout = 1

3) 78, d5

3)  $\begin{matrix} 0111 & 1000 \\ 0000 & 1011 \\ 1000 & 0011 \end{matrix}$

carryout = 0

4) 35, 2d

4)  $\begin{matrix} 0111 & 1011 \\ 0011 & 0101 \\ 0010 & 1101 \\ 0100 & 0010 \end{matrix}$

carryout = 0

-112 in 2's complement

0000 - 0111 - 0000

1111 - 1000 - 1111

1111 - 1001 - 0000

## As two's complement signed overflow

overflow = yes	no	yes	no
msb is different	msb is the same	msb is different	msb is the same
so sign changes		so sign changes	

## As unsigned overflow

overflow yes	yes	no	no
carryout is 1	carryout is 1	carryout is 0	carryout is 0

## Exercise E

1) 0x20, 0x8f      2) 0xc8, 0x6e      3) 0xac, 0xa9      4) 0x26, 0x27

1) 
$$\begin{array}{r} 0010 - 0000 \text{ a} \\ 1000 - 1111 \text{ b} \\ 0111 - 0000 \text{ b} \\ 1001 - 0001 \\ \hline -111 \end{array}$$

carry out 0

2) 
$$\begin{array}{r} 1100 - 1000 \text{ a} \\ 0110 - 1110 \text{ b} \\ 1001 - 0001 \text{ b} \\ 0101 - 1010 \\ \hline 90 \end{array}$$

1

3) 
$$\begin{array}{r} 1010 - 1100 \text{ a} \\ 1010 - 0101 \text{ b} \\ 0101 - 1010 \text{ b} \\ 0000 - 0111 \\ \hline 7 \end{array}$$

1

4) 
$$\begin{array}{r} 0010 - 0110 \text{ a} \\ 0010 - 0111 \text{ b} \\ 1101 - 1000 \text{ b} \\ 1111 - 1111 \\ \hline -1 \end{array}$$

0

## Signed overflow

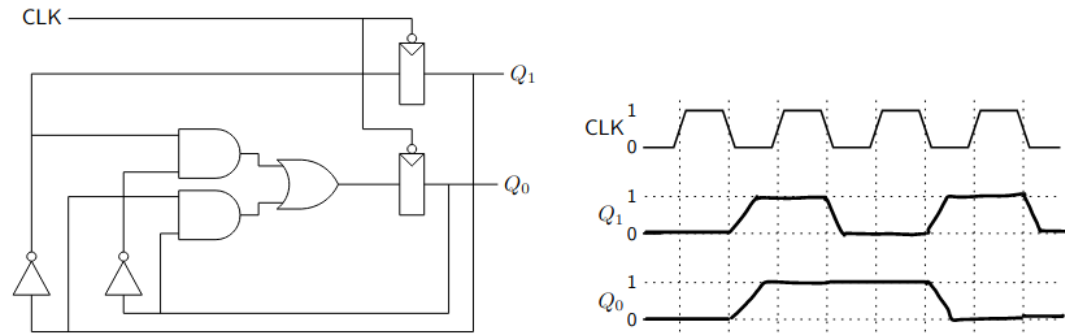
yes	yes	no	no
msb is different	msb is different	msb is between the values	msb is between the values
so sign changes	so sign changes		

## As unsigned overflow

yes	no	no	yes
carryout is 0	carryout is 1	carryout is 1	carryout is 0

# Worksheet for Exercise F

## Part I



## Part II

