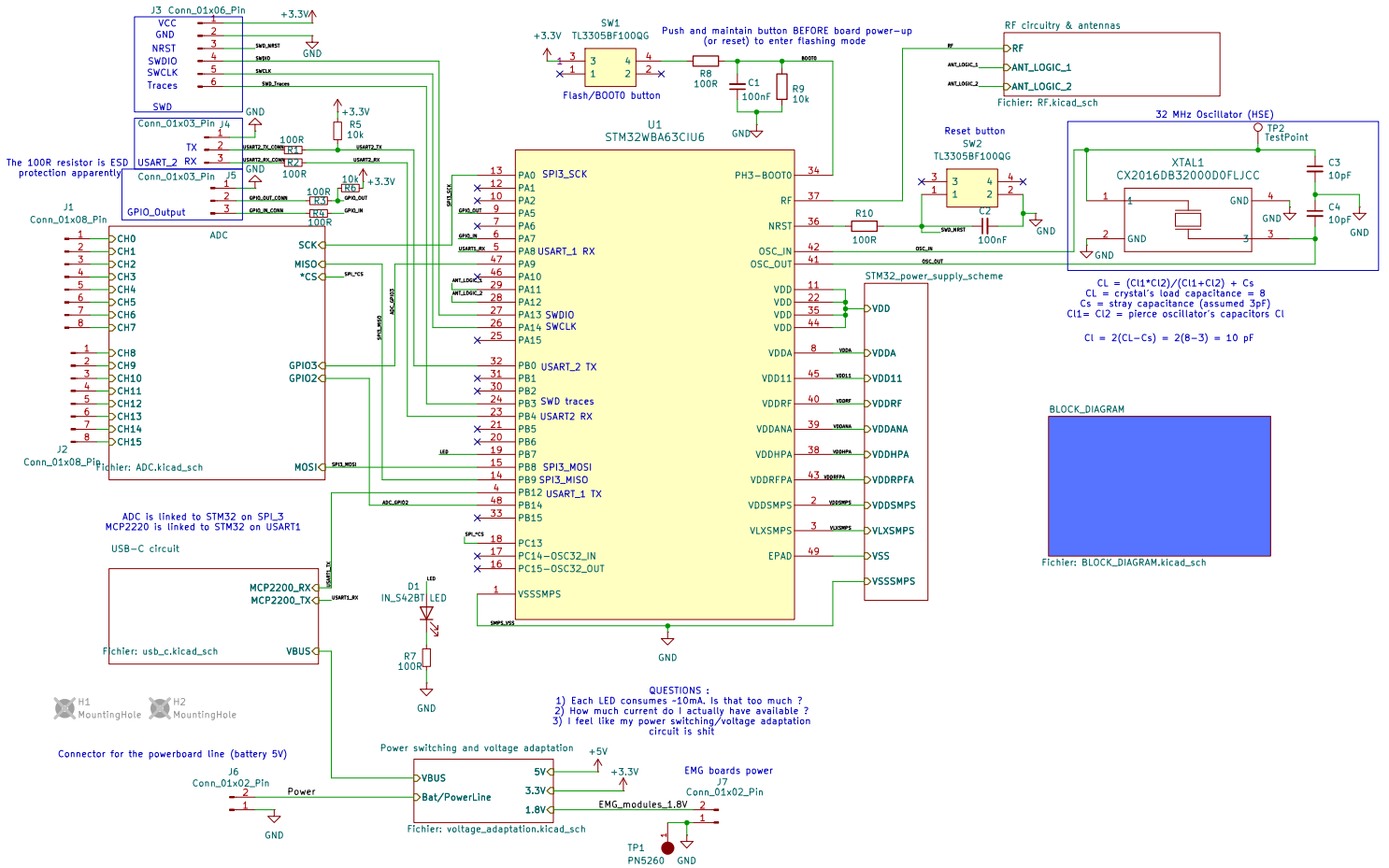
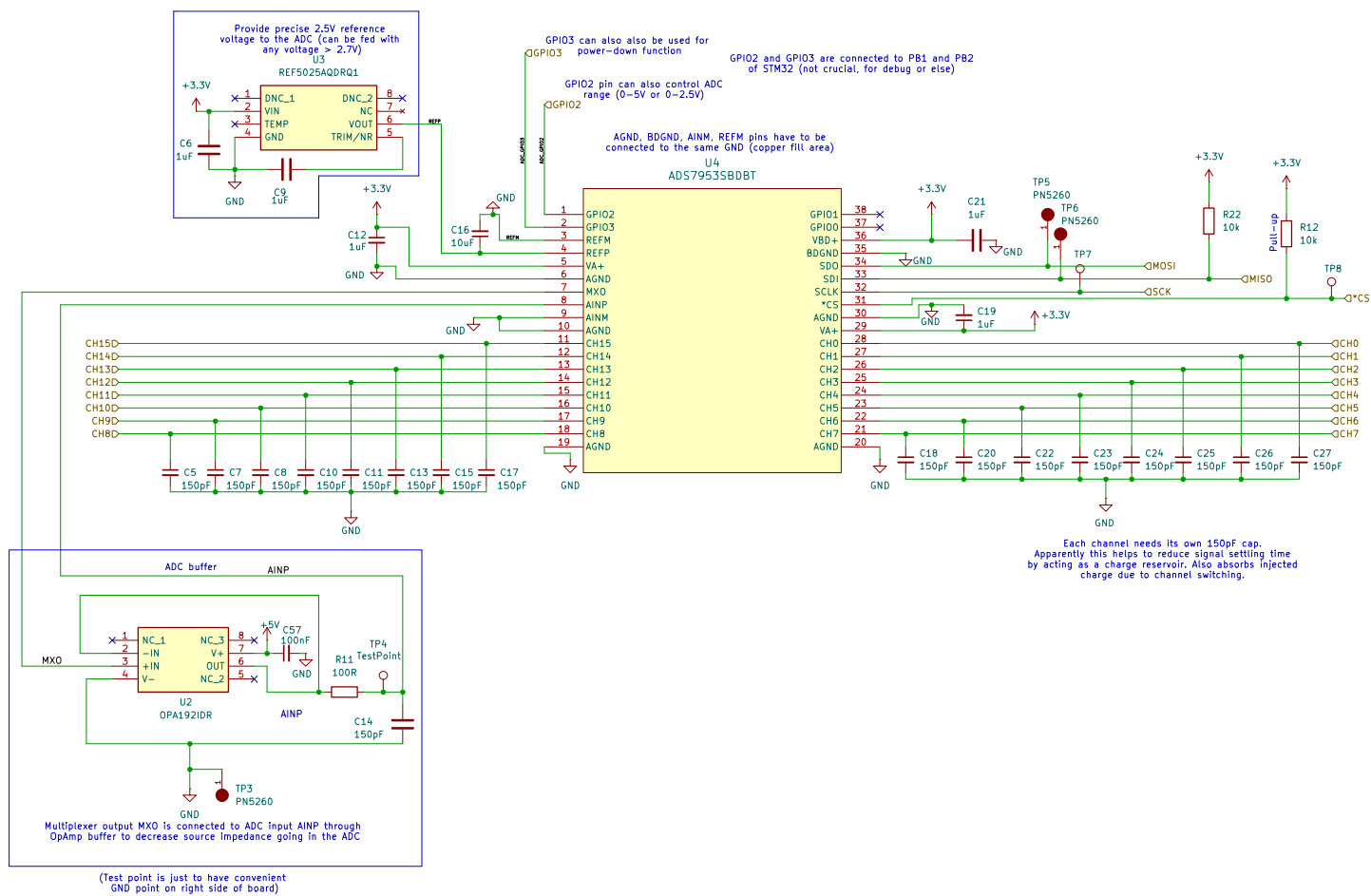


SWD/JTAG is for debug and flashing
(apparently UART can also be used for flashing ?)
IO & CLK are important. Trace is for additional
debug capability or can be GPIO if needed

R12 and C3 in combo with the pulldown resistor
form a low-pass filter for button debouncing



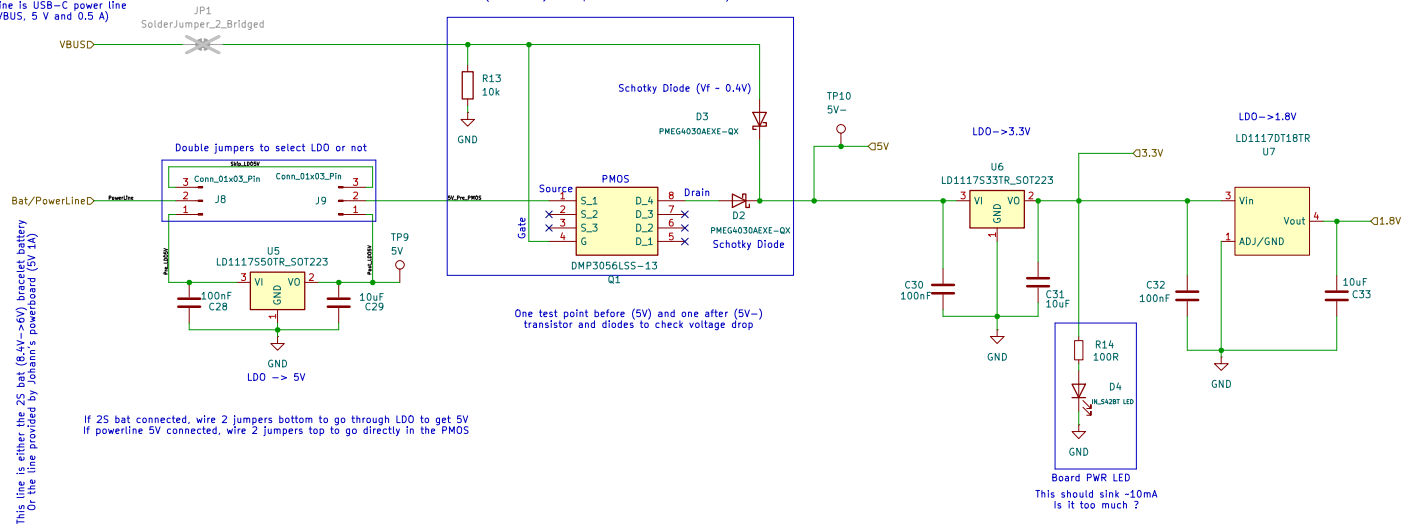


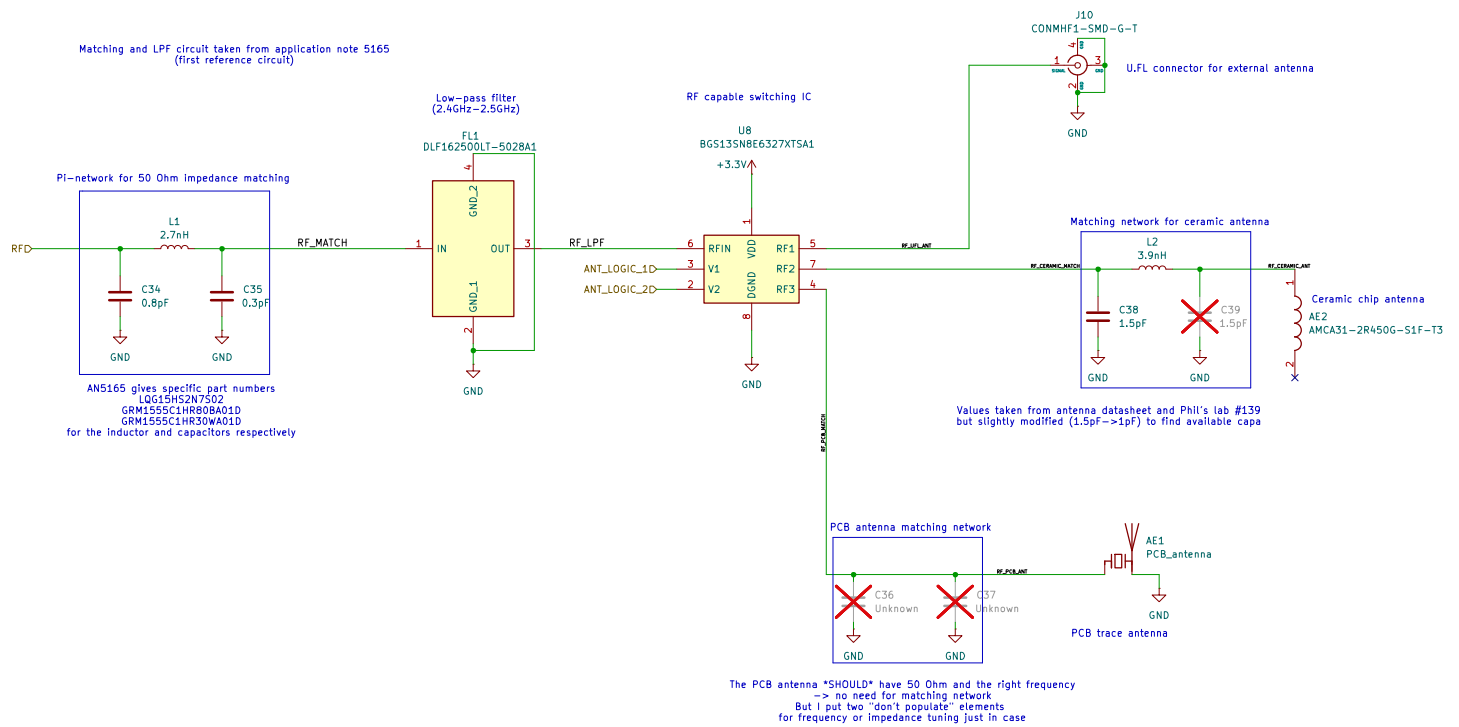
Replace battery cut-off PMOS by power-switching IC with integrated battery recharger

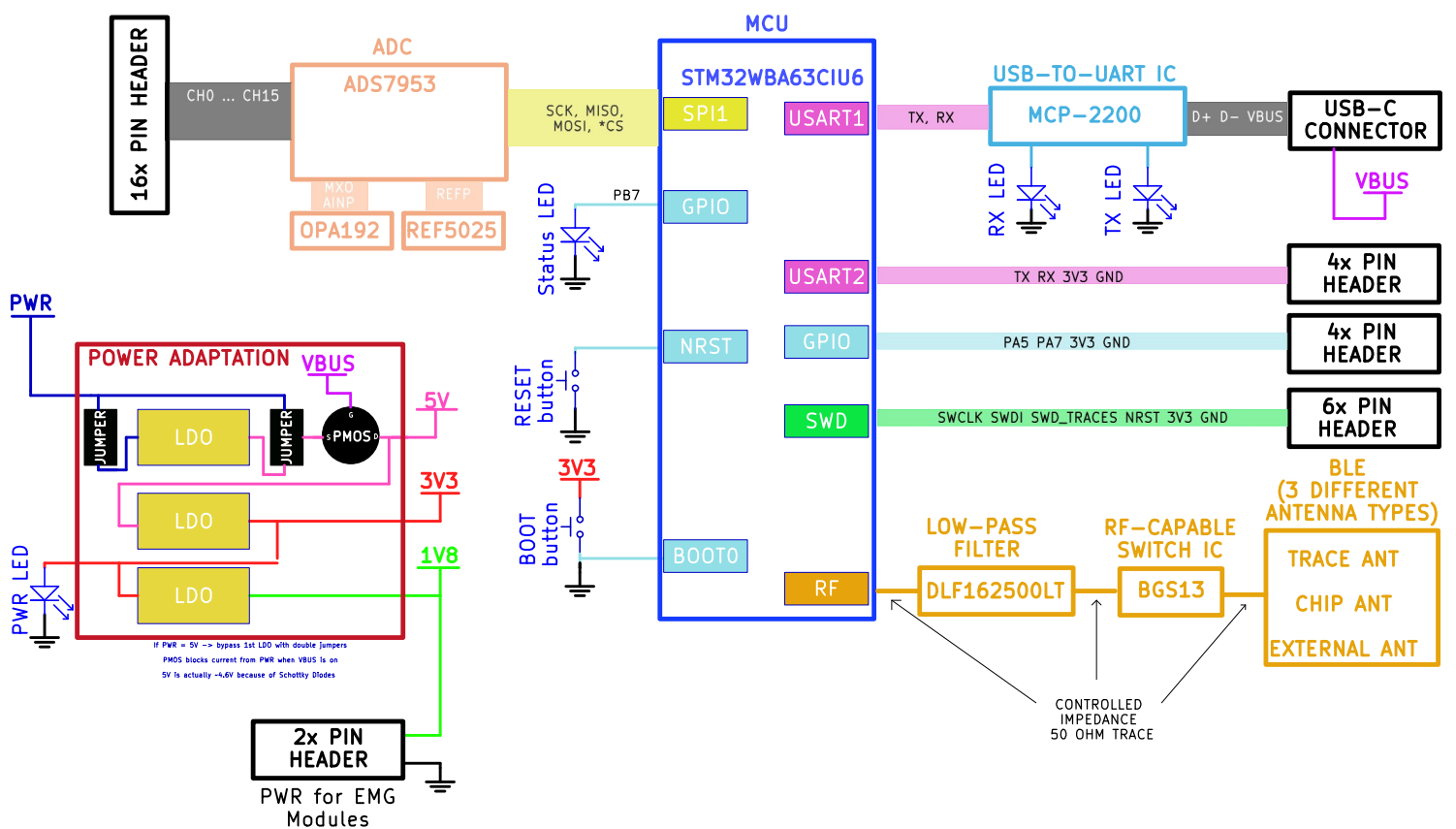
This solderjumper is just in case my circuit doesn't work. After testing : it works
-> remove jumper

This circuit switches from battery to USB-C power supply if USB-C cable is connected (diode are just to prevent backflow of current)

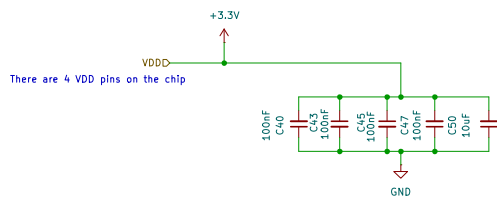
This line is USB-C power line (VBUS, 5 V and 0.5 A)







Review:
Actually USART3 does not exist ! So the 2nd 4x connector is only for GPIOs I guess



This is based on the Power supply scheme with SMPS p.98 of datasheet
 NB : pins VDDUSB, VREF+, VDDIO2 VSSA and VSSRY don't exist on my package,
 so I had to connect them alternatively as proposed in the scheme.
 Also I used p.29 to determine the acceptable voltage of VDD (3.3V) and other details

