







M2

M1

Updates:

- * Design with manufacturing tolerance 0.175 mm
- * TVSOP footprint to accomodate the tolerance
- * Fixed wrong placement of pmod connectors wrt board edge
- * Fixed LED swap
- * Used 0805 passives
- * Added Mounting holes
- * Fixed pmod wrong numbering
- * change chip to UL_{SN74CBTLV16292GR} (TVSSOP => TSSOP)
- * 4 layer board

TODO :

- * mux, capas on the back?
- * Surface mount headers ?
- * Level shifter / Input protection ?
- * 1.8v (other ?) Voltage reference ? as Vtrgt













