

- 1) How many total bits are required for a direct mapped cache with 16 Kb of data with 4 word blocks assuming 32 bit address?

cache size = 16 Kb = $2^4 \times 2^{10} = 2^{14}$ bytes
each line = 4 words

$$\text{no of lines} = \frac{2^{14}}{2^2}$$

$$= \underline{\underline{2^{12} \text{ lines}}}$$

tag	block	word
18	12	2
← 32 →		

$$\text{Cache size} = 2^{14} \text{ bytes} = 2^{14} \times 2^3 = 2^{17} \text{ bits}$$

$$\text{Size of tag memory} = \text{tag bits} \times \text{No of lines} = 18 \times 2^{12} = 72 \text{ Kbits}$$

$$\text{total mem. needed for cache} = 200 \text{ Kbits}$$

- 2) Consider a cache with 64 blocks & a block size of 16 bytes what block number does the byte addr 1200 map? Assume all are decimal no.

$$\text{no of blocks} = 64$$

$$\text{block size} = 16 \text{ bytes}$$

$$\text{Cache size} = \text{no of blocks} \times \text{block size}$$

$$x = 64$$

$$x = 2^4 \times 2^6 = 2^{10}$$

$$\boxed{x = 2^{10}} \quad \underline{\underline{1 \text{ Kb}}}$$

$$\boxed{\text{Cache size} = 1 \text{ Kb}}$$

$$\frac{1200}{16} = \frac{\text{byte addr}}{\text{block size}}$$

$$= 75$$

find cache block no

related to mem. block no

$$\text{Cache block no} = 75 \% 64$$

$$= \underline{\underline{11}}$$