

# Dardel and Arrhenius Intensive Course: Advanced Workshop on CPU partition

## Day 1: Tuesday 27/01 (times in CET)

Start	Duration	Item
09:00	10	<b>Welcome, introduction to the course.</b> <i>Course organization.</i>
09:10	60	<b>Dardel and Arrhenius Architecture (CPU part) programming and execution environment</b> <i>Recap of the Dardel and Arrhenius architecture, programming, and execution environment.</i>
10:10	60	<b>Overview of compilers and Parallel Programming Models</b> <i>An introduction to the compiler suites available. Special attention is given the Cray Compilation Environment (CCE) noting options relevant to porting and performance. Description of the Parallel Programming models.</i>
11:10	20	<b>Break</b>
11:30	60	<b>Compilers, libraries, runtime, Performance Optimization: Improving single-core efficiency</b> <i>Compiler feedback</i> <i>Presentation of the Cray Scientific Libraries for CPU</i>
12:30	60	<b>Lunch break</b>
13:30	60	<b>MPI Topics on the HPE Cray EX supercomputer</b>
14:30	60	<b>Advanced mapping of parallel application to the CPU partition.</b>
15:30	20	<b>Break</b>
15:50	70	<b>Exercises (session #1)</b>
17:00	30	<b>Open Questions &amp; Answers</b> Participants are encouraged to continue with exercises in case there should be no questions.
17:30		<b>End of the course day</b>

## Day 2: Wednesday 28/01 (times in CET)

Start	Duration	Item
9:00	30	<b>Python on HPE Cray EX Supercomputer</b>
9:30	50	<b>Introduction to Perftools - Perftools-lite modules</b> <i>Overview of the Cray Performance and Analysis toolkit for profiling applications.</i>
10:20	20	<b>Break</b>
10:40	50	<b>Advanced performance analysis</b> <i>Sampling, tracing, loop work estimates with Perftools. APA, Communication Imbalance, Hardware Counters, Perftools API, OpenMP. Reveal</i>
11:30	60	<b>Exercises (session #2)</b>
12:30	60	<b>Lunch break</b>
13:30	50	<b>Debugging at Scale</b> <i>gd4hpc, valgrind4hpc, sanitizer4hpc, ATP, STAT.</i>
14:20	40	<b>Optimizing Large Scale I/O</b> <i>Tips for optimising parallel bandwidth for a variety of parallel I/O schemes. Introduction to the Lustre filesystem, controls and being nice to Lustre.</i>
15:00	20	<b>Break</b>
15:20	80	<b>Exercises (session #3)</b>
16:40	50	<b>Final Open Questions &amp; Answers</b> Participants are encouraged to continue with exercises in case there should be no questions.
17:30		<b>End of the course day</b>