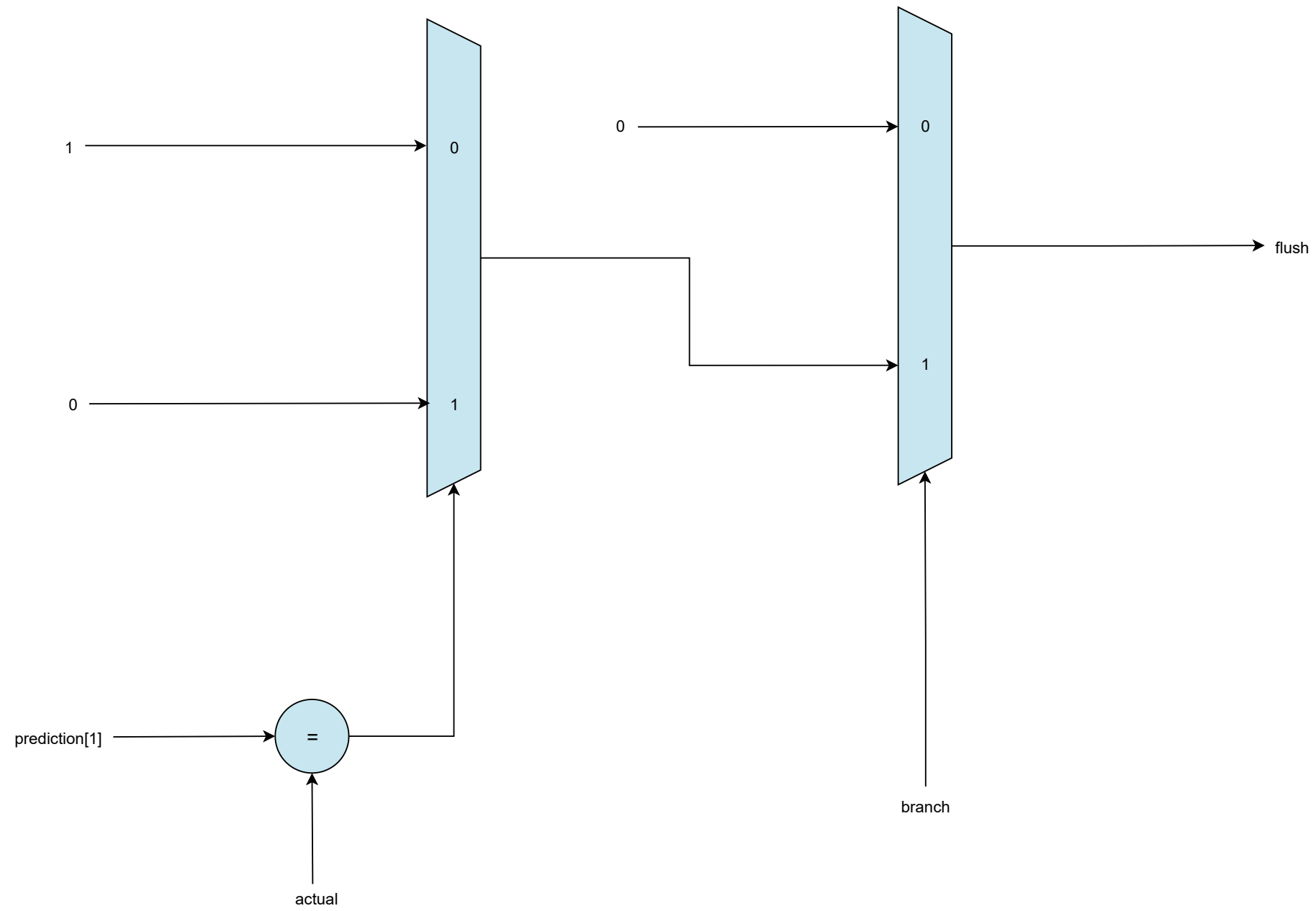
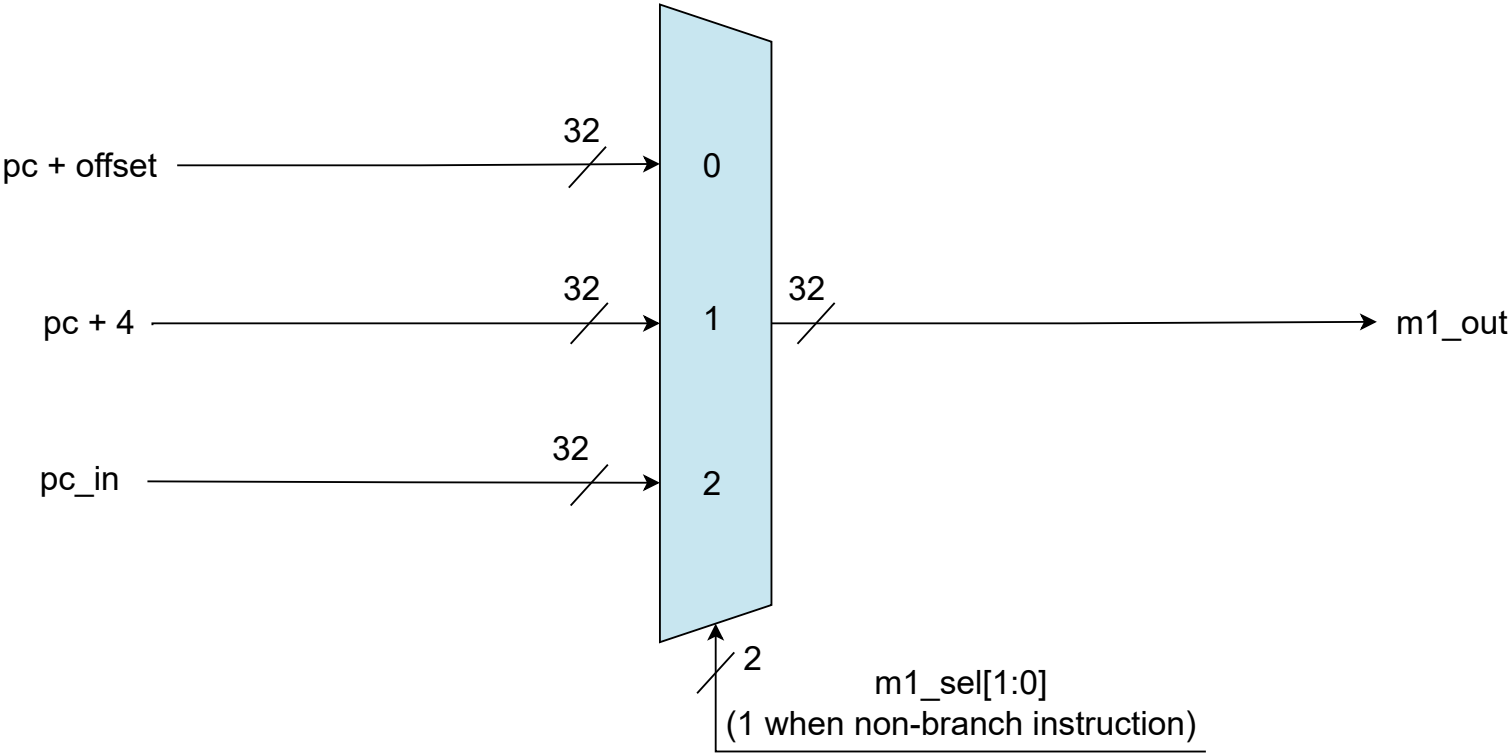


(write signal to BHT)

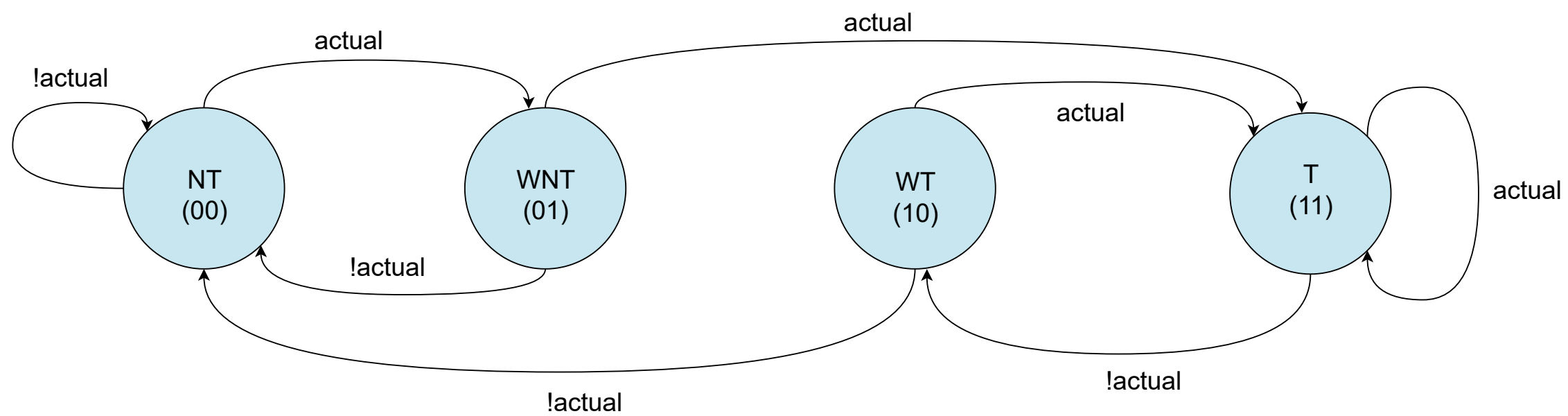


(flush signal to pipeline)

m1_sel				
actual	predicted	fetch	decode&execute	memory&writeback
✓	✓	2	1	1
✗	✓	2	flush	1
✗	✗	2	1	1
✓	✗	2	flush	0
		read	branch	instq1[6:0] == 7'b1100011



(selection from mux to PC from PIPELINE)



(updated prediction to BHT)