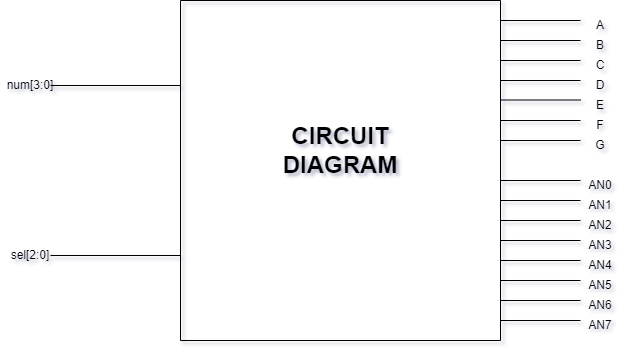
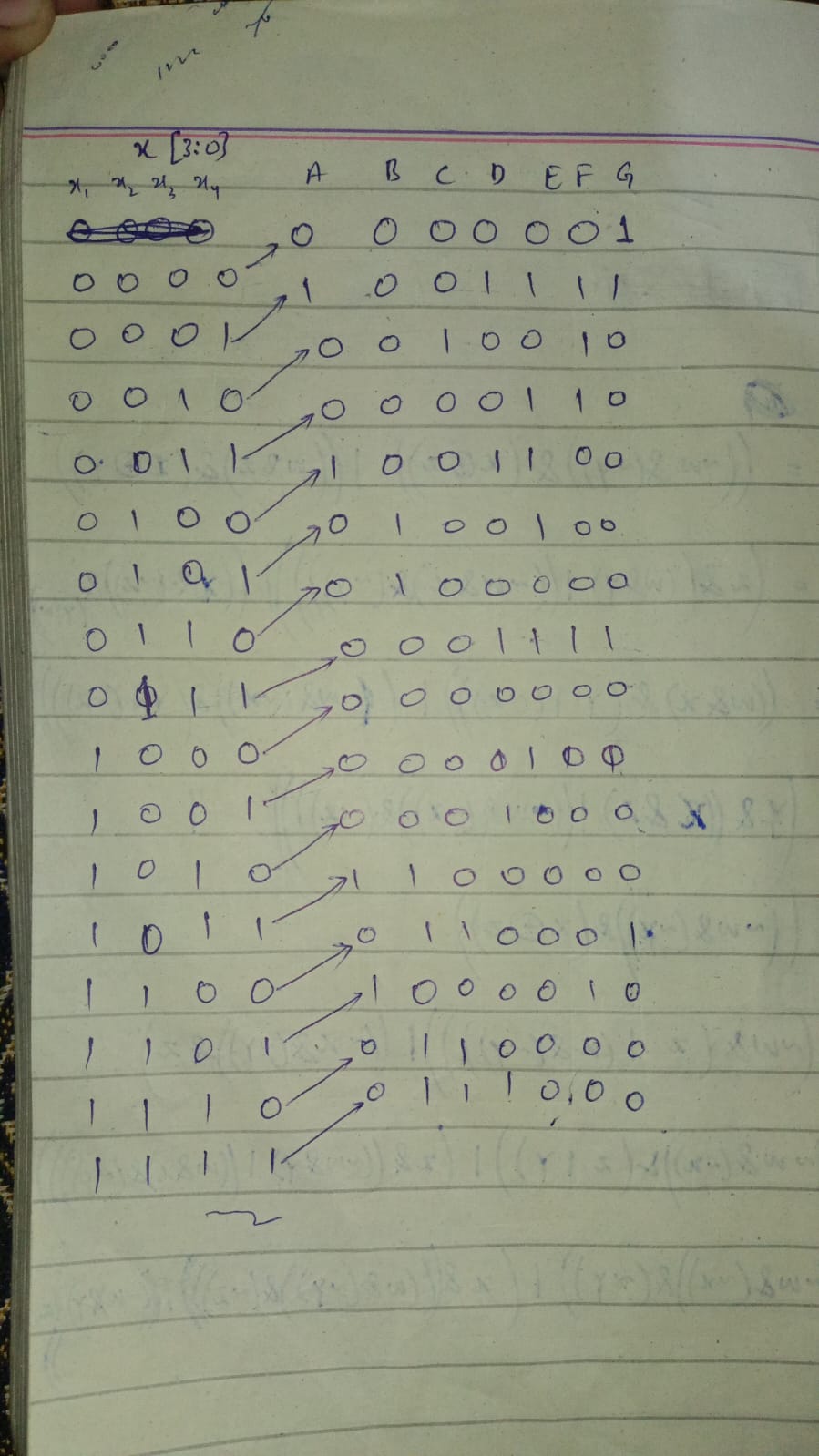
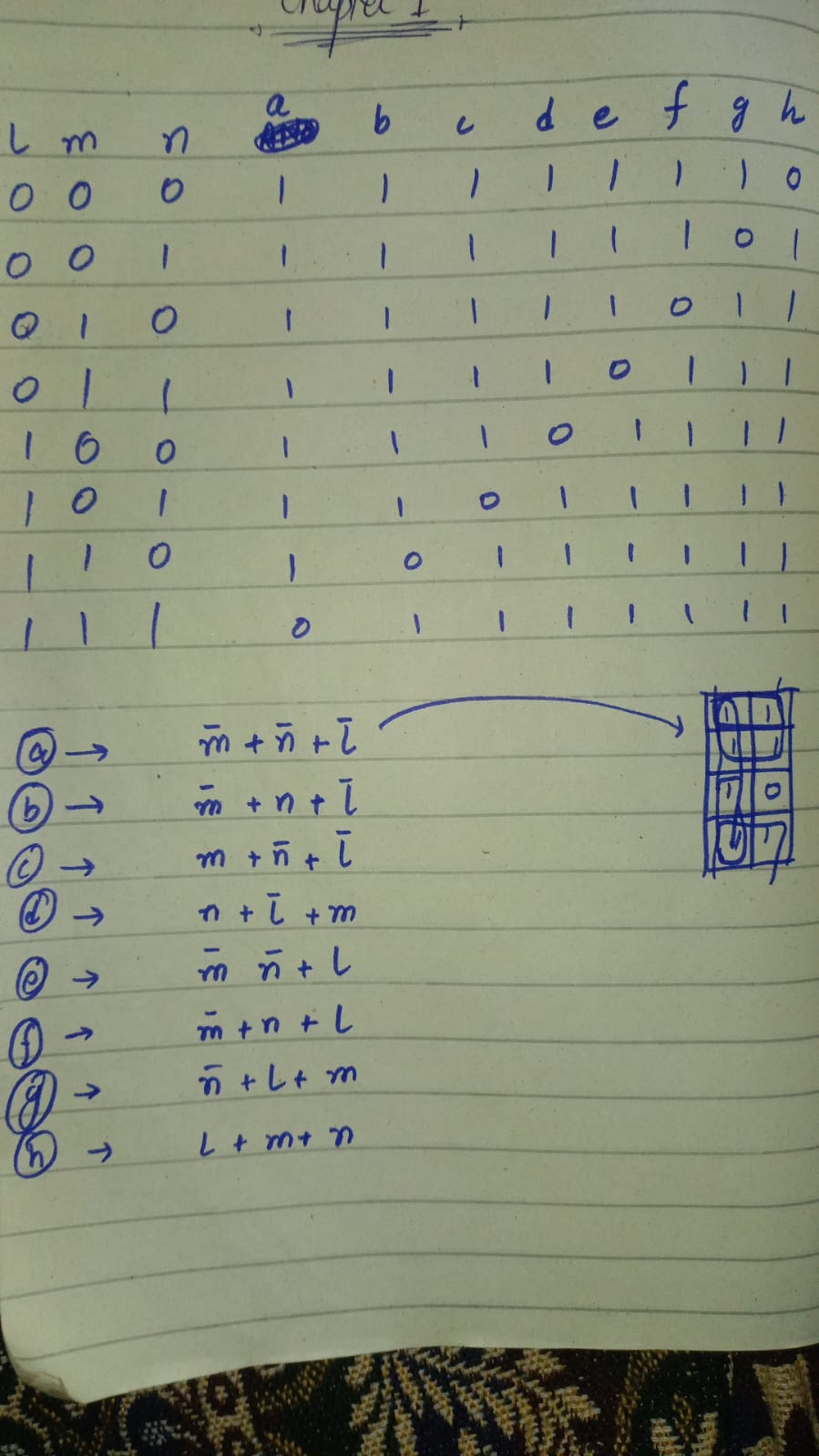
LAB # 5(Report)

Circuit I/O:

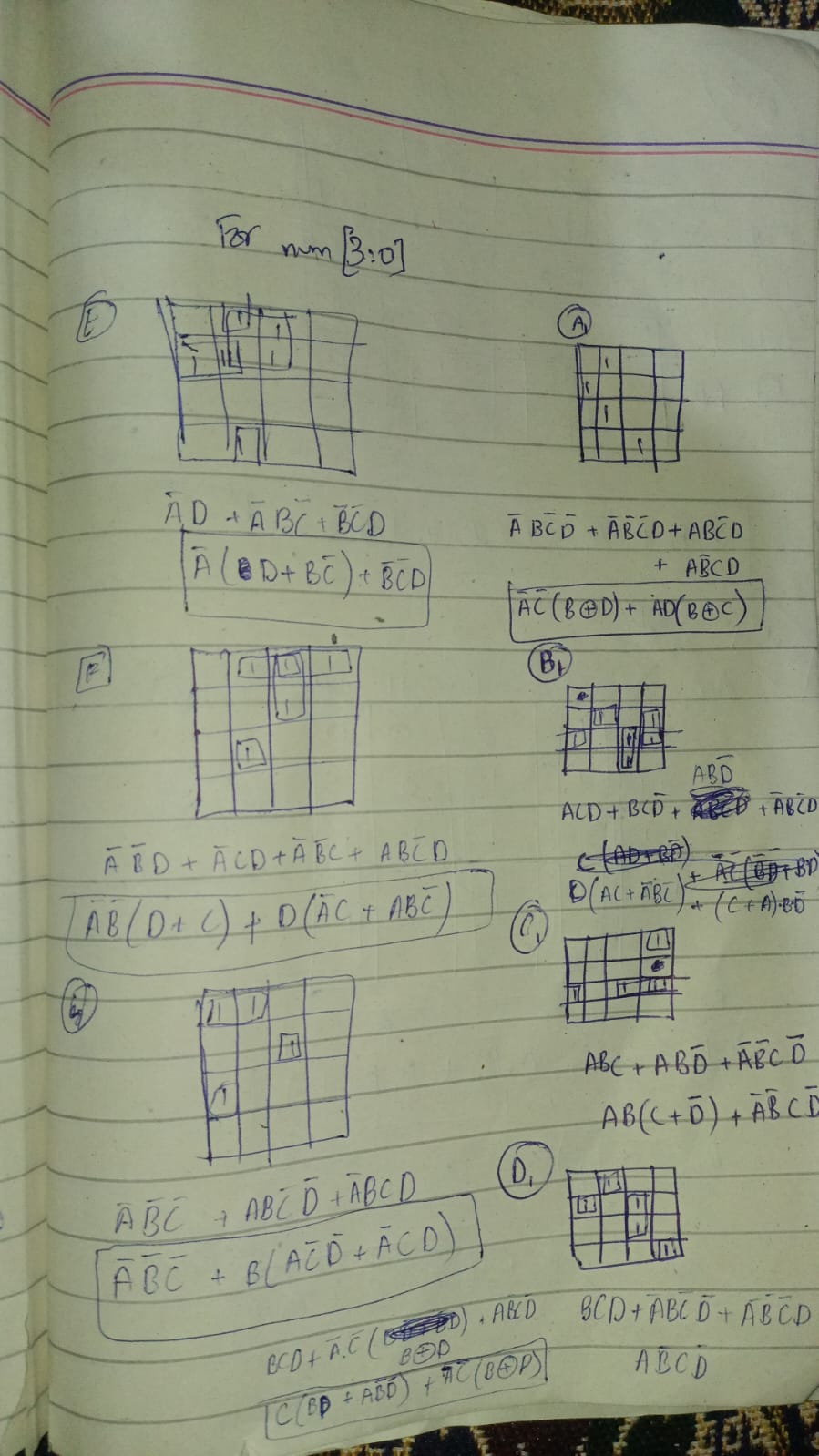


Truth table:

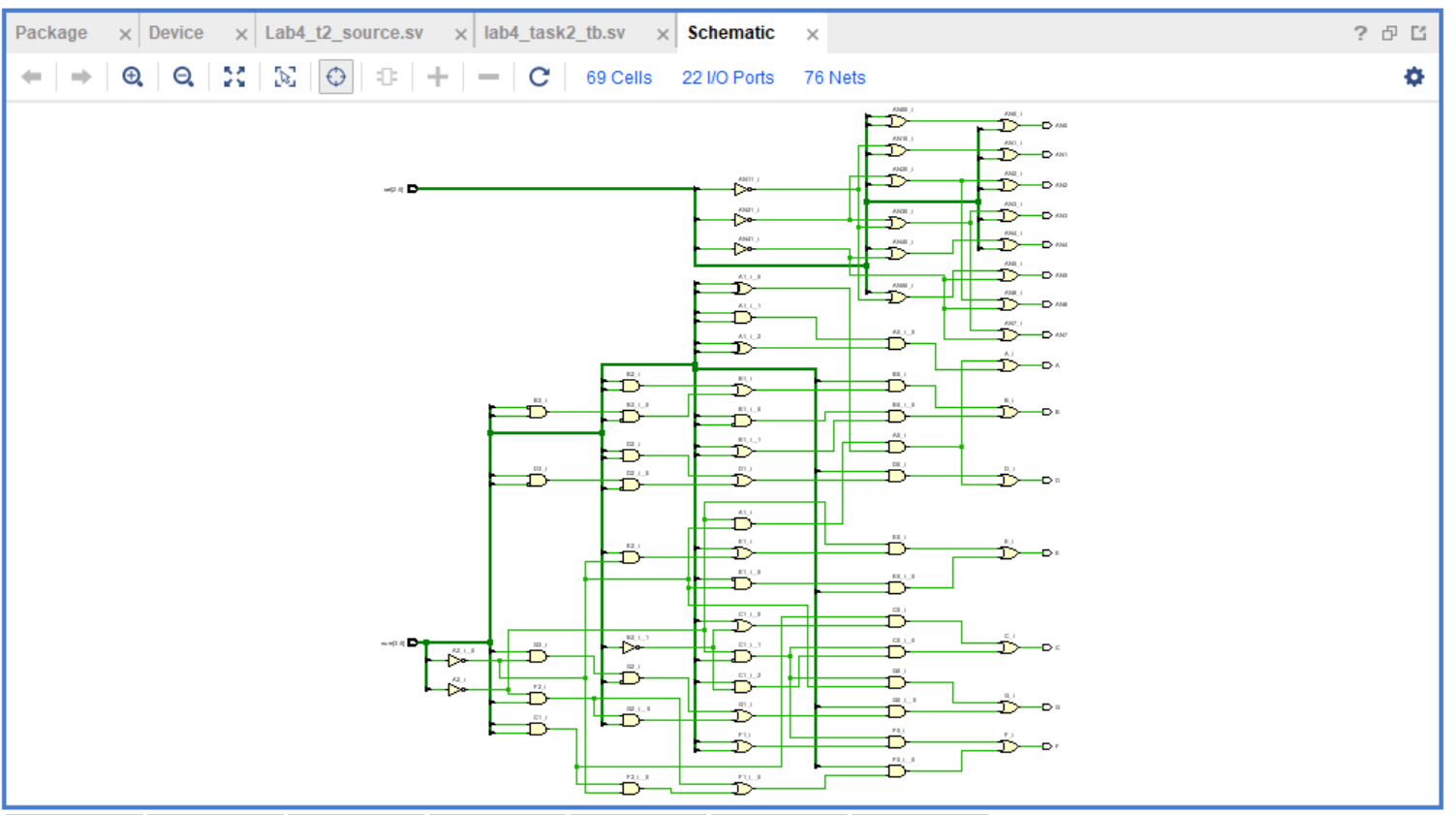




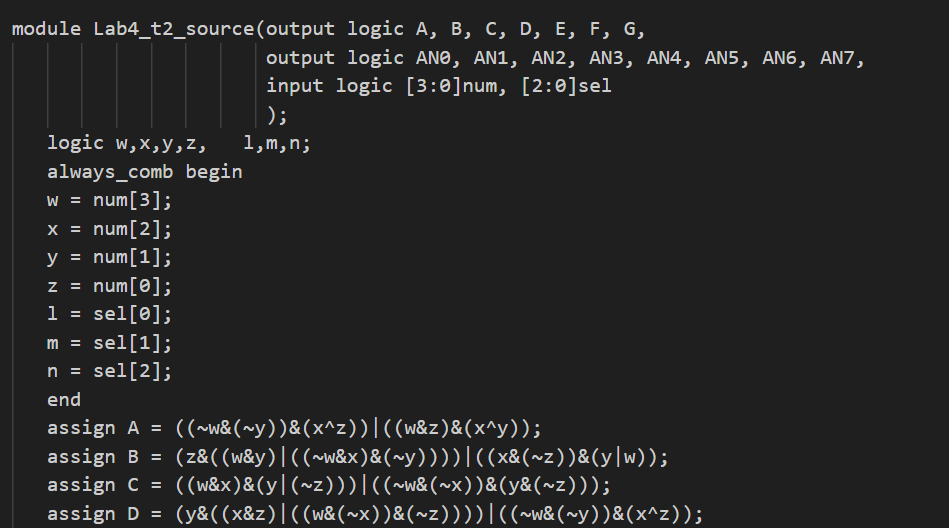
K-maps:

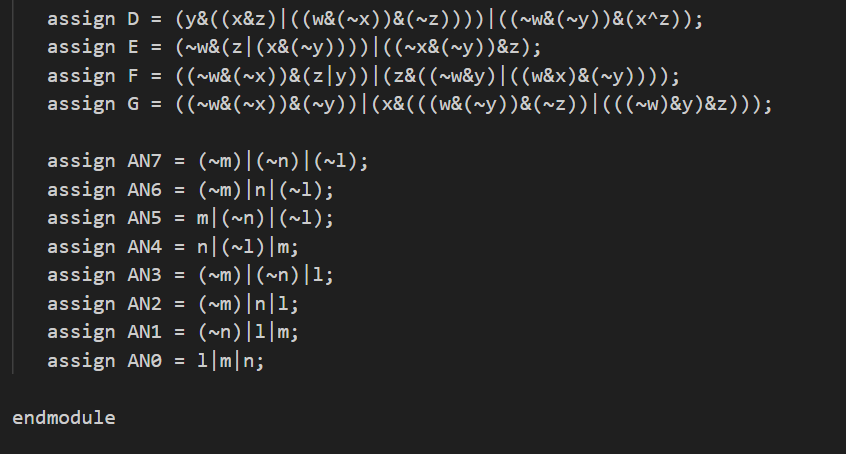


Circuit diagram:

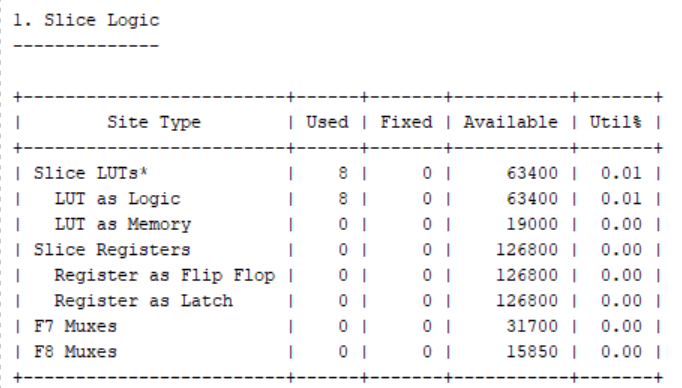


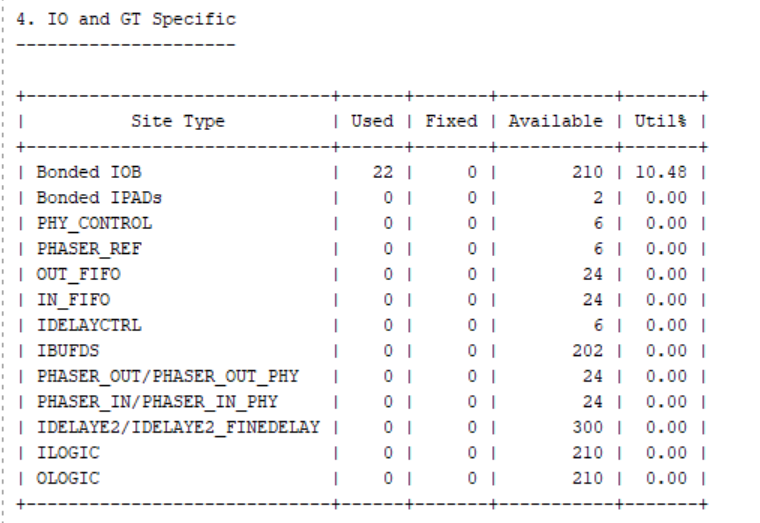
Code:

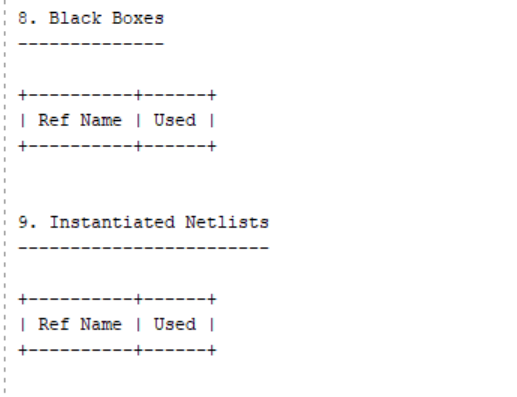


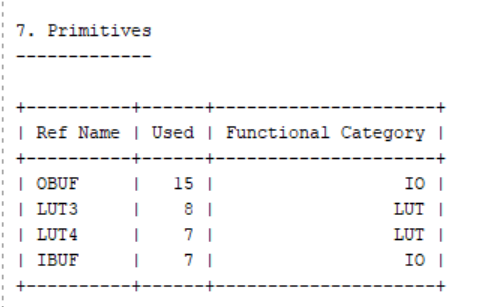


Resources utilization:









So, these are the resources that have been used.

Simulation results:

