4	reset	0														
4	• dk	1	$\overline{}$								\uparrow L					\neg
4	start	1														
⊕ 💠	→ addr	355	50865		354		179	χ:	3	355			(50	866	355	
4	state state	readSetup	writeData		read	Setup	readCen	ter (r	readAbove	(readi	Below	calculateFi	ter (wr	iteData	read	Setup
- ⊟-	pix_reg	{40'h71427C9D78	{40'h7C9	D78343	9} {	40'h5D918	. (40'h7C	D7C9 ((40'h7C9D7C	({40'h	7C9D7C9	{40'h7142	7C9D78} {	10'h6A415D91	8 <mark>8</mark> } {40	h562
) - (2)	71427C9D78	7C9D783	439			7C9D7C	D78				71427C9D	78			
) - (1)	6A415D9188	5D91886	274			5D915D9	188	5A415D9188							
+) - (0)	562D23555C	23555C5	26C			2355235	55C		562D	23555C					
⊕ 📣	→ dataR	16'h7142		=	16'h	7C9D		16'h6A41	16	1562D	16'h714	2	\longrightarrow		16'h	7142
⊕ 💠	dataW	16'h4632	16'h2515										(16	h <u>4632</u>		
4	firstRow	0														
4	lastRow	0														
4	→ firstColumnR	0														
4	→ firstColumnW	0														
4	▶ lastColumnW	0														
ax 🗐 🔾	Now	91000 ns				liiiiiiiil 0 ns	8660			1		1		iliiiiiiiiii 900 ns		11111111 37000 ns
<u></u>	Start cycle	86480 ns		8648	0 ns											
<u></u>	done cycle	86960 ns												86	960 ns	