HMC Redesign Debug Notes:

1. Removal of U6 allows Pentek to be discovered by host.
2. Pin 1 (OE) of U6 lifted and tied high allows Pentek to be discovered by host.
3. Lifted associated outputs of U6 with OE; pins 18, 16, 14, 12. Pentek could not be discovered???
4. Lifted pin 9, Pentek is discovered!
5. Attached pin 18; got Pentek segmentation fault and unrecognized interrupt vector (255)?
6. Attached remainder of output pins; lifted both output enables (pins 1 & 19) and tied them high. Pentek is discovered.
7. After 6. Tied enables low; Pentek still discovered!
8. Attached 3K pull-up resistor to both U6 OE’s and also attach to unused FPGA I/O (pin 49). Pentek was discovered by the host.