CPE 353 – Team Software Process

Grooming

Product Backlog

Sprint Planning

Definition of Ready

Definition of Ready is validated against each story to be committed within the sprint.

Acceptance Tests

SPRINT

Review

Implementation

Retrospect

Planning

Definition of Done

APPROVED

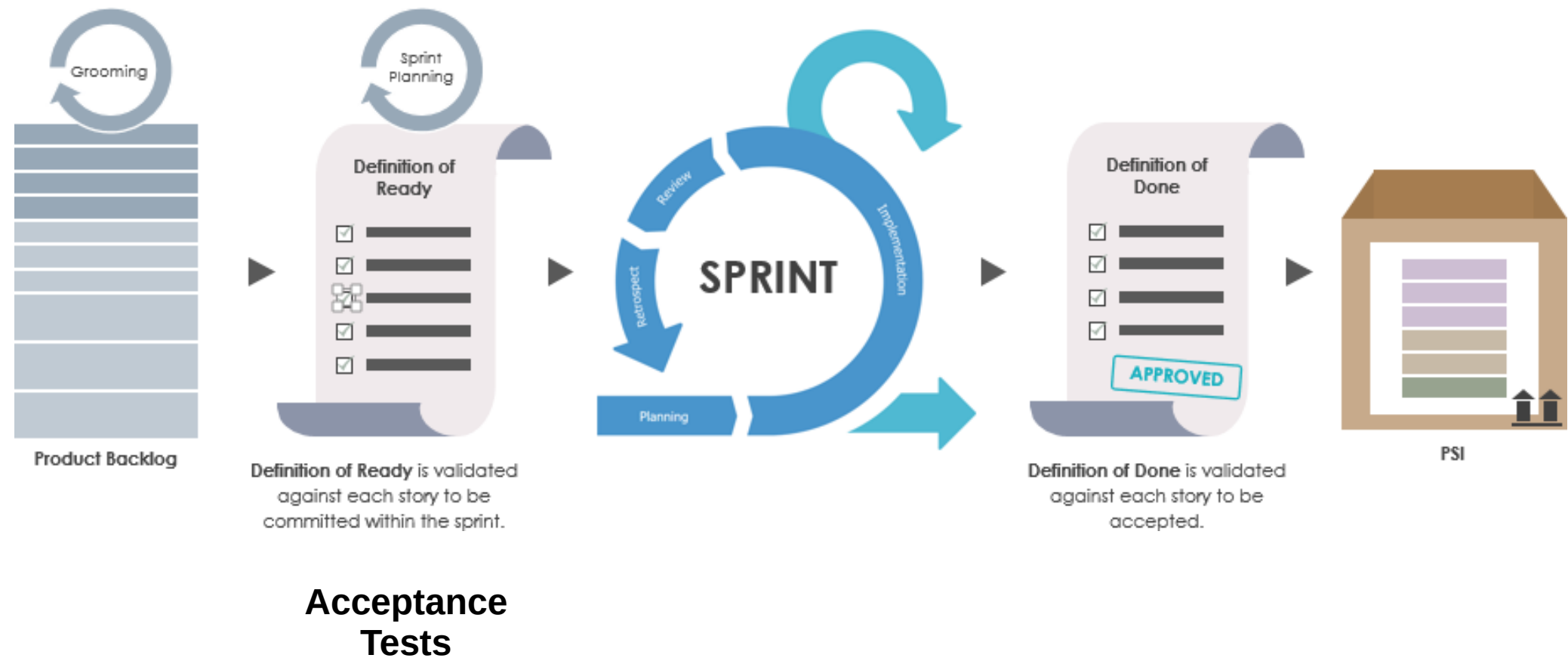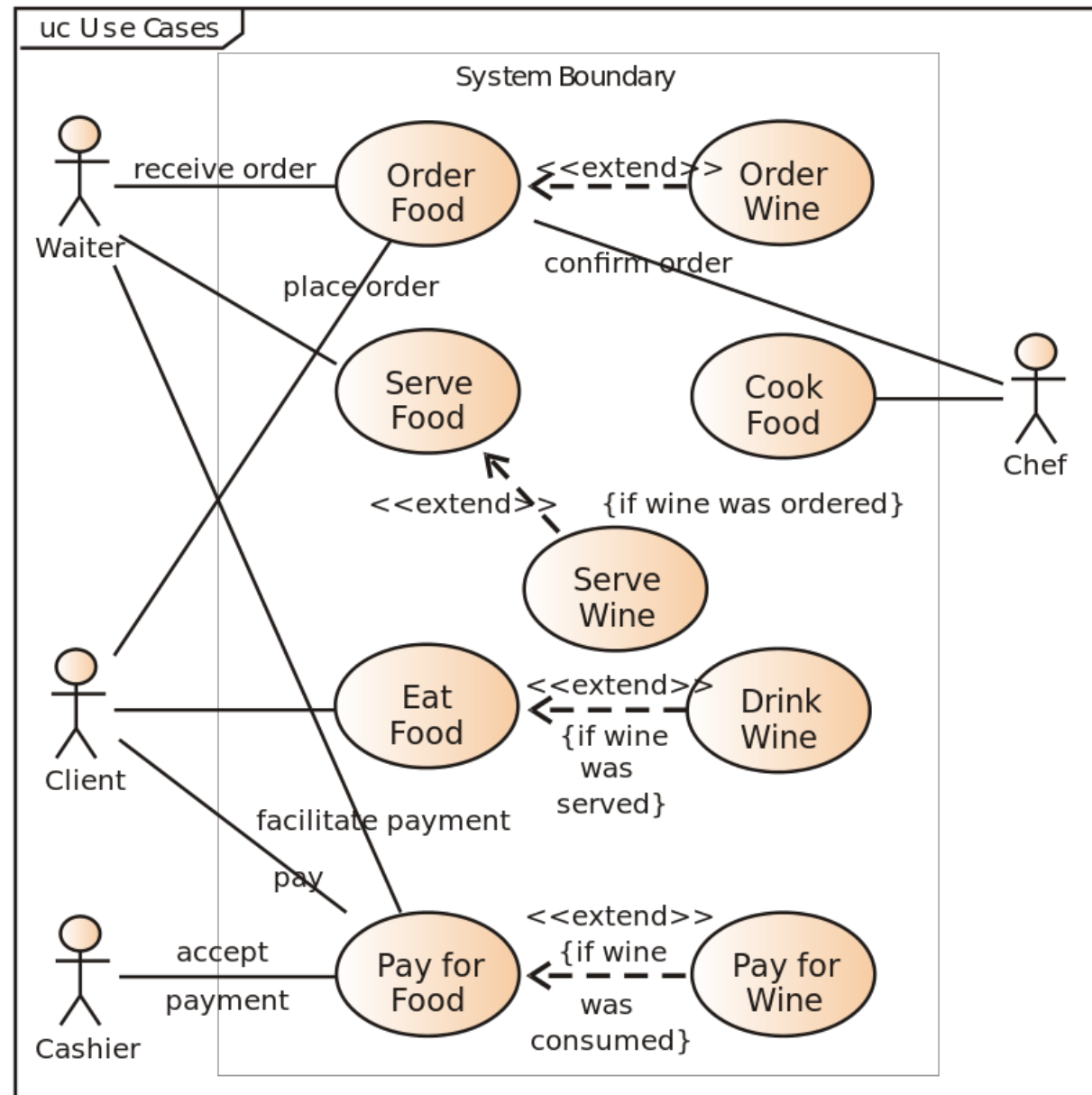**Definition of Done** is validated against each story to be accepted.

PSI

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**Sample
Use
Case
Diagram**

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Sample Use Case

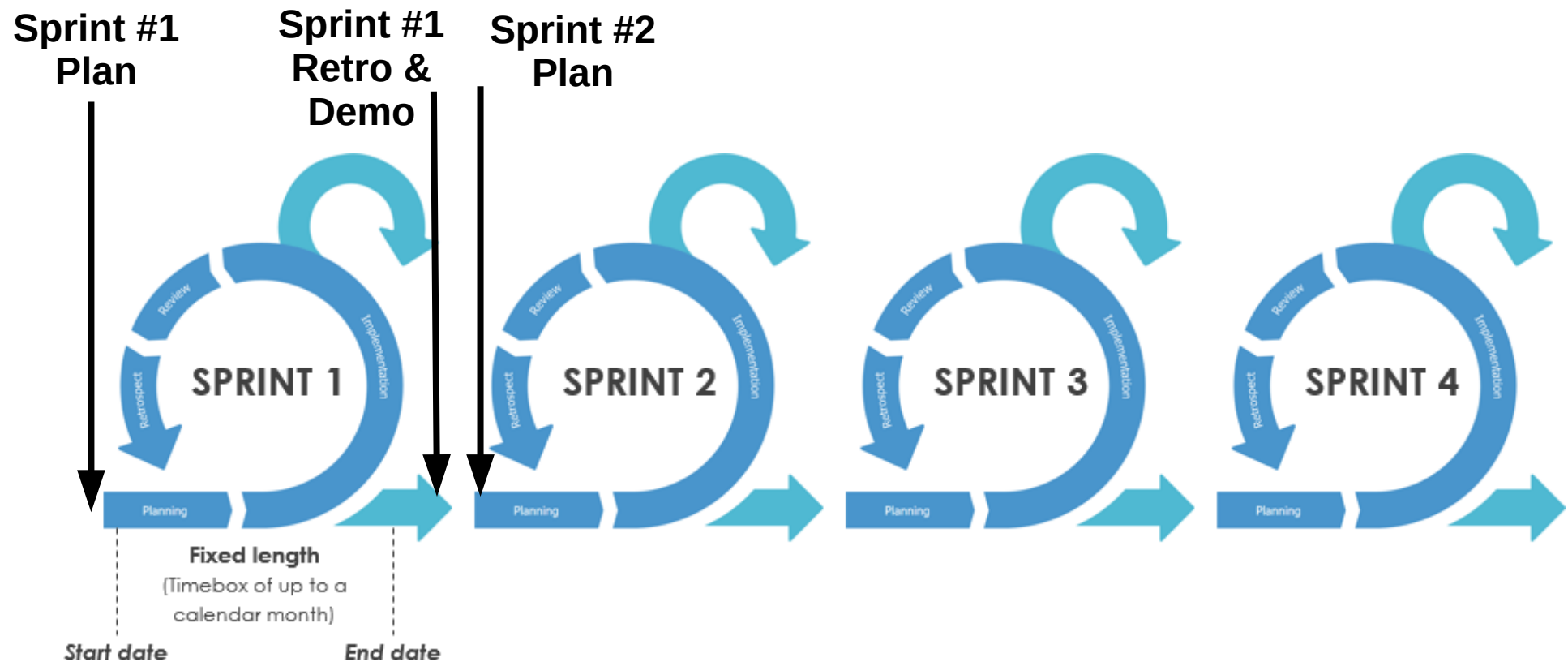| Use Case	Draw Image
Pre-condition	• Frame buffer must be cleared • Reset must switch off
Post-condition	• Image data output to VRAM
Basic path	1. This use case starts while Host CPU has already prepared the display list for drawing image. 2. Host CPU writes display list system's FIFO. 3. 2D/3D Graphics reads display list from FIFO and starts drawing image according to commands of display list. 4. 2D/3D Graphics outputs image data to VRAM.
Alternative path	At step 2 of the basic path, Host CPU writes display list that includes SYNC command to system's FIFO. At step 3 of the basic path, 2D/3D Graphics stops after reading SYNC command, and then 2D/3D graphics restarts drawing image after receiving a blank pulse from LCDC.
Exceptional path	At step 2 of the basic path, Host CPU writes display list includes undefined commands. At step 3 of basic path, 2D/3D graphics raises an error interrupt to Host CPU and clears system's FIFO.

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Sample Use Case

Two Column

Usecase Description	
System : ConveyorSystem	**Name : Defects extration**
Scenario : Identifies defective products using 2 photo sensors. If defect product is identified, controller actuators extraction cylinder for the removal of product.	
Pre-condition : identification of defective products	
Post-condition : extraction of defective products	
Typical course or Events	
Actor	System
1. Product arrives 1.1 high level sensor is OFF and low level sensor is ON 1.2 high&low level sensors are all ON 1.3 high&low level sensor are all OFF 3. Extraction point sensor senses product 5. Extract defective product by forward stroke 6. Proximity switch senses good product	2. Identifies state of product 2.1 identifies good product 2.2/2.3 identifies defective product 4. Controller control cylinder according to the state of product 4.1 in case of defective products, sends forward stroke signal 4.2 initialize product status memory 7. Initialize product status memory

CPE 353 Software Process

Sprint #1 Plan

Sprint #1 Retro & Demo

Sprint #2 Plan

SPRINT 1 — Review, Implementation, Retrospect, Planning

Fixed length
(Timebox of up to a calendar month)

Start date   End date

SPRINT 2 — Review, Implementation, Retrospect, Planning

SPRINT 3 — Review, Implementation, Retrospect, Planning

SPRINT 4 — Review, Implementation, Retrospect, Planning

Four Sprints
Design, Implementation, Testing

CPE 353 Project Timeline

- Preparation: 10/19/23 - 10/26/23
- Sprint #1:   10/26/23 - 11/02/23
- Sprint #2:   11/02/23 – 11/09/23
- Sprint #3:   11/09/23 – 11/16/23
- Sprint #4:   11/16/23 - 11/30/23
- Finals Week: Final Presentations