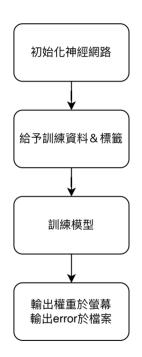
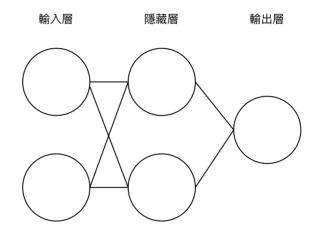
system architecture





每層皆為全聯接層