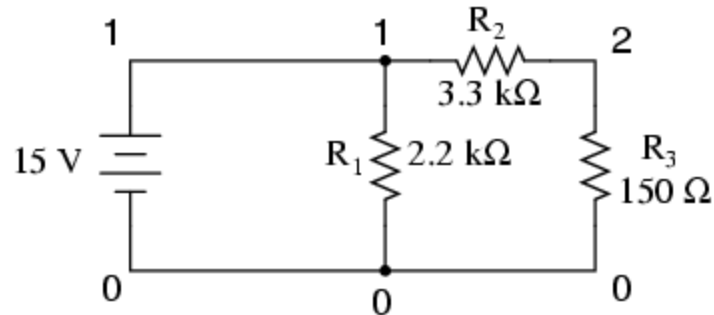
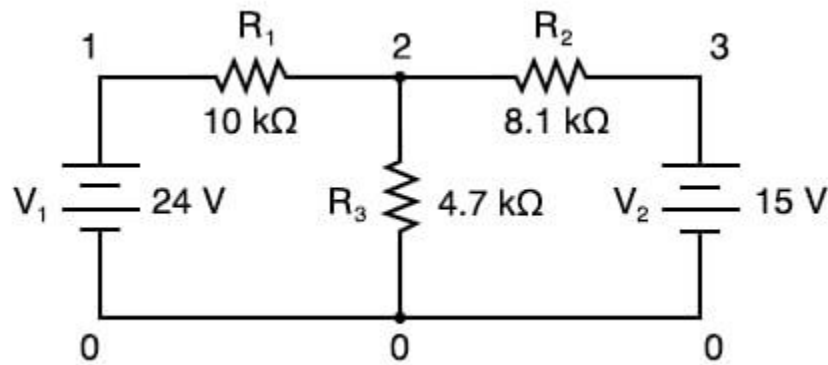


1. Write SPICE netlist and DC analysis and display voltages for nodes 1, 2, and 3 (with reference to node 0,

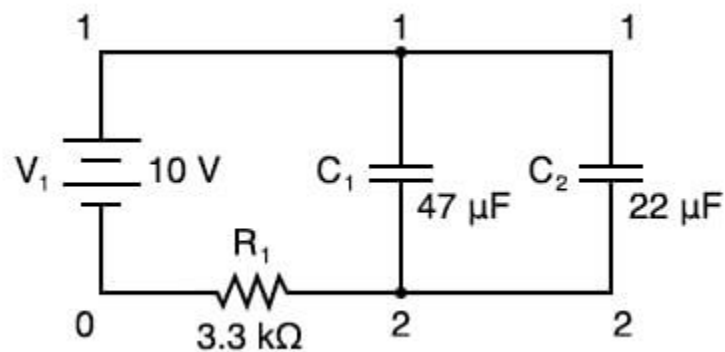


2. Write SPICE netlist and DC analysis and print all nodal voltages and current



3. Write SPICE netlist for the following RC time constant circuit , print voltage from 1 to 2 .

Hint : For DC analysis, the initial conditions of any reactive component (C or L) must be specified (voltage for capacitors, current for inductors). This is provided by the last data field of each capacitor card (ic=0). `.uic` telling it to “use initial conditions”



4. Write SPICE netlist and DC analysis and verify KCL and KVL on following circuit

