

# APPENDIX A

## Assembly Syntax Translation

### Objective

Appendix A provides some general rules and a table for translating code between Assemblers and syntaxes. Examples are provided for x86 and x86\_64.

### Rules

- GAS prefixes registers with %.
- GAS prefixes immediate values with \$.
- GAS also uses the \$ prefix to indicate an address of a variable.
- MASM and NASM use \$ as the *current location counter*, while GAS uses the dot (.).
- GAS operands are source first, destination second.
- MASM and NASM operands are destination first, source second.
- GAS denotes operand sizes with *b*, *w*, *l*, and *q* suffixes on the instruction.
- GAS and NASM identifiers are case-sensitive.
- MASM identifiers are not case-sensitive by default, but can be by adding option `casemap:none` (usually after the `.MODEL` directive in 32-bit programs).
- NASM writes FPU stack registers as *ST0*, *ST1*, etc., without parentheses.
- GAS/MASM usually write FPU stack registers as *%st(1)*/*ST(1)*, *%st(2)*/*ST(2)*, etc., with parentheses.
- GAS uses `.equ` to set a symbol to an expression, NASM uses `EQU`, and MASM uses `=` or `EQU`.
- All three Assemblers can use single or double quotes for strings.
- MASM relies more on assumptions (e.g., data sizes), so sometimes interpreting what an instruction does can be difficult.
- NASM *rip*-relative addressing can be noted explicitly, but is only required if external functions are used: `mov rax, [rel test]`; GAS always requires explicit notation: `movq test(%rip),%rax`.
- NASM usually does not require a size directive for source operands, but a size directive can be used. A size directive is required for destination operands.

```
mov rax, [test]           ; source size is not required
mov rax, DWORD [test]    ; but can be used
mov DWORD [test], rax    ; required for destination
```

Operation	GAS	NASM	MASM
Clear a register ( <i>rax</i> )	<code>xorq %rax, %rax</code>	<code>xor rax, rax</code>	
Move contents of <i>rax</i> to <i>rsi</i>	<code>movq %rax, %rsi</code>	<code>mov rsi, rax</code>	
Move contents of <i>ax</i> to <i>si</i>	<code>movw %ax, %si</code>	<code>mov si, ax</code>	
Move immediate byte value 4 to <i>al</i>	<code>movb \$4, %al</code>	<code>mov al, 4</code>	

Operation	GAS	NASM	MASM
Move contents of address 0xf into <i>eax</i>	<code>movl 0x0f, %eax</code>	<code>mov eax, [0x0f]</code>	<code>mov eax, ds:[0fh]</code>
Move contents of variable <i>temp</i> into <i>rax</i>	<code>movq temp(%rip), %rax</code>	<code>mov rax, QWORD [temp]</code>	<code>mov rax, temp</code>
Move address of variable <i>temp</i> into <i>eax/rax</i> using MOV	<code>movl \$temp, %eax</code> # no absolute addressing # in 64-bit, use LEA	<code>mov eax, temp</code> <code>mov rax, temp</code>	<code>mov eax, OFFSET temp</code> <code>mov rax, OFFSET temp</code>
Load address of variable <i>temp</i> into <i>eax</i> using LEA (32-bit)	<code>leal temp, %eax</code>	<code>lea eax, [temp]</code>	<code>lea eax, temp</code>
Load address of variable <i>temp</i> into <i>rax</i> using LEA (64-bit)	<code>leaq temp(%rip), %rax</code>	<code>lea rax, [rel temp]</code> ; rel required if ; external functions used	<code>lea rax, temp</code>
Move contents of <i>rax</i> into variable <i>temp</i>	<code>movq %rax, temp(%rip)</code>	<code>mov QWORD [temp], rax</code>	<code>mov temp, rax</code>
Move immediate byte value 2 into <i>temp</i>	<code>movb \$2, temp(%rip)</code>	<code>mov BYTE [temp], 2</code>	<code>mov temp, 2</code> <code>mov BYTE PTR temp, 2</code>
Move immediate byte value 2 into memory pointed to by <i>eax/rax</i>	<code>movb \$2, (%eax)</code> <code>movb \$2, (%rax)</code>	<code>mov BYTE [eax], 2</code> <code>mov BYTE [rax], 2</code>	<code>mov BYTE PTR [eax], 2</code> <code>mov BYTE PTR [rax], 2</code>
Move immediate word value 4 into memory pointed to by <i>eax/rax</i>	<code>movw \$4, (%eax)</code> <code>movw \$4, (%rax)</code>	<code>mov WORD [eax], 4</code> <code>mov WORD [rax], 4</code>	<code>mov WORD PTR [eax], 4</code> <code>mov WORD PTR [rax], 4</code>
Move immediate doubleword value 6 into memory pointed to by <i>eax/rax</i>	<code>movl \$6, (%eax)</code> <code>movl \$6, (%rax)</code>	<code>mov DWORD [eax], 6</code> <code>mov DWORD [rax], 6</code>	<code>mov DWORD PTR [eax], 6</code> <code>mov DWORD PTR [rax], 6</code>
Move immediate quadword value 8 into memory pointed to by <i>eax/rax</i>	<code>movq \$8, (%eax)</code> <code>movq \$8, (%rax)</code>	<code>mov QWORD [eax], 8</code> <code>mov QWORD [rax], 8</code>	<code>mov QWORD PTR [eax], 8</code> <code>mov QWORD PTR [rax], 8</code>
Include file syntax	<code>.include "file.ext"</code>	<code>%include "file.ext"</code>	<code>INCLUDE file.ext</code>
Identifier syntax	<code>identifier: type value</code>		<code>identifier type value</code>
Get size of array in bytes using <i>current location counter</i> (code directly after array declaration)	<code>aSize: .quad (. - array)</code>	<code>aSize: EQU (\$ - array)</code>	<code>aSize = (\$ - array)</code>
Create and use a symbol with EQU	<code>.equ temp, (2 * 6 / 3)</code> <code>mov \$temp, %rax</code>	<code>temp: EQU (2 * 6 / 3)</code> <code>mov rax, temp</code>	<code>temp EQU (2 * 6 / 3)</code> <code>mov rax, temp</code>
Reserve 64 bytes of memory	<code>.space 64</code>	<code>resb 64</code>	<code>db 64 DUP (?)</code>
Create uninitialized 32-bit/64-bit variable <i>temp</i>	<code>.lcomm temp, 4</code> <code>.lcomm temp, 8</code>	<code>temp: resd 1</code> <code>temp: resq 1</code>	<code>temp DWORD ?</code> <code>temp QWORD ?</code>
Create initialized 32-bit/64-bit variable <i>temp</i> with value 5	<code>temp: .long 5</code> <code>temp: .quad 5</code>	<code>temp: dd 5</code> <code>temp: dq 5</code>	<code>temp DWORD 5</code> <code>temp QWORD 5</code>
Create an array w/ 32-bit/64-bit values	<code>temp: .long 5, 10, 15</code> <code>temp: .quad 5, 10, 15</code>	<code>temp: dd, 5, 10, 15</code> <code>temp: dq, 5, 10, 15</code>	<code>temp DWORD 5, 10, 15</code> <code>temp QWORD 5, 10, 15</code>
Create "Hello, World" string (code on one line)	<code>identifier: .ascii</code> <code>"Hello, World"</code>	<code>identifier: db</code> <code>'Hello, World'</code>	<code>identifier BYTE</code> <code>"Hello, World"</code>

Operation	GAS	NASM	MASM
Create "Hello, World" w/newline and null (code on one line)	identifier: .asciz "Hello, World\n"	identifier: db 'Hello, World', 10, 0	identifier BYTE "Hello, World", 10, 0
Function structure	identifier: ... ret		identifier PROC ... ret identifier ENDP
Program segments (sections)	.data .bss .text	SECTION .data SECTION .bss SECTION .text	.data .code
Data types	.byte .word .long .quad	db dw dd dq	BYTE WORD DWORD QWORD
Repetition (code on one line)	identifier: .fill count, size, value	identifier: TIMES count type value	identifier type count DUP (value)
Macro definition	.macro identifier arg1, arg2... args referenced as \arg1 .endm	%macro identifier argcount args referenced as [%1] %endmacro	identifier MACRO arg1, arg2... args referenced as arg1 ENDM
Macro usage	identifier param1, param2, etc...		
Comment (single-line)	# this is a comment	; this is a comment	
32-bit _main exit routine	# for GAS/Clang on Mac pushl \$0 subl \$4, %esp movl \$1, %eax int \$0x80  # for GAS/Clang on Linux mov \$1, %eax mov \$0, %ebx int \$0x80	; for NASM on Linux mov eax, 1 mov ebx, 0 int 80h  ; for NASM on Mac push DWORD 0 sub esp, 4 mov eax, 1 int 80h	; before .data segment ExitProcess PROTO, dwExitCode:DWORD  ; before _main ENDP INVOKE ExitProcess, 0
64-bit _main exit routine	# for GAS/Clang on Mac movq \$0x2000001, %rax xorq %rdi, %rdi syscall  # for GAS/Clang on Linux mov \$60, %rax xor %rdi, %rdi syscall	; for NASM on Linux mov rax, 60 xor rdi, rdi syscall  ; for NASM on Mac mov rax, 2000001h xor rdi, rdi syscall	; before .data segment extrn ExitProcess:proc  ; before _main ENDP xor rcx, rcx call ExitProcess

