Lab6_FreeRTOS

Generated by Doxygen 1.8.13

Contents

1	File	Index			1
	1.1	File Lis	st		1
2	File	Docum	entation		3
	2.1			eConnectedMCU_Labs/nhutsenko/lab6_RTOS/Lab10_Adv_Concurrency/src/↔ uce.c File Reference	3
		2.1.1	Macro D	efinition Documentation	3
			2.1.1.1	hwUNLOCK_KEY_0	3
			2.1.1.2	hwUNLOCK_KEY_1	3
		2.1.2	Function	Documentation	4
			2.1.2.1	vHardwareConfigurePerformance()	4
			2.1.2.2	vHardwareUseMultiVectoredInterrupts()	4
	2.2			eConnectedMCU_Labs/nhutsenko/lab6_RTOS/Lab10_Adv_Concurrency/src/↔ uce.h File Reference	4
		2.2.1	Function	Documentation	4
			2.2.1.1	vHardwareConfigurePerformance()	4
			2.2.1.2	vHardwareUseMultiVectoredInterrupts()	4
	2.3		•	eConnectedMCU_Labs/nhutsenko/lab6_RTOS/Lab10_Adv_Concurrency/src/↔ g.h File Reference	4
		2.3.1	Macro D	efinition Documentation	5
			2.3.1.1	configASSERT	6
			2.3.1.2	configCHECK_FOR_STACK_OVERFLOW	6
			2.3.1.3	configCPU_CLOCK_HZ	6
			2.3.1.4	configGENERATE_RUN_TIME_STATS	6
			2315	configIDLE_SHOULD_VIELD	6

ii CONTENTS

2.3.1.6	configISR_STACK_SIZE	6
2.3.1.7	configKERNEL_INTERRUPT_PRIORITY	6
2.3.1.8	configMAX_CO_ROUTINE_PRIORITIES	7
2.3.1.9	configMAX_PRIORITIES	7
2.3.1.10	configMAX_SYSCALL_INTERRUPT_PRIORITY	7
2.3.1.11	configMAX_TASK_NAME_LEN	7
2.3.1.12	configMINIMAL_STACK_SIZE	7
2.3.1.13	configPERIPHERAL_CLOCK_HZ	7
2.3.1.14	configQUEUE_REGISTRY_SIZE	7
2.3.1.15	configTICK_RATE_HZ	7
2.3.1.16	configTIMER_QUEUE_LENGTH	8
2.3.1.17	configTIMER_TASK_PRIORITY	8
2.3.1.18	configTIMER_TASK_STACK_DEPTH	8
2.3.1.19	configTOTAL_HEAP_SIZE	8
2.3.1.20	configUSE_16_BIT_TICKS	8
2.3.1.21	configUSE_APPLICATION_TASK_TAG	8
2.3.1.22	configUSE_CO_ROUTINES	8
2.3.1.23	configUSE_COUNTING_SEMAPHORES	8
2.3.1.24	configUSE_IDLE_HOOK	9
2.3.1.25	configUSE_MALLOC_FAILED_HOOK	9
2.3.1.26	configUSE_MUTEXES	9
2.3.1.27	configUSE_PORT_OPTIMISED_TASK_SELECTION	9
2.3.1.28	configUSE_PREEMPTION	9
2.3.1.29	configUSE_QUEUE_SETS	9
2.3.1.30	configUSE_RECURSIVE_MUTEXES	9
2.3.1.31	configUSE_TICK_HOOK	9
2.3.1.32	configUSE_TIMERS	10
2.3.1.33	configUSE_TRACE_FACILITY	10
2.3.1.34	INCLUDE_eTaskGetState	10
2.3.1.35	INCLUDE_uxTaskGetStackHighWaterMark	10

CONTENTS

		2.3.1.36	INCLUDE_uxTaskPriorityGet	10
		2.3.1.37	INCLUDE_vTaskCleanUpResources	10
		2.3.1.38	INCLUDE_vTaskDelay	10
		2.3.1.39	INCLUDE_vTaskDelayUntil	10
		2.3.1.40	INCLUDE_vTaskDelete	11
		2.3.1.41	INCLUDE_vTaskPrioritySet	11
		2.3.1.42	INCLUDE_vTaskSuspend	11
		2.3.1.43	INCLUDE_xTimerPendFunctionCall	11
	2.3.2	Function	Documentation	11
		2.3.2.1	vAssertCalled()	11
2.4		•	ConnectedMCU_Labs/nhutsenko/lab6_RTOS/Lab10_Adv_Concurrency/src/main.c	11
	2.4.1	Function	Documentation	12
		2.4.1.1	_general_exception_handler()	12
		2.4.1.2	main()	12
		2.4.1.3	vApplicationStackOverflowHook()	12
		2.4.1.4	vApplicationTickHook()	12
		2.4.1.5	vAssertCalled()	12
2.5			ConnectedMCU_Labs/nhutsenko/lab6_RTOS/Lab10_Adv_Concurrency/src/UAconce	13
	2.5.1	Function	Documentation	13
		2.5.1.1	UART4_init()	13
		2.5.1.2	UART4_putc()	13
		2.5.1.3	UART4_puts()	14
2.6		•	ConnectedMCU_Labs/nhutsenko/lab6_RTOS/Lab10_Adv_Concurrency/src/UAconce	14
	2.6.1	Function	Documentation	14
		2.6.1.1	UART4_init()	14
		2.6.1.2	UART4_putc()	15
		2.6.1.3	UART4_puts()	15
2.7		•	ConnectedMCU_Labs/nhutsenko/lab6_RTOS/Lab10_Adv_Concurrency/src/user.c	15

iv CONTENTS

	2.7.1	Macro De	efinition Documentation	16
		2.7.1.1	NELEMS	16
	2.7.2	Function	Documentation	16
		2.7.2.1	DelayMs()	16
		2.7.2.2	InitApp()	17
		2.7.2.3	InitGPIO()	17
		2.7.2.4	SetNum()	18
		2.7.2.5	Task1()	18
		2.7.2.6	Task2()	18
	2.7.3	Variable I	Documentation	18
		2.7.3.1	bulls	18
		2.7.3.2	cows	19
		2.7.3.3	gameEnd	19
		2.7.3.4	$1 \ldots \ldots \ldots \ldots \ldots$	19
		2.7.3.5	$j \ \dots $	19
		2.7.3.6	my_mass	19
		2.7.3.7	num	19
		2.7.3.8	random_mass	19
2.8		•	ConnectedMCU_Labs/nhutsenko/lab6_RTOS/Lab10_Adv_Concurrency/src/user.h	
				20
	2.8.1		efinition Documentation	20
		2.8.1.1	BIOS_BTN1_PORT_BIT	21
		2.8.1.2	BIOS_BTN2_PORT_BIT	21
		2.8.1.3	BIOS_BTN3_PORT_BIT	21
		2.8.1.4	BIOS_BTN4_PORT_BIT	21
		2.8.1.5	BIOS_LD1_PORT_BIT	21
		2.8.1.6	BIOS_LD2_PORT_BIT	21
		2.8.1.7	BIOS_LD3_PORT_BIT	21
		2.8.1.8	BIOS_LD4_PORT_BIT	21
		2.8.1.9	BIOS_LD5_PORT_BIT	22
		2.8.1.10	BIOS_LD6_PORT_BIT	22
		2.8.1.11	BIOS_LD7_PORT_BIT	22
		2.8.1.12	BIOS_LD8_PORT_BIT	22
		2.8.1.13	BTN1_PORT_BIT	22
		2.8.1.14	BTN2_PORT_BIT	22
		2.8.1.15	BTN3_PORT_BIT	22
	2.8.2	Function	Documentation	22
		2.8.2.1	DelayMs()	22
		2.8.2.2	InitApp()	23
		2.8.2.3	Task1()	23
		2.8.2.4	Task2()	23
	2.8.3	Variable I	Documentation	23
		2.8.3.1	xMutex	23

Index 25

Chapter 1

File Index

1.1 File List

Here is a list of all files with brief descriptions:

D:/KPI/pic32/TheConnectedMCU_Labs/nhutsenko/lab6_RTOS/Lab10_Adv_Concurrency/src/Config	
Performance.c	3
D:/KPI/pic32/TheConnectedMCU_Labs/nhutsenko/lab6_RTOS/Lab10_Adv_Concurrency/src/Config←	
Performance.h	4
D:/KPI/pic32/TheConnectedMCU_Labs/nhutsenko/lab6_RTOS/Lab10_Adv_Concurrency/src/FreeRTO -	
SConfig.h	4
D:/KPI/pic32/TheConnectedMCU_Labs/nhutsenko/lab6_RTOS/Lab10_Adv_Concurrency/src/main.c	11
$lem:connectedMCU_Labs/nhutsenko/lab6_RTOS/Lab10_Adv_Concurrency/src/UART.c$	13
$lem:connectedMCU_Labs/nhutsenko/lab6_RTOS/Lab10_Adv_Concurrency/src/UART.h$	14
D:/KPI/pic32/TheConnectedMCU_Labs/nhutsenko/lab6_RTOS/Lab10_Adv_Concurrency/src/user.c	15
D:/KPI/pic32/TheConnectedMCU_Labs/nhutsenko/lab6_RTOS/Lab10_Adv_Concurrency/src/user.h	20

2 File Index

Chapter 2

File Documentation

2.1 D:/KPI/pic32/TheConnectedMCU_Labs/nhutsenko/lab6_RTOS/Lab10_Adv_Concurrency/src/← ConfigPerformance.c File Reference

```
#include "FreeRTOS.h"
#include "ConfigPerformance.h"
```

Macros

- #define hwUNLOCK_KEY_0 (0xAA996655UL)
- #define hwUNLOCK_KEY_1 (0x556699AAUL)

Functions

- void vHardwareConfigurePerformance (void)
- void vHardwareUseMultiVectoredInterrupts (void)

2.1.1 Macro Definition Documentation

```
2.1.1.1 hwUNLOCK_KEY_0
#define hwUNLOCK_KEY_0 ( 0xAA996655UL )

2.1.1.2 hwUNLOCK_KEY_1
#define hwUNLOCK_KEY_1 ( 0x556699AAUL )
```

2.1.2 Function Documentation

2.1.2.1 vHardwareConfigurePerformance()

2.1.2.2 vHardwareUseMultiVectoredInterrupts()

```
\begin{tabular}{ll} \begin{tabular}{ll} void & vo
```

2.2 D:/KPI/pic32/TheConnectedMCU_Labs/nhutsenko/lab6_RTOS/Lab10_Adv_Concurrency/src/
ConfigPerformance.h File Reference

Functions

- void vHardwareConfigurePerformance (void)
- void vHardwareUseMultiVectoredInterrupts (void)
- 2.2.1 Function Documentation
- 2.2.1.1 vHardwareConfigurePerformance()

2.2.1.2 vHardwareUseMultiVectoredInterrupts()

```
\begin{tabular}{ll} void & vHardwareUseMultiVectoredInterrupts ( \\ & void & ) \end{tabular}
```

2.3 D:/KPI/pic32/TheConnectedMCU_Labs/nhutsenko/lab6_RTOS/Lab10_Adv_Concurrency/src/

FreeRTOSConfig.h File Reference

```
#include <xc.h>
```

Reference 5
Macros

- #define configUSE PREEMPTION 1
- #define configUSE_PORT_OPTIMISED_TASK_SELECTION 1
- #define configUSE QUEUE SETS 1
- #define configUSE IDLE HOOK 0
- #define configUSE TICK HOOK 1
- #define configTICK RATE HZ ((TickType t) 1000)
- #define configCPU CLOCK HZ (200000000UL)
- #define configPERIPHERAL_CLOCK_HZ (40000000UL)
- #define configMAX PRIORITIES (8UL)
- #define configMINIMAL STACK SIZE (190)
- #define configISR STACK SIZE (400)
- #define configTOTAL HEAP SIZE ((size t) 60000)
- #define configMAX TASK NAME LEN (16)
- #define configUSE_TRACE_FACILITY 0
- #define configUSE_16_BIT_TICKS 0
- #define configIDLE SHOULD YIELD 1
- #define configUSE MUTEXES 1
- #define configCHECK_FOR_STACK_OVERFLOW 3 /* Three also checks the system/interrupt stack. */
- #define configQUEUE_REGISTRY_SIZE 0
- #define configUSE RECURSIVE MUTEXES 1
- #define configUSE_MALLOC_FAILED_HOOK 0
- #define configUSE_APPLICATION_TASK_TAG 0
- #define configUSE COUNTING SEMAPHORES 1
- #define configGENERATE RUN TIME STATS 0
- #define configUSE CO ROUTINES 0
- #define configMAX_CO_ROUTINE_PRIORITIES (2)
- #define configUSE_TIMERS 1
- #define configTIMER TASK PRIORITY (2)
- #define configTIMER QUEUE LENGTH 5
- #define configTIMER_TASK_STACK_DEPTH (configMINIMAL_STACK_SIZE * 2)
- #define INCLUDE vTaskPrioritySet 1
- #define INCLUDE uxTaskPriorityGet 1
- #define INCLUDE vTaskDelete 1
- #define INCLUDE vTaskCleanUpResources 0
- #define INCLUDE vTaskSuspend 1
- #define INCLUDE vTaskDelayUntil 1
- #define INCLUDE_vTaskDelay 1
- #define INCLUDE_uxTaskGetStackHighWaterMark 1
- #define INCLUDE_eTaskGetState 1
- #define INCLUDE_xTimerPendFunctionCall 1
- #define configKERNEL INTERRUPT PRIORITY 0x01
- #define configMAX SYSCALL INTERRUPT PRIORITY 0x03
- #define configASSERT(x) if((x) == 0) vAssertCalled(__FILE__, __LINE__)

Functions

void vAssertCalled (const char *pcFile, unsigned long ulLine)

2.3.1 Macro Definition Documentation

2.3.1.1 configASSERT

2.3.1.2 configCHECK_FOR_STACK_OVERFLOW

#define configCHECK_FOR_STACK_OVERFLOW 3 /* Three also checks the system/interrupt stack. */

2.3.1.3 configCPU_CLOCK_HZ

#define configCPU_CLOCK_HZ (20000000UL)

2.3.1.4 configGENERATE_RUN_TIME_STATS

#define configGENERATE_RUN_TIME_STATS 0

2.3.1.5 configIDLE_SHOULD_YIELD

#define configIDLE_SHOULD_YIELD 1

2.3.1.6 configISR_STACK_SIZE

#define configISR_STACK_SIZE (400)

2.3.1.7 configKERNEL_INTERRUPT_PRIORITY

#define configKERNEL_INTERRUPT_PRIORITY 0x01

Reference 7

2.3.1.8 configMAX_CO_ROUTINE_PRIORITIES

```
#define configMAX_CO_ROUTINE_PRIORITIES ( 2 )
```

2.3.1.9 configMAX_PRIORITIES

```
#define configMAX_PRIORITIES ( 8UL )
```

2.3.1.10 configMAX_SYSCALL_INTERRUPT_PRIORITY

```
#define configMAX_SYSCALL_INTERRUPT_PRIORITY 0x03
```

2.3.1.11 configMAX_TASK_NAME_LEN

```
#define configMAX_TASK_NAME_LEN ( 16 )
```

2.3.1.12 configMINIMAL_STACK_SIZE

```
#define configMINIMAL_STACK_SIZE ( 190 )
```

2.3.1.13 configPERIPHERAL_CLOCK_HZ

```
#define configPERIPHERAL_CLOCK_HZ ( 4000000UL )
```

2.3.1.14 configQUEUE_REGISTRY_SIZE

```
#define configQUEUE_REGISTRY_SIZE 0
```

2.3.1.15 configTICK_RATE_HZ

```
#define configTICK_RATE_HZ ( ( TickType_t ) 1000 )
```

2.3.1.16 configTIMER_QUEUE_LENGTH

#define configTIMER_QUEUE_LENGTH 5

2.3.1.17 configTIMER_TASK_PRIORITY

#define configTIMER_TASK_PRIORITY (2)

2.3.1.18 configTIMER_TASK_STACK_DEPTH

#define configTIMER_TASK_STACK_DEPTH (configMINIMAL_STACK_SIZE * 2)

2.3.1.19 configTOTAL_HEAP_SIZE

#define configTOTAL_HEAP_SIZE ((size_t) 60000)

2.3.1.20 configUSE_16_BIT_TICKS

#define configUSE_16_BIT_TICKS 0

2.3.1.21 configUSE_APPLICATION_TASK_TAG

#define configUSE_APPLICATION_TASK_TAG 0

2.3.1.22 configUSE_CO_ROUTINES

#define configUSE_CO_ROUTINES 0

2.3.1.23 configUSE_COUNTING_SEMAPHORES

#define configUSE_COUNTING_SEMAPHORES 1

$2.3 \ D:/KPI/pic32/The Connected MCU_Labs/nhutsenko/lab6_RTOS/Lab10_Adv_Concurrency/src/FreeRTO {\it Config.} \\ have a configuration of the configuration o$

Reference 9

2.3.1.24 configUSE_IDLE_HOOK

#define configUSE_IDLE_HOOK 0

2.3.1.25 configUSE_MALLOC_FAILED_HOOK

#define configUSE_MALLOC_FAILED_HOOK 0

2.3.1.26 configUSE_MUTEXES

#define configUSE_MUTEXES 1

2.3.1.27 configUSE_PORT_OPTIMISED_TASK_SELECTION

#define configUSE_PORT_OPTIMISED_TASK_SELECTION 1

2.3.1.28 configUSE_PREEMPTION

#define configUSE_PREEMPTION 1

2.3.1.29 configUSE_QUEUE_SETS

#define configUSE_QUEUE_SETS 1

2.3.1.30 configUSE_RECURSIVE_MUTEXES

#define configUSE_RECURSIVE_MUTEXES 1

2.3.1.31 configUSE_TICK_HOOK

#define configUSE_TICK_HOOK 1

2.3.1.32 configUSE_TIMERS

#define configUSE_TIMERS 1

2.3.1.33 configUSE_TRACE_FACILITY

#define configUSE_TRACE_FACILITY 0

2.3.1.34 INCLUDE_eTaskGetState

#define INCLUDE_eTaskGetState 1

2.3.1.35 INCLUDE_uxTaskGetStackHighWaterMark

#define INCLUDE_uxTaskGetStackHighWaterMark 1

2.3.1.36 INCLUDE_uxTaskPriorityGet

#define INCLUDE_uxTaskPriorityGet 1

2.3.1.37 INCLUDE_vTaskCleanUpResources

#define INCLUDE_vTaskCleanUpResources 0

2.3.1.38 INCLUDE_vTaskDelay

#define INCLUDE_vTaskDelay 1

2.3.1.39 INCLUDE_vTaskDelayUntil

#define INCLUDE_vTaskDelayUntil 1

2.3.1.40 INCLUDE_vTaskDelete

```
#define INCLUDE_vTaskDelete 1
```

2.3.1.41 INCLUDE_vTaskPrioritySet

```
#define INCLUDE_vTaskPrioritySet 1
```

2.3.1.42 INCLUDE_vTaskSuspend

```
#define INCLUDE_vTaskSuspend 1
```

2.3.1.43 INCLUDE_xTimerPendFunctionCall

```
#define INCLUDE_xTimerPendFunctionCall 1
```

2.3.2 Function Documentation

2.3.2.1 vAssertCalled()

2.4 D:/KPI/pic32/TheConnectedMCU_Labs/nhutsenko/lab6_RTOS/Lab10_Adv_Concurrency/src/main.c File Reference

```
#include "FreeRTOS.h"
#include "task.h"
#include "semphr.h"
#include "queue.h"
#include "user.h"
#include "OLED.h"
#include "OLED/OledChar.h"
#include "OLED/OledGrph.h"
#include "UART.h"
#include "ConfigPerformance.h"
```

Functions

- int main (void)
- void vApplicationStackOverflowHook (TaskHandle_t pxTask, char *pcTaskName)
- void vApplicationTickHook (void)
- void vAssertCalled (const char *pcFile, unsigned long ulLine)
- void _general_exception_handler (void)

2.4.1 Function Documentation

2.4.1.1 _general_exception_handler()

```
\begin{tabular}{ll} \begin{tabular}{ll} void & \_general\_exception\_handler ( \\ & void & ) \end{tabular}
```

2.4.1.2 main()

```
int main (
     void )
```

2.4.1.3 vApplicationStackOverflowHook()

2.4.1.4 vApplicationTickHook()

2.4.1.5 vAssertCalled()

2.5 D:/KPI/pic32/TheConnectedMCU_Labs/nhutsenko/lab6_RTOS/Lab10_Adv_Concurrency/src/ **UART.c** File Reference

```
#include "FreeRTOS.h"
#include "task.h"
#include "semphr.h"
#include "queue.h"
#include "UART.h"
#include <sys/attribs.h>
#include "user.h"
```

Functions

- void UART4_init (void)
- void UART4_putc (char c)
- void UART4_puts (char *s)

2.5.1 Function Documentation

```
2.5.1.1 UART4_init()
```

```
void UART4_init (
            void )
```

Function prototype:

Summary:

Description:

Precondition:

Parameters:

Returns:

Example:

Remarks:

2.5.1.2 UART4_putc()

```
void UART4_putc (
           char c )
```

2.5.1.3 UART4_puts()

```
void UART4_puts ( {\tt char} \ * \ s \ )
```

2.6 D:/KPI/pic32/TheConnectedMCU_Labs/nhutsenko/lab6_RTOS/Lab10_Adv_Concurrency/src/

UART.h File Reference

```
#include <xc.h>
#include "FreeRTOS.h"
#include "task.h"
#include "semphr.h"
#include "queue.h"
```

Functions

- void UART4_init (void)
- void UART4_putc (char c)
- void UART4_puts (char *s)

2.6.1 Function Documentation

2.6.1.1 UART4_init()

```
void UART4_init (
     void )
```

Function prototype:

Summary:

Description:

Precondition:

Parameters:

Returns:

Example:

Remarks:

2.6.1.2 UART4_putc()

```
void UART4_putc ( {\tt char}~c~)
```

2.6.1.3 UART4_puts()

2.7 D:/KPI/pic32/TheConnectedMCU_Labs/nhutsenko/lab6_RTOS/Lab10_Adv_Concurrency/src/user.c File Reference

```
#include <xc.h>
#include <stdint.h>
#include <sys/attribs.h>
#include <time.h>
#include "FreeRTOS.h"
#include "task.h"
#include "semphr.h"
#include "oleD.h"
#include "OledChar.h"
#include "OledGrph.h"
#include "UART.h"
```

Macros

#define NELEMS(x) (sizeof(x) / sizeof((x)[0]))

Array length calculator.

Functions

void InitGPIO (void)

This function is using for setting buttons on I/O shield.

void InitApp (void)

This function is using for setting all used devices.

int32_t SetNum ()

This function is using for choose a number for game.

void Task1 (void *pvParameters)

Game status.

void Task2 (void *pvParameters)

Points tatus.

void DelayMs (int t)

Delay function.

Variables

```
• int32_t random_mass [4]
```

```
• int32_t my_mass [4]
```

- int cows = 0
- int **bulls** = 0
- int32_t i
- int32_t j
- int32_t num
- int32_t gameEnd = 0

2.7.1 Macro Definition Documentation

2.7.1.1 NELEMS

```
#define NELEMS( x ) (sizeof(x) / sizeof((x)[0]))
```

Array length calculator.

Software realization of delay function, using empty for cycle

Parameters

in	X	type of vars into array
out	length	of mass

Returns

array length

2.7.2 Function Documentation

2.7.2.1 DelayMs()

```
void DelayMs ( \quad \text{int } t \ )
```

Delay function.

This function is using for delay between operations

2.7 D:/KPI/pic32/TheConnectedMCU_Labs/nhutsenko/lab6_RTOS/Lab10_Adv_Concurrency/src/user.c File Reference

P	a	ra	m	ρi	ŀΔ	re
г	a	ıa		C.	ıc	ıə

in <i>delay</i> in ms

Returns

NONE

2.7.2.2 InitApp()

```
void InitApp (
     void )
```

This function is using for setting all used devices.

This function is using for setting all used devices

Parameters

NONE

Returns

NONE

2.7.2.3 InitGPIO()

```
void InitGPIO (
     void )
```

This function is using for setting buttons on I/O shield.

This function is using for setting all used devices

Parameters

NONE

Returns

NONE

2.7.2.4 SetNum()

```
int32_t SetNum ( )
```

This function is using for choose a number for game.

This function is using for chhose a number for game

Parameters

out	integer	number from 0 to 9
-----	---------	--------------------

Returns

integer number from 0 to 9

2.7.2.5 Task1()

Game status.

This task is using for input user numbers or reseting the game

2.7.2.6 Task2()

```
void Task2 ( \label{eq:void * pvParameters} \ )
```

Points tatus.

This task is using for checking points status at the game

2.7.3 Variable Documentation

2.7.3.1 bulls

```
int bulls = 0
```



2.8 D:/KPI/pic32/TheConnectedMCU_Labs/nhutsenko/lab6_RTOS/Lab10_Adv_Concurrency/src/user.h File Reference

```
#include "FreeRTOS.h"
#include "semphr.h"
```

Macros

- #define BTN1 PORT BIT PORTAbits.RA3
- #define BTN2 PORT BIT PORTDbits.RD5

Define name for BTN2 on I/O shield.

• #define BTN3 PORT BIT PORTFbits.RF1

Define name for BTN3 on I/O shield.

- #define BIOS_LD1_PORT_BIT LATEbits.LATE0
- #define BIOS_LD2_PORT_BIT LATEbits.LATE1
- #define BIOS LD3 PORT BIT LATEbits.LATE2
- #define BIOS LD4 PORT BIT LATEbits.LATE3
- #define BIOS_LD5_PORT_BIT LATEbits.LATE4
- #define BIOS_LD6_PORT_BIT LATEbits.LATE5
- #define BIOS LD7 PORT BIT LATEbits.LATE6
- #define BIOS LD8 PORT BIT LATEbits.LATE7
- #define BIOS_BTN1_PORT_BIT PORTGbits.RG7
- #define BIOS_BTN2_PORT_BIT PORTDbits.RD5
- #define BIOS_BTN3_PORT_BIT PORTFbits.RF1
- #define BIOS BTN4 PORT BIT PORTAbits.RA2

Functions

void InitApp (void)

This function is using for setting all used devices.

void Task1 (void *pvParameters)

Game status.

void Task2 (void *pvParameters)

Points tatus.

• void DelayMs (int t)

Delay function.

Variables

SemaphoreHandle_t xMutex

2.8.1 Macro Definition Documentation

2.8.1.1 BIOS_BTN1_PORT_BIT #define BIOS_BTN1_PORT_BIT PORTGbits.RG7 2.8.1.2 BIOS_BTN2_PORT_BIT #define BIOS_BTN2_PORT_BIT PORTDbits.RD5 2.8.1.3 BIOS_BTN3_PORT_BIT #define BIOS_BTN3_PORT_BIT PORTFbits.RF1 2.8.1.4 BIOS_BTN4_PORT_BIT #define BIOS_BTN4_PORT_BIT PORTAbits.RA2 2.8.1.5 BIOS_LD1_PORT_BIT #define BIOS_LD1_PORT_BIT LATEbits.LATE0 2.8.1.6 BIOS LD2 PORT BIT #define BIOS_LD2_PORT_BIT LATEbits.LATE1 2.8.1.7 BIOS_LD3_PORT_BIT #define BIOS_LD3_PORT_BIT LATEbits.LATE2

2.8.1.8 BIOS_LD4_PORT_BIT

#define BIOS_LD4_PORT_BIT LATEbits.LATE3

2.8.1.9 BIOS_LD5_PORT_BIT

```
#define BIOS_LD5_PORT_BIT LATEbits.LATE4
```

2.8.1.10 BIOS_LD6_PORT_BIT

```
#define BIOS_LD6_PORT_BIT LATEbits.LATE5
```

2.8.1.11 BIOS_LD7_PORT_BIT

```
#define BIOS_LD7_PORT_BIT LATEbits.LATE6
```

2.8.1.12 BIOS_LD8_PORT_BIT

```
#define BIOS_LD8_PORT_BIT LATEbits.LATE7
```

2.8.1.13 BTN1_PORT_BIT

```
#define BTN1_PORT_BIT PORTAbits.RA3
```

2.8.1.14 BTN2_PORT_BIT

```
#define BTN2_PORT_BIT PORTDbits.RD5
```

Define name for BTN2 on I/O shield.

2.8.1.15 BTN3_PORT_BIT

```
#define BTN3_PORT_BIT PORTFbits.RF1
```

Define name for BTN3 on I/O shield.

2.8.2 Function Documentation

2.8.2.1 DelayMs()

```
void DelayMs ( \quad \text{int } t \ )
```

Delay function.

This function is using for delay between operations

Parameters

in <i>delay</i>	in ms
-----------------	-------

Returns

NONE

2.8.2.2 InitApp()

```
void InitApp (
     void )
```

This function is using for setting all used devices.

This function is using for setting all used devices

Parameters

NONE

Returns

NONE

2.8.2.3 Task1()

Game status.

This task is using for input user numbers or reseting the game

2.8.2.4 Task2()

```
void Task2 ( \mbox{void} \ *\ pvParameters \ )
```

Points tatus.

This task is using for checking points status at the game

2.8.3 Variable Documentation

2.8.3.1 xMutex

 ${\tt SemaphoreHandle_t xMutex}$

Index

_general_exception_handler	configMAX_CO_ROUTINE_PRIORITIES
main.c, 12	FreeRTOSConfig.h, 6
	configMAX_PRIORITIES
BIOS_BTN1_PORT_BIT	FreeRTOSConfig.h, 7
user.h, 20	configMAX_SYSCALL_INTERRUPT_PRIORITY
BIOS_BTN2_PORT_BIT	FreeRTOSConfig.h, 7
user.h, 21	configMAX_TASK_NAME_LEN
BIOS_BTN3_PORT_BIT	FreeRTOSConfig.h, 7
user.h, 21	configMINIMAL STACK SIZE
BIOS_BTN4_PORT_BIT	FreeRTOSConfig.h, 7
user.h, 21	configPERIPHERAL CLOCK HZ
BIOS_LD1_PORT_BIT	FreeRTOSConfig.h, 7
user.h, 21	ConfigPerformance.c
BIOS_LD2_PORT_BIT	hwUNLOCK_KEY_0, 3
user.h, 21	hwUNLOCK_KEY_1, 3
BIOS_LD3_PORT_BIT	vHardwareConfigurePerformance, 4
user.h, 21	vHardwareUseMultiVectoredInterrupts, 4
BIOS_LD4_PORT_BIT	ConfigPerformance.h
user.h, 21	vHardwareConfigurePerformance, 4
BIOS_LD5_PORT_BIT	vHardwareUseMultiVectoredInterrupts, 4
user.h, 21	configQUEUE_REGISTRY_SIZE
BIOS_LD6_PORT_BIT	FreeRTOSConfig.h, 7
user.h, 22	configTICK_RATE_HZ
BIOS_LD7_PORT_BIT	FreeRTOSConfig.h, 7
user.h, 22	configTIMER_QUEUE_LENGTH
BIOS_LD8_PORT_BIT	FreeRTOSConfig.h, 7
user.h, 22	configTIMER_TASK_PRIORITY
BTN1_PORT_BIT	FreeRTOSConfig.h, 8
user.h, 22	configTIMER_TASK_STACK_DEPTH
BTN2_PORT_BIT	FreeRTOSConfig.h, 8
user.h, 22	configTOTAL_HEAP_SIZE
BTN3_PORT_BIT	FreeRTOSConfig.h, 8
user.h, 22	configUSE 16 BIT TICKS
bulls	FreeRTOSConfig.h, 8
user.c, 18	configUSE APPLICATION TASK TAG
configASSERT	FreeRTOSConfig.h, 8
FreeRTOSConfig.h, 5	configUSE_CO_ROUTINES
configCHECK_FOR_STACK_OVERFLOW	FreeRTOSConfig.h, 8
FreeRTOSConfig.h, 6	configUSE_COUNTING_SEMAPHORES
configCPU_CLOCK_HZ	FreeRTOSConfig.h, 8
FreeRTOSConfig.h, 6	configUSE_IDLE_HOOK
configGENERATE_RUN_TIME_STATS	FreeRTOSConfig.h, 8
FreeRTOSConfig.h, 6	configUSE_MALLOC_FAILED_HOOK
configIDLE_SHOULD_YIELD	FreeRTOSConfig.h, 9
FreeRTOSConfig.h, 6	configUSE_MUTEXES
configISR_STACK_SIZE	FreeRTOSConfig.h, 9
FreeRTOSConfig.h, 6	configUSE PORT OPTIMISED TASK SELECTION
configKERNEL_INTERRUPT_PRIORITY	FreeRTOSConfig.h, 9
FreeRTOSConfig.h. 6	configUSE PREEMPTION

26 INDEX

FreeRTOSConfig.h, 9	configTIMER TASK PRIORITY, 8
configUSE QUEUE SETS	configTIMER_TASK_STACK_DEPTH, 8
FreeRTOSConfig.h, 9	configTOTAL HEAP SIZE, 8
configUSE_RECURSIVE_MUTEXES	configUSE_16_BIT_TICKS, 8
FreeRTOSConfig.h, 9	configUSE_APPLICATION_TASK_TAG, 8
configUSE TICK HOOK	configUSE_CO_ROUTINES, 8
·	
FreeRTOSConfig.h, 9	configUSE_COUNTING_SEMAPHORES, 8
configUSE_TIMERS	configUSE_IDLE_HOOK, 8
FreeRTOSConfig.h, 9	configUSE_MALLOC_FAILED_HOOK, 9
configUSE_TRACE_FACILITY	configUSE_MUTEXES, 9
FreeRTOSConfig.h, 10	$configUSE_PORT_OPTIMISED_TASK_SELEC\leftarrow$
cows	TION, 9
user.c, 18	configUSE_PREEMPTION, 9
	configUSE_QUEUE_SETS, 9
$D:/KPI/pic32/The Connected MCU_Labs/nhutsenko/lab6 \hookleftarrow$	configUSE_RECURSIVE_MUTEXES, 9
$_$ RTOS/Lab10 $_$ Adv $_$ Concurrency/src/ \longleftrightarrow	configUSE_TICK_HOOK, 9
ConfigPerformance.c, 3	configUSE TIMERS, 9
D:/KPI/pic32/TheConnectedMCU_Labs/nhutsenko/lab6←	configUSE_TRACE_FACILITY, 10
_RTOS/Lab10_Adv_Concurrency/src/←	INCLUDE_eTaskGetState, 10
ConfigPerformance.h, 4	INCLUDE_uxTaskGetStackHighWaterMark, 10
D:/KPI/pic32/TheConnectedMCU_Labs/nhutsenko/lab6←	INCLUDE uxTaskPriorityGet, 10
_RTOS/Lab10_Adv_Concurrency/src/Free ↔	INCLUDE_vTaskCleanUpResources, 10
RTOSConfig.h, 4	INCLUDE vTaskDelay, 10
D:/KPI/pic32/TheConnectedMCU_Labs/nhutsenko/lab6↔	- •••
_RTOS/Lab10_Adv_Concurrency/src/UAR←	INCLUDE_vTaskDelayUntil, 10
RTO3/LabTo_Adv_Concurrency/stc/OAR T.c, 13	INCLUDE_vTaskDelete, 10
	INCLUDE_vTaskPrioritySet, 11
D:/KPI/pic32/TheConnectedMCU_Labs/nhutsenko/lab6←	INCLUDE_vTaskSuspend, 11
_RTOS/Lab10_Adv_Concurrency/src/UAR	INCLUDE_xTimerPendFunctionCall, 11
T.h, 14	vAssertCalled, 11
D:/KPI/pic32/TheConnectedMCU_Labs/nhutsenko/lab6←	
_RTOS/Lab10_Adv_Concurrency/src/main.c,	gameEnd
_RTOS/Lab10_Adv_Concurrency/src/main.c,	gameEnd user.c, 19
_RTOS/Lab10_Adv_Concurrency/src/main.c, 11 D:/KPI/pic32/TheConnectedMCU_Labs/nhutsenko/lab6↔	user.c, 19
_RTOS/Lab10_Adv_Concurrency/src/main.c,	user.c, 19 hwUNLOCK_KEY_0
_RTOS/Lab10_Adv_Concurrency/src/main.c, 11 D:/KPI/pic32/TheConnectedMCU_Labs/nhutsenko/lab6← _RTOS/Lab10_Adv_Concurrency/src/user.c, 15	user.c, 19 hwUNLOCK_KEY_0 ConfigPerformance.c, 3
_RTOS/Lab10_Adv_Concurrency/src/main.c, 11 D:/KPI/pic32/TheConnectedMCU_Labs/nhutsenko/lab6↔ _RTOS/Lab10_Adv_Concurrency/src/user.c,	user.c, 19 hwUNLOCK_KEY_0 ConfigPerformance.c, 3
_RTOS/Lab10_Adv_Concurrency/src/main.c, 11 D:/KPI/pic32/TheConnectedMCU_Labs/nhutsenko/lab6← _RTOS/Lab10_Adv_Concurrency/src/user.c, 15	user.c, 19 hwUNLOCK_KEY_0 ConfigPerformance.c, 3
_RTOS/Lab10_Adv_Concurrency/src/main.c, 11 D:/KPI/pic32/TheConnectedMCU_Labs/nhutsenko/lab6↔ _RTOS/Lab10_Adv_Concurrency/src/user.c, 15 D:/KPI/pic32/TheConnectedMCU_Labs/nhutsenko/lab6↔	user.c, 19 hwUNLOCK_KEY_0 ConfigPerformance.c, 3 hwUNLOCK_KEY_1
_RTOS/Lab10_Adv_Concurrency/src/main.c, 11 D:/KPI/pic32/TheConnectedMCU_Labs/nhutsenko/lab6↔ _RTOS/Lab10_Adv_Concurrency/src/user.c, 15 D:/KPI/pic32/TheConnectedMCU_Labs/nhutsenko/lab6↔ _RTOS/Lab10_Adv_Concurrency/src/user.h, 20	user.c, 19 hwUNLOCK_KEY_0 ConfigPerformance.c, 3 hwUNLOCK_KEY_1
_RTOS/Lab10_Adv_Concurrency/src/main.c, 11 D:/KPI/pic32/TheConnectedMCU_Labs/nhutsenko/lab6↔ _RTOS/Lab10_Adv_Concurrency/src/user.c, 15 D:/KPI/pic32/TheConnectedMCU_Labs/nhutsenko/lab6↔ _RTOS/Lab10_Adv_Concurrency/src/user.h, 20 DelayMs	user.c, 19 hwUNLOCK_KEY_0 ConfigPerformance.c, 3 hwUNLOCK_KEY_1 ConfigPerformance.c, 3
_RTOS/Lab10_Adv_Concurrency/src/main.c, 11 D:/KPI/pic32/TheConnectedMCU_Labs/nhutsenko/lab6← _RTOS/Lab10_Adv_Concurrency/src/user.c, 15 D:/KPI/pic32/TheConnectedMCU_Labs/nhutsenko/lab6← _RTOS/Lab10_Adv_Concurrency/src/user.h, 20 DelayMs user.c, 16	user.c, 19 hwUNLOCK_KEY_0 ConfigPerformance.c, 3 hwUNLOCK_KEY_1 ConfigPerformance.c, 3 i
_RTOS/Lab10_Adv_Concurrency/src/main.c, 11 D:/KPI/pic32/TheConnectedMCU_Labs/nhutsenko/lab6↔ _RTOS/Lab10_Adv_Concurrency/src/user.c, 15 D:/KPI/pic32/TheConnectedMCU_Labs/nhutsenko/lab6↔ _RTOS/Lab10_Adv_Concurrency/src/user.h, 20 DelayMs	user.c, 19 hwUNLOCK_KEY_0
_RTOS/Lab10_Adv_Concurrency/src/main.c, 11 D:/KPI/pic32/TheConnectedMCU_Labs/nhutsenko/lab6← _RTOS/Lab10_Adv_Concurrency/src/user.c, 15 D:/KPI/pic32/TheConnectedMCU_Labs/nhutsenko/lab6← _RTOS/Lab10_Adv_Concurrency/src/user.h, 20 DelayMs user.c, 16 user.h, 22	user.c, 19 hwUNLOCK_KEY_0 ConfigPerformance.c, 3 hwUNLOCK_KEY_1 ConfigPerformance.c, 3 i user.c, 19 INCLUDE_eTaskGetState FreeRTOSConfig.h, 10
_RTOS/Lab10_Adv_Concurrency/src/main.c, 11 D:/KPI/pic32/TheConnectedMCU_Labs/nhutsenko/lab6 _RTOS/Lab10_Adv_Concurrency/src/user.c, 15 D:/KPI/pic32/TheConnectedMCU_Labs/nhutsenko/lab6 _RTOS/Lab10_Adv_Concurrency/src/user.h, 20 DelayMs user.c, 16 user.h, 22 FreeRTOSConfig.h	user.c, 19 hwUNLOCK_KEY_0
_RTOS/Lab10_Adv_Concurrency/src/main.c, 11 D:/KPI/pic32/TheConnectedMCU_Labs/nhutsenko/lab6← _RTOS/Lab10_Adv_Concurrency/src/user.c, 15 D:/KPI/pic32/TheConnectedMCU_Labs/nhutsenko/lab6← _RTOS/Lab10_Adv_Concurrency/src/user.h, 20 DelayMs user.c, 16 user.h, 22 FreeRTOSConfig.h configASSERT, 5	user.c, 19 hwUNLOCK_KEY_0
_RTOS/Lab10_Adv_Concurrency/src/main.c, 11 D:/KPI/pic32/TheConnectedMCU_Labs/nhutsenko/lab6← _RTOS/Lab10_Adv_Concurrency/src/user.c, 15 D:/KPI/pic32/TheConnectedMCU_Labs/nhutsenko/lab6← _RTOS/Lab10_Adv_Concurrency/src/user.h, 20 DelayMs user.c, 16 user.h, 22 FreeRTOSConfig.h configASSERT, 5 configCHECK_FOR_STACK_OVERFLOW, 6	user.c, 19 hwUNLOCK_KEY_0
_RTOS/Lab10_Adv_Concurrency/src/main.c, 11 D:/KPI/pic32/TheConnectedMCU_Labs/nhutsenko/lab6↔ _RTOS/Lab10_Adv_Concurrency/src/user.c, 15 D:/KPI/pic32/TheConnectedMCU_Labs/nhutsenko/lab6↔ _RTOS/Lab10_Adv_Concurrency/src/user.h, 20 DelayMs user.c, 16 user.h, 22 FreeRTOSConfig.h configASSERT, 5 configCHECK_FOR_STACK_OVERFLOW, 6 configCPU_CLOCK_HZ, 6	user.c, 19 hwUNLOCK_KEY_0
_RTOS/Lab10_Adv_Concurrency/src/main.c, 11 D:/KPI/pic32/TheConnectedMCU_Labs/nhutsenko/lab6← _RTOS/Lab10_Adv_Concurrency/src/user.c, 15 D:/KPI/pic32/TheConnectedMCU_Labs/nhutsenko/lab6← _RTOS/Lab10_Adv_Concurrency/src/user.h, 20 DelayMs user.c, 16 user.h, 22 FreeRTOSConfig.h configASSERT, 5 configCHECK_FOR_STACK_OVERFLOW, 6 configCPU_CLOCK_HZ, 6 configGENERATE_RUN_TIME_STATS, 6	user.c, 19 hwUNLOCK_KEY_0
_RTOS/Lab10_Adv_Concurrency/src/main.c, 11 D:/KPI/pic32/TheConnectedMCU_Labs/nhutsenko/lab6← _RTOS/Lab10_Adv_Concurrency/src/user.c, 15 D:/KPI/pic32/TheConnectedMCU_Labs/nhutsenko/lab6← _RTOS/Lab10_Adv_Concurrency/src/user.h, 20 DelayMs user.c, 16 user.h, 22 FreeRTOSConfig.h configASSERT, 5 configCHECK_FOR_STACK_OVERFLOW, 6 configCPU_CLOCK_HZ, 6 configGENERATE_RUN_TIME_STATS, 6 configIDLE_SHOULD_YIELD, 6	user.c, 19 hwUNLOCK_KEY_0
_RTOS/Lab10_Adv_Concurrency/src/main.c, 11 D:/KPI/pic32/TheConnectedMCU_Labs/nhutsenko/lab6← _RTOS/Lab10_Adv_Concurrency/src/user.c, 15 D:/KPI/pic32/TheConnectedMCU_Labs/nhutsenko/lab6← _RTOS/Lab10_Adv_Concurrency/src/user.h, 20 DelayMs user.c, 16 user.h, 22 FreeRTOSConfig.h configASSERT, 5 configCHECK_FOR_STACK_OVERFLOW, 6 configCPU_CLOCK_HZ, 6 configGENERATE_RUN_TIME_STATS, 6 configIDLE_SHOULD_YIELD, 6 configISR_STACK_SIZE, 6	user.c, 19 hwUNLOCK_KEY_0
_RTOS/Lab10_Adv_Concurrency/src/main.c, 11 D:/KPI/pic32/TheConnectedMCU_Labs/nhutsenko/lab6← _RTOS/Lab10_Adv_Concurrency/src/user.c, 15 D:/KPI/pic32/TheConnectedMCU_Labs/nhutsenko/lab6← _RTOS/Lab10_Adv_Concurrency/src/user.h, 20 DelayMs user.c, 16 user.h, 22 FreeRTOSConfig.h configASSERT, 5 configCHECK_FOR_STACK_OVERFLOW, 6 configCPU_CLOCK_HZ, 6 configGENERATE_RUN_TIME_STATS, 6 configIDLE_SHOULD_YIELD, 6 configISR_STACK_SIZE, 6 configKERNEL_INTERRUPT_PRIORITY, 6	user.c, 19 hwUNLOCK_KEY_0
_RTOS/Lab10_Adv_Concurrency/src/main.c, 11 D:/KPI/pic32/TheConnectedMCU_Labs/nhutsenko/lab6 _RTOS/Lab10_Adv_Concurrency/src/user.c, 15 D:/KPI/pic32/TheConnectedMCU_Labs/nhutsenko/lab6 _RTOS/Lab10_Adv_Concurrency/src/user.h, 20 DelayMs user.c, 16 user.h, 22 FreeRTOSConfig.h configASSERT, 5 configCHECK_FOR_STACK_OVERFLOW, 6 configCPU_CLOCK_HZ, 6 configGENERATE_RUN_TIME_STATS, 6 configIDLE_SHOULD_YIELD, 6 configISR_STACK_SIZE, 6 configMAX_CO_ROUTINE_PRIORITY, 6 configMAX_CO_ROUTINE_PRIORITIES, 6	user.c, 19 hwUNLOCK_KEY_0
_RTOS/Lab10_Adv_Concurrency/src/main.c, 11 D:/KPI/pic32/TheConnectedMCU_Labs/nhutsenko/lab6 _RTOS/Lab10_Adv_Concurrency/src/user.c, 15 D:/KPI/pic32/TheConnectedMCU_Labs/nhutsenko/lab6 _RTOS/Lab10_Adv_Concurrency/src/user.h, 20 DelayMs user.c, 16 user.h, 22 FreeRTOSConfig.h configASSERT, 5 configCHECK_FOR_STACK_OVERFLOW, 6 configCPU_CLOCK_HZ, 6 configGENERATE_RUN_TIME_STATS, 6 configIDLE_SHOULD_YIELD, 6 configISR_STACK_SIZE, 6 configMAX_CO_ROUTINE_PRIORITY, 6 configMAX_PRIORITIES, 7	user.c, 19 hwUNLOCK_KEY_0
_RTOS/Lab10_Adv_Concurrency/src/main.c, 11 D:/KPI/pic32/TheConnectedMCU_Labs/nhutsenko/lab6← _RTOS/Lab10_Adv_Concurrency/src/user.c, 15 D:/KPI/pic32/TheConnectedMCU_Labs/nhutsenko/lab6← _RTOS/Lab10_Adv_Concurrency/src/user.h, 20 DelayMs user.c, 16 user.h, 22 FreeRTOSConfig.h configASSERT, 5 configCHECK_FOR_STACK_OVERFLOW, 6 configCPU_CLOCK_HZ, 6 configGENERATE_RUN_TIME_STATS, 6 configIDLE_SHOULD_YIELD, 6 configISR_STACK_SIZE, 6 configKERNEL_INTERRUPT_PRIORITY, 6 configMAX_CO_ROUTINE_PRIORITIES, 6 configMAX_PRIORITIES, 7 configMAX_SYSCALL_INTERRUPT_PRIORITY, 7	user.c, 19 hwUNLOCK_KEY_0
_RTOS/Lab10_Adv_Concurrency/src/main.c, 11 D:/KPI/pic32/TheConnectedMCU_Labs/nhutsenko/lab6← _RTOS/Lab10_Adv_Concurrency/src/user.c, 15 D:/KPI/pic32/TheConnectedMCU_Labs/nhutsenko/lab6← _RTOS/Lab10_Adv_Concurrency/src/user.h, 20 DelayMs user.c, 16 user.h, 22 FreeRTOSConfig.h configCHECK_FOR_STACK_OVERFLOW, 6 configCPU_CLOCK_HZ, 6 configGENERATE_RUN_TIME_STATS, 6 configIDLE_SHOULD_YIELD, 6 configISR_STACK_SIZE, 6 configMAX_CO_ROUTINE_PRIORITY, 6 configMAX_PRIORITIES, 7 configMAX_SYSCALL_INTERRUPT_PRIORITY, 7 configMAX_TASK_NAME_LEN, 7	user.c, 19 hwUNLOCK_KEY_0
_RTOS/Lab10_Adv_Concurrency/src/main.c, 11 D:/KPI/pic32/TheConnectedMCU_Labs/nhutsenko/lab6← _RTOS/Lab10_Adv_Concurrency/src/user.c, 15 D:/KPI/pic32/TheConnectedMCU_Labs/nhutsenko/lab6← _RTOS/Lab10_Adv_Concurrency/src/user.h, 20 DelayMs user.c, 16 user.h, 22 FreeRTOSConfig.h configCHECK_FOR_STACK_OVERFLOW, 6 configCPU_CLOCK_HZ, 6 configCPU_CLOCK_HZ, 6 configIDLE_SHOULD_YIELD, 6 configISR_STACK_SIZE, 6 configKERNEL_INTERRUPT_PRIORITY, 6 configMAX_CO_ROUTINE_PRIORITIES, 6 configMAX_PRIORITIES, 7 configMAX_SYSCALL_INTERRUPT_PRIORITY, 7 configMAX_TASK_NAME_LEN, 7 configMINIMAL_STACK_SIZE, 7	user.c, 19 hwUNLOCK_KEY_0
_RTOS/Lab10_Adv_Concurrency/src/main.c, 11 D:/KPI/pic32/TheConnectedMCU_Labs/nhutsenko/lab6← _RTOS/Lab10_Adv_Concurrency/src/user.c, 15 D:/KPI/pic32/TheConnectedMCU_Labs/nhutsenko/lab6← _RTOS/Lab10_Adv_Concurrency/src/user.h, 20 DelayMs user.c, 16 user.h, 22 FreeRTOSConfig.h configASSERT, 5 configCHECK_FOR_STACK_OVERFLOW, 6 configCPU_CLOCK_HZ, 6 configGENERATE_RUN_TIME_STATS, 6 configIDLE_SHOULD_YIELD, 6 configISR_STACK_SIZE, 6 configMAX_CO_ROUTINE_PRIORITY, 6 configMAX_PRIORITIES, 7 configMAX_TASK_NAME_LEN, 7 configMINIMAL_STACK_SIZE, 7 configPERIPHERAL_CLOCK_HZ, 7	user.c, 19 hwUNLOCK_KEY_0
_RTOS/Lab10_Adv_Concurrency/src/main.c, 11 D:/KPI/pic32/TheConnectedMCU_Labs/nhutsenko/lab6← _RTOS/Lab10_Adv_Concurrency/src/user.c, 15 D:/KPI/pic32/TheConnectedMCU_Labs/nhutsenko/lab6← _RTOS/Lab10_Adv_Concurrency/src/user.h, 20 DelayMs user.c, 16 user.h, 22 FreeRTOSConfig.h configCHECK_FOR_STACK_OVERFLOW, 6 configCPU_CLOCK_HZ, 6 configCPU_CLOCK_HZ, 6 configIDLE_SHOULD_YIELD, 6 configISR_STACK_SIZE, 6 configKERNEL_INTERRUPT_PRIORITY, 6 configMAX_CO_ROUTINE_PRIORITIES, 6 configMAX_PRIORITIES, 7 configMAX_SYSCALL_INTERRUPT_PRIORITY, 7 configMAX_TASK_NAME_LEN, 7 configMINIMAL_STACK_SIZE, 7	user.c, 19 hwUNLOCK_KEY_0
_RTOS/Lab10_Adv_Concurrency/src/main.c, 11 D:/KPI/pic32/TheConnectedMCU_Labs/nhutsenko/lab6← _RTOS/Lab10_Adv_Concurrency/src/user.c, 15 D:/KPI/pic32/TheConnectedMCU_Labs/nhutsenko/lab6← _RTOS/Lab10_Adv_Concurrency/src/user.h, 20 DelayMs user.c, 16 user.h, 22 FreeRTOSConfig.h configASSERT, 5 configCHECK_FOR_STACK_OVERFLOW, 6 configCPU_CLOCK_HZ, 6 configGENERATE_RUN_TIME_STATS, 6 configIDLE_SHOULD_YIELD, 6 configISR_STACK_SIZE, 6 configMAX_CO_ROUTINE_PRIORITY, 6 configMAX_PRIORITIES, 7 configMAX_TASK_NAME_LEN, 7 configMINIMAL_STACK_SIZE, 7 configPERIPHERAL_CLOCK_HZ, 7	user.c, 19 hwUNLOCK_KEY_0

INDEX 27

FreeRTOSConfig.h, 11	gameEnd, 19
InitApp	i, 19
user.c, 17	InitApp, 17
user.h, 23	InitGPIO, 17
InitGPIO	j, 19
user.c, 17	my_mass, 19
	NELEMS, 16
j	num, 19
user.c, 19	random_mass, 19
	SetNum, 17
main	Task1, 18
main.c, 12	Task2, 18
main.c	user.h
_general_exception_handler, 12	BIOS_BTN1_PORT_BIT, 20
main, 12	BIOS BTN2 PORT BIT, 21
vApplicationStackOverflowHook, 12	BIOS BTN3 PORT BIT, 21
vApplicationTickHook, 12	BIOS BTN4 PORT BIT, 21
vAssertCalled, 12	BIOS LD1 PORT BIT, 21
my_mass	BIOS LD2 PORT BIT, 21
user.c, 19	BIOS LD3 PORT BIT, 21
NELENO.	BIOS LD4 PORT BIT, 21
NELEMS	BIOS LD5 PORT BIT, 21
user.c, 16	BIOS LD6 PORT BIT, 22
num	BIOS LD7 PORT BIT, 22
user.c, 19	BIOS LD8 PORT BIT, 22
randam mass	BTN1 PORT BIT, 22
random_mass	BTN2 PORT BIT, 22
user.c, 19	BTN3 PORT BIT, 22
SetNum	DelayMs, 22
user.c, 17	InitApp, 23
user.c, 17	Task1, 23
Task1	Task2, 23
user.c, 18	xMutex, 23
user.h, 23	ANICOX, 20
Task2	vApplicationStackOverflowHook
user.c, 18	main.c, 12
user.h, 23	vApplicationTickHook
333, 23	main.c, 12
UART.c	vAssertCalled
UART4_init, 13	FreeRTOSConfig.h, 11
UART4_putc, 13	main.c, 12
UART4 puts, 13	vHardwareConfigurePerformance
UART.h	ConfigPerformance.c, 4
UART4 init, 14	ConfigPerformance.h, 4
UART4_putc, 14	vHardwareUseMultiVectoredInterrupts
UART4_puts, 15	ConfigPerformance.c, 4
UART4 init	ConfigPerformance.h, 4
UART.c, 13	
UART.h, 14	xMutex
UART4_putc	user.h, 23
UART.c, 13	
UART.h, 14	
UART4_puts	
UART.c, 13	
UART.h, 15	
user.c	
bulls, 18	
cows, 18	
DelayMs, 16	
-·-·y···, · -	