#### Lexical Analysis

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Topic: Scanning

Section:

Lancing Const.

Specifying Scanners

Constructing DFAs

Tokenizing the Inpu

Representing DFA

Minimizing DFA

#### **Outline**

- Introduction
- Specifying scanners
- Tokenizing input using DFAs
- Constructing DFAs
- Representing DFAs using four-arrays
- Minimizing DFAs



Topic:

Scanning

Section: Introduction

Specifying Scanners

Constructing DFAs

Tokenizing the Input

Representing DFAs

Minimizing DFAs



Topic:

Scanning Section:

Introduction

Specifying Scanners

Constructing DFAs

Tokenizing the Input

Representing DFAs

Minimizing DFAs

# Introduction



Topic:

Scanning

Section: Introduction

Specifying Scanners

Constructing DFAs

Tokenizing the Input

Representing DFAs

Minimizing DFAs

#### Introduction

Prof. Sanyal's slides (scanning-slides-sanyal-part1.pdf)



Topic:

Scanning

Section:

Introduction

Specifying Scanners

Constructing DFAs

Tokenizing the Input

Representing DFAs

Minimizing DFAs



Topic:

Scanning Section:

Specifying Scanners

Constructing DFAs

Tokenizing the Input

Representing DFAs

Minimizing DFAs

# Specifying Scanners



Topic: Scanning

Section:

Introduction

Specifying Scanners

Constructing DFAs

Tokenizing the Input

Representing DFAs

Minimizing DFAs

#### Introduction

Prof. Sanyal's slides (scanning-slides-sanyal-part2.pdf)



Topic:

Scanning

Section:

Specifying Scanners

Constructing DFAs

Tokenizing the Input

Representing DFAs

Minimizing DFAs



Topic: Scanning

Section:

10000

Specifying Scanners

Constructing DFAs

Tokenizing the Input

Representing DFAs

Minimizing DFAs

# Constructing DFAs



Topic: Scanning

Section:

Introduction

Constructing DFAs

Representing DFAs

Minimizing DFA

#### **Constructing DFA for Multiple Patterns**

- Join multiple DFAs/NFAs using  $\epsilon$  transition Transition without consuming any input symbol
- This creates an NFA (Non-deterministic Finite Automaton)
  - Possible transition without consuming any input symbol
  - Possibly multiple transitions on the same input symbol
- Make the NFA deterministic by subset construction
  - Each state in the resulting DFA is a set of "similar" states of the NFA
  - The start state of the DFA is a union of all original start states (of multiple patterns)
  - Subsequent states are identified by finding out the sets of states of the NFA for each possible input symbol



Topic: Scanning

Section:

Specifying Scanners

Constructing DFAs

Tokenizing the Input Representing DFAs Minimizing DFAs

#### **Constructing NFA for a Regular Expression**

Consider a regular expression R. Apply steps 1 to 4 to construct an NFA for R inductively:

- 1. If R is a letter in the alphabet  $\Sigma$ , create a two state NFA that accepts the letter (single transition from the start state to a single final state on the letter)
- 2. If R is  $R_1 \cdot R_2$ , create an NFA by joining the two NFAs  $N_1$  and  $N_2$  by adding an epsilon transition from every final state of  $N_1$  to the start state of  $N_2$ .
- 3. If the R is  $R_1 \mid R_2$ , create an NFA by joining the two NFAs  $N_1$  and  $N_2$  by creating a new start state  $s_0$  and a new final state  $s_f$ . Add an epsilon transition from  $s_0$  the start state of  $R_1$  and similarly for  $R_2$ . Add an epsilon transition from every final state of  $N_1$  to  $s_f$  and similarly for  $N_2$ .
- 4. If R is  $R_1^*$ , create an NFA by adding an epslion transition from every final state of  $R_1$  to the start state of  $R_1$

Alternatively, we can create a new start state  $s_0$  with an epsilon transition to the start state of  $R_1$  and a new final state  $s_f$  with epsilon transitions from the final states of  $R_1$ , and then add an epsilon transition from  $s_f$  to  $s_0$ .



Topic:

Scanning

Section:

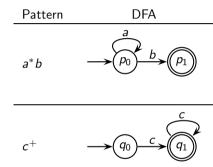
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Constructing DFAs

Tokenizing the Inpu

Representing DFAs

Minimizing DFA





Topic:

Scanning

Section:

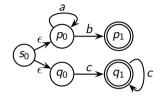
Specifying Scanners

Constructing DFAs

Tokenizing the Inp

Representing DFAs

Minimizing DFAs



State	Transition				
State	а	b	С		



Topic:

Scanning

Section:

Specifying Scanners

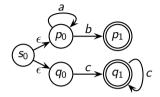
Constructing DFAs

Tokenizing the Inp

Representing DFAs

Minimizing DFAs

# **Constructing DFA for Multiple Patterns: Example 1**



State	Transition				
State	а	Ь	С		
$\{s_0, p_0, q_0\}$					

 $\{s_0,p_0,q_0\}$ 



Topic:

Scanning

Section:

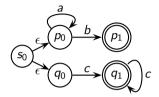
Specifying Scanners

Constructing DFAs

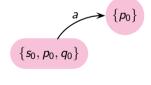
Tokenizing the Inpu

Representing DFAs

Minimizing DFAs



State	Transition			
State	а	Ь	С	
$\{s_0, p_0, q_0\}$	$\{p_0\}$			
$\{p_0\}$				





Topic:

Scanning

Section:

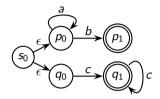
Specifying Scanners

Constructing DFAs

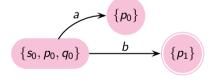
Tokenizing the Inn

Representing DFAs

Minimizing DFA



State	Transition			
State	а	Ь	С	
$\{s_0, p_0, q_0\}$	$\{p_0\}$	$\{p_1\}$		
$\{p_0\}$				
$\{p_1\}$				





Topic:

Scanning

Section:

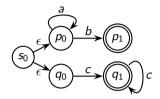
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Constructing DFAs

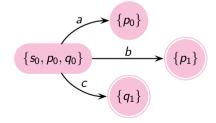
Tokenizing the Inc

Representing DFAs

Minimizing DFA



State	Transition			
State	а	Ь	С	
$\{s_0, p_0, q_0\}$	$\{p_0\}$ $\{p_1\}$ $\{q_2\}$			
$\{p_0\}$				
$\{p_1\}$				
$\{q_1\}$				





Topic:

Scanning

Section:

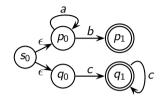
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Constructing DFAs

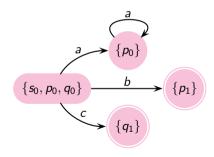
Tokenizing the In

Representing DFAs

Minimizing DFA



State	Transition			
State	а	Ь	С	
$\{s_0, p_0, q_0\}$	$\{p_0\}$	$\{p_1\}$	$\{q_1\}$	
$\{p_0\}$	$\{p_0\}$			
$\{p_1\}$				
$\{q_1\}$				





Topic:

Scanning Section:

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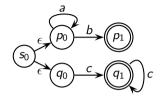
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Constructing DFAs

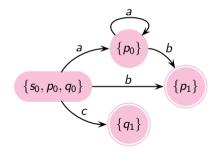
Tokenizing the Inp

Representing DFAs

Minimizing DFAs



State	Transition				
State	а	Ь	С		
$\{s_0, p_0, q_0\}$	$\{p_0\}$	$\{p_0\}$ $\{p_1\}$			
$\{p_0\}$	$\{p_0\}$	$\{p_1\}$			
$\{p_1\}$					
$\{q_1\}$					





Topic:

Scanning

Section:

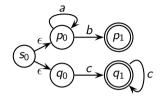
Specifying Scanners

Constructing DFAs

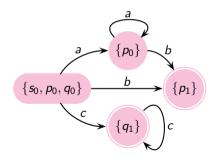
Tokenizing the Inpu

Representing DFAs

Minimizing DFA



State	Transition			
State	а	Ь	С	
$\{s_0, p_0, q_0\}$	$\{p_0\}$	$\{q_1\}$		
$\{p_0\}$	$\{p_0\}$	$\{p_1\}$		
$\{p_1\}$				
$\{q_1\}$			$\{q_1\}$	





Topic:

Scanning

Section:

Specifying Scanners

Constructing DFAs

Tokenizing the Inpu

Representing DFA

Minimizing DFA

#### **Constructing DFA for Multiple Patterns: Example 2**

Let L and D denote the set of all letters and digits, respectively

Pattern	Token
int	INT
L(L D)*	ID
$D^+$	NUM
=	=
;	;

For convenience, we will ignore the last two patterns that are completely independent



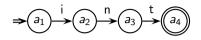
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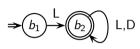
Section:

Specifying Scanners

Constructing DFAs

Representing DFAs





$$\rightarrow$$
  $C_1$   $D$   $C_2$   $D$ 

State	i	n	t	$L-\{i, n, t\}$	D



Topic:

Scanning

Section:

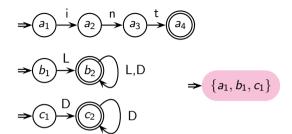
Specifying Scanners

Constructing DFAs

lokenizing the In

Representing DFAs

Minimizing DFAs



State	i	n	t	$L-\{i,n,t\}$	D
$\{a_1,b_1,c_1\}$					



Topic: Scanning

Section:

Introduction

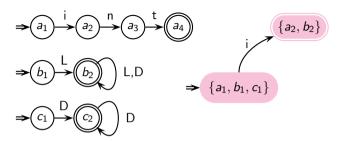
Specifying Scanners

Constructing DFAs

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Representing DFAs

Minimizing DFAs



State	i	n	t	$L-\{i,n,t\}$	D
$\{a_1, b_1, c_1\}$ $\{a_2, b_2\}$	$\{a_2,b_2\}$				
$\{a_2,b_2\}$					



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Section:

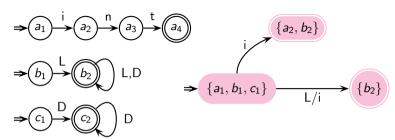
Specifying Scanners

Constructing DFAs

Tokenizing the Inp

Representing DFAs

Minimizing DFAs



State	i	n	t	$L-\{i,n,t\}$	D
$\{a_1, b_1, c_1\}$ $\{a_2, b_2\}$	$\{a_2,b_2\}$	{ b <sub>2</sub> }	{ b <sub>2</sub> }	$\{b_2\}$	
$\{a_2,b_2\}$					
$\{b_2\}$					



Topic:

Section:

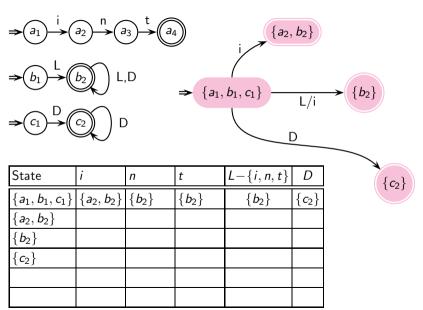
Specifying Scanners

Constructing DFAs

Tokenizing the Inpi

Representing DFAs

Minimizing DFAs





Topic:

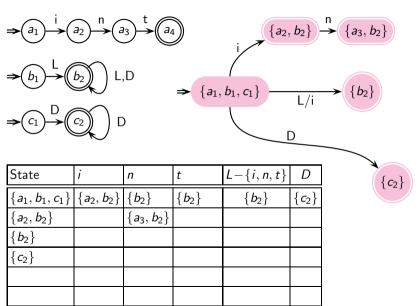
Section:

Specifying Scanners

Constructing DFAs

Tokenizing the Inpu

Representing DFAs





Topic: Scanning

Section:

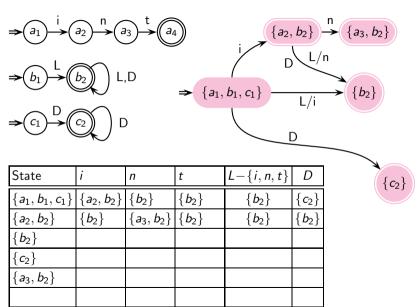
Specifying Scanners

Constructing DFAs

Tokenizing the Inpu

Representing DFAs

Minimizing DFAs





Topic:

Section:

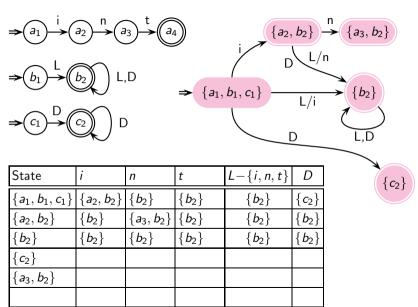
Specifying Scanners

Constructing DFAs

Tokenizing the Inpi

Representing DFAs

Minimizing DFAs





Topic: Scanning

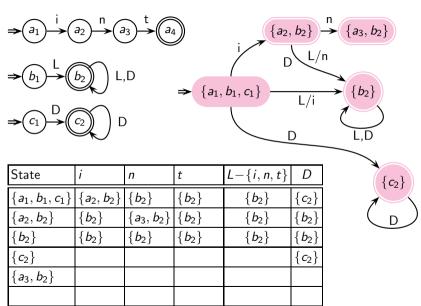
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Specifying Scanners

Constructing DFAs

Tokenizing the Inpi

Minimizing DFAs





Topic:

Section:

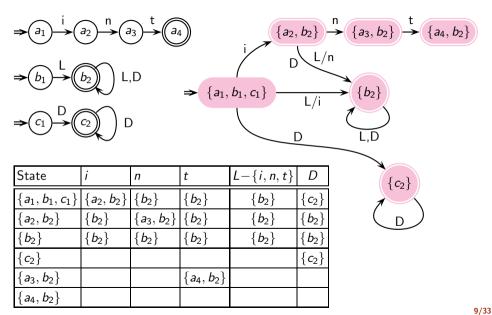
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Constructing DFAs

Tokemizing the inpu

Representing DFAs

Minimizing DFAs





Topic:

Section:

Specifying Scanner

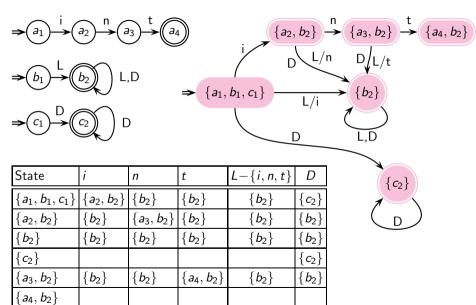
Constructing DFAs

Tokenizing the Inpu

Representing DFAs

Minimizing DFAs

#### **Constructing DFA for Multiple Patterns: Example 2**



9/33



Topic:

Section:

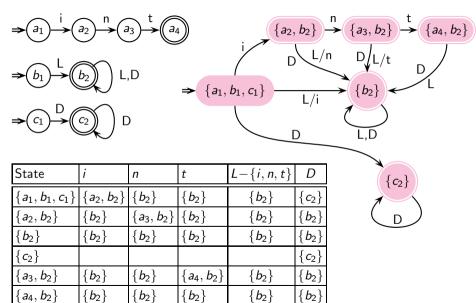
Specifying Scanner

 $Constructing\ DFAs$ 

Tokenizing the inpu

Representing DFAs

Minimizing DFAs





Topic:

Section:

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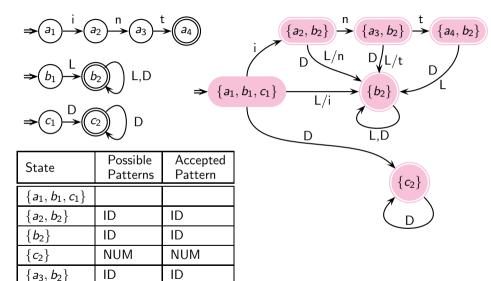
Constructing DFAs

Tokenizing the In

Representing DFAs

Minimizing DFAs

#### **Constructing DFA for Multiple Patterns: Example 2**



INT. ID

 $\{a_4, b_2\}$ 

INT



Topic: Scanning

Section

Introduction

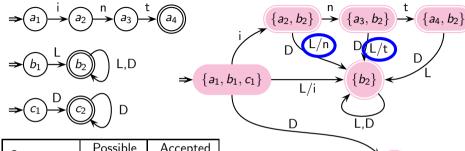
Constructing DFAs

Tokenizing the Inpu

Representing DFAs

Minimizing DFAs

#### **Constructing DFA for Multiple Patterns: Example 2**



State	Possible Patterns	Accepted Pattern	
$\{a_1,b_1,c_1\}$			
$\{a_2,b_2\}$	ID	ID	
$\{b_2\}$	ID	ID	
$\{c_2\}$	NUM	NUM	
$\{a_3,b_2\}$	ID	ID	
$\{a_4, b_2\}$	INT, ID	INT	

Longest match. Lexeme "int" reaches state  $\{a_4, b_2\}$  whereas lexeme "integer" reaches the state  $\{b_2\}$ 

First matching rule preferred. Transitions L/n and L/t to state  $\{b_2\}$  ensure that INT is preferred over ID for the lexeme "int"



Topic:

Scanning

Section:

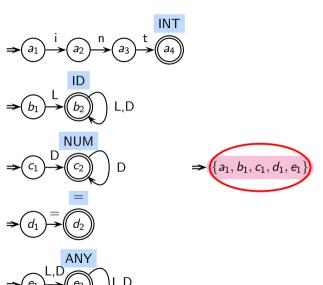
Specifying Scanners

Constructing DFAs

Tokenizing the inpu

Representing DFAs

Minimizing DFAs





Topic:

Scanning

Section:

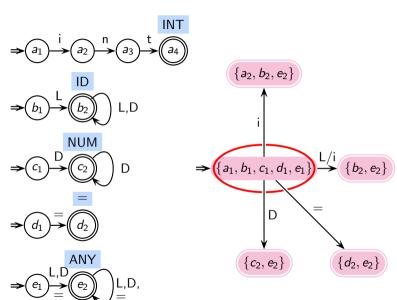
Specifying Scanners

Constructing DFAs

Tokenizing the I

Representing DFAs

Minimizing DFAs





Topic:

Scanning

Section:

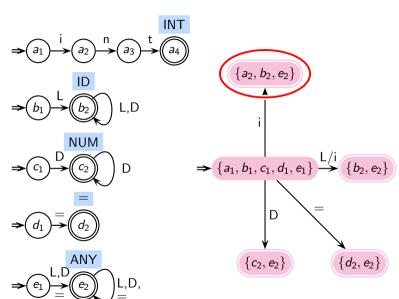
Specifying Scanners

Constructing DFAs

Tokenizing the In

Representing DFAs

Minimizing DFAs





Topic:

Scanning

Section:

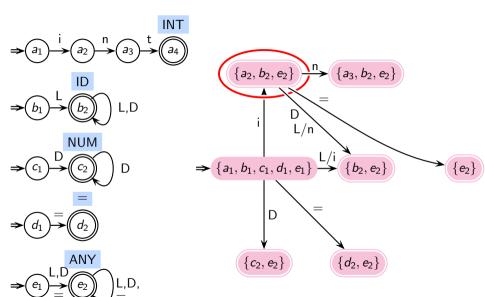
Specifying Scanners

Constructing DFAs

Tokenizing the Input

Representing DFAs

Minimizing DFAs





Topic:

Scanning

Section:

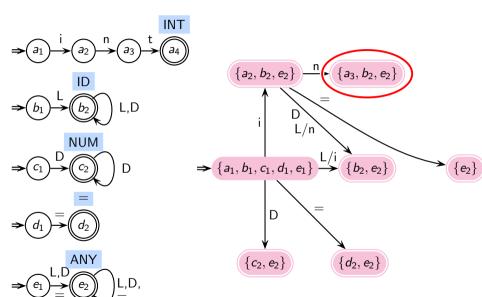
Specifying Scanners

Constructing DFAs

Tokenizing the Input

Representing DFAs

Minimizing DFA





Topic:

Scanning

Section:

Introduction

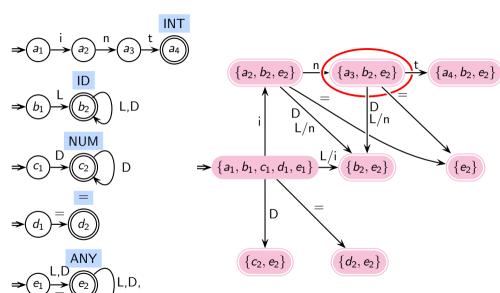
Specifying Scanners

Constructing DFAs

Tokenizing the Input

Representing DFAs

Minimizing DFAs





Topic:

Scanning

Section:

Introductio

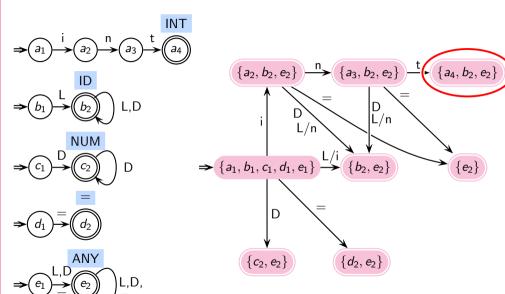
Specifying Scanners

Constructing DFAs

Tokenizing the Input

Representing DFAs

Minimizing DFAs





Topic:

Scanning

Section:

Introductio

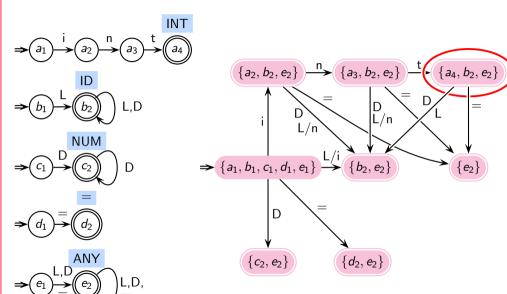
Specifying Scanners

Constructing DFAs

Tokenizing the Input

Representing DFAs

Minimizing DFAs





Topic:

Scanning

Section:

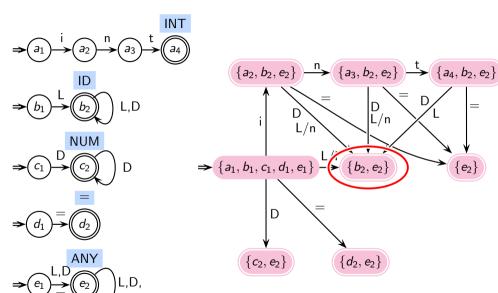
Specifying Scanners

Constructing DFAs

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Representing DFAs

Minimizing DFAs





Topic:

Scanning

Section:

Introductio

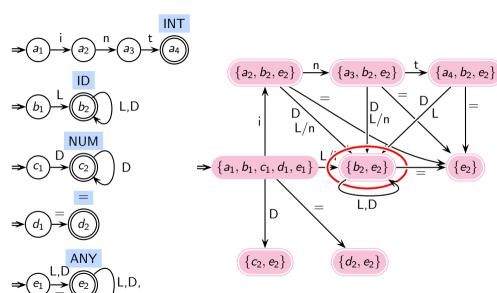
Specifying Scanners

Constructing DFAs

Tokenizing the Input

Representing DFAs

Minimizing DFA





Topic:

Scanning

Section:

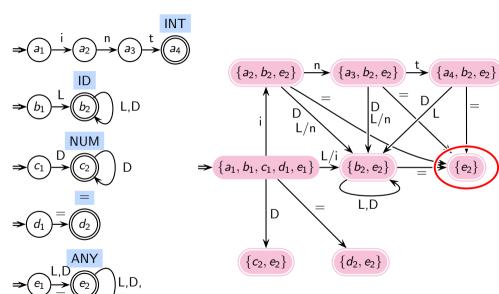
Introduction

Specifying Scanners
Constructing DFAs

Constituting DIA

Representing DFAs

Minimizing DFA





Topic:

Scanning

Section:

Introduction

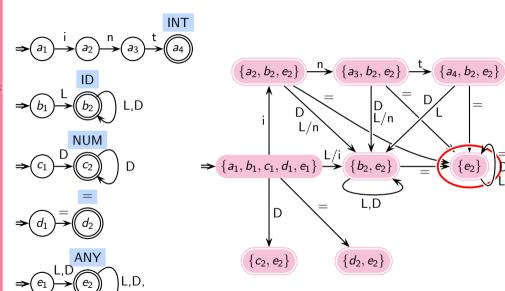
Specifying Scanners

Constructing DFAs

Tokenizing the Input

Representing DFAs

Minimizing DFAs





Topic:

Scanning

Section:

Introductio

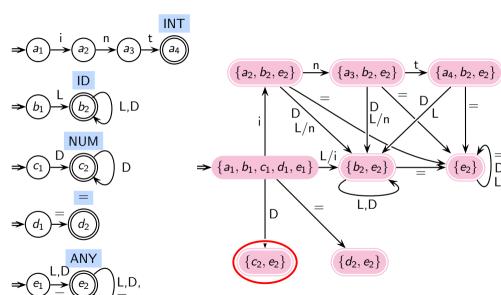
Specifying Scanners

Constructing DFAs

Tokenizing the Input

Representing DFAs

Minimizing DFAs





Topic:

Scanning

Section:

Introduction

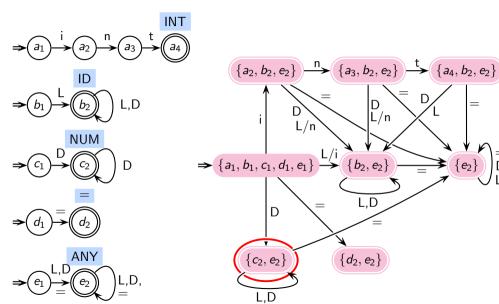
Specifying Scanners

Constructing DFAs

Tokenizing the Inpu

Representing DFAs

Minimizing DFAs





Topic:

Scanning

Section:

Introduction

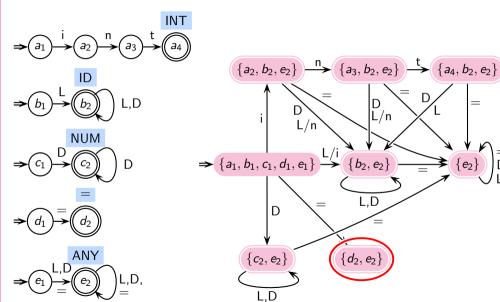
Specifying Scanners
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Representing DFAs

Minimizing DFAs





Topic:

Scanning

Section:

Introduction

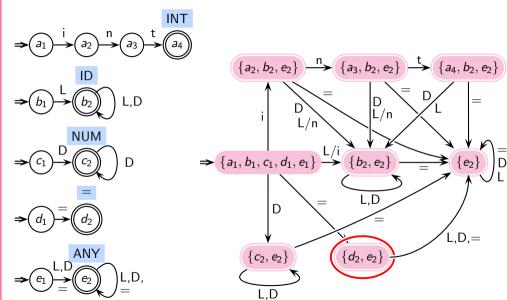
Specifying Scanners

Constructing DFAs

Tokenizing the Inpu

Representing DFAs

Minimizing DFAs





Topic:

Scanning

Section:

Introduction

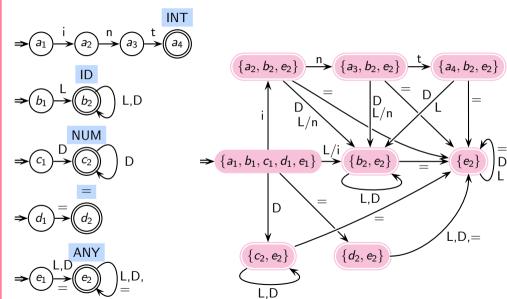
Specifying Scanners

Constructing DFAs

Tokenizing the Input

Representing DFAs

Minimizing DFAs





Topic:

Scanning

Section:

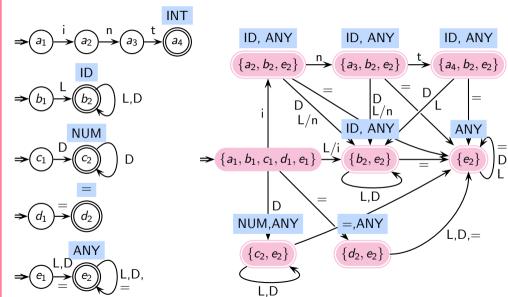
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Representing DFAs

Minimizing DFAs





Topic:

Scanning

Section:

Introduction

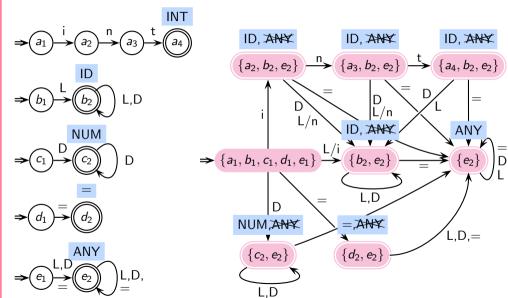
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Representing DFAs

Minimizing DFAs





Topic: Scanning

Section:

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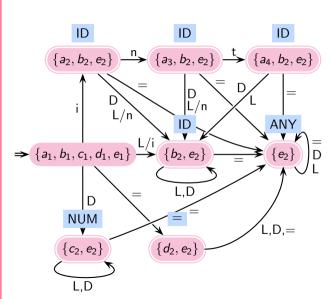
Specifying Scanners

Constructing DFAs

Tokenizing the Inp

Representing DFAs

Minimizing DFAs





Topic: Scanning

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Section:

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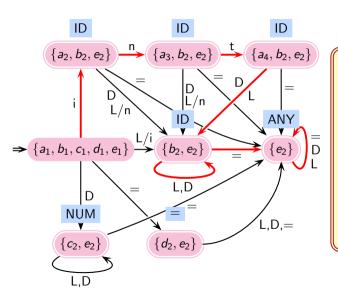
Constructing DFAs

Tokenizing the Input

Representing DFAs

Minimizing DFA

#### **Constructing DFA for Multiple Patterns: Example 3**



Input int12=3 reaches state  $\{e_2\}$  along the red transitions, recognizing the token ANY

Hence the ".\*" pattern should be used with caution in a lex script

The "." pattern is much safer in a lex script



Topic: Scanning

Section:

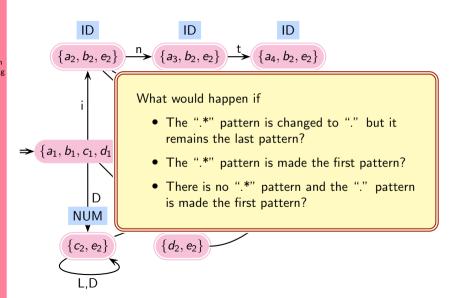
Specifying Scanners

Constructing DFAs

Tokenizing the In

Representing DFA

Minimizing DFAs





Topic:

Scanning

Section:

Specifying Scanners

Constructing DFAs

Tokenizing the Input

Representing DFAs

Minimizing DFAs



Topic: Scanning

Section:

Introduction

Specifying Scanners

Constructing DFAs

Tokenizing the Input

Representing DFAs

Minimizing DFAs

# Tokenizing the Input Using DFAs



Topic:

Scanning Section:

. . . . . . .

Specifying Scanners

Constructing DFAs

Tokenizing the Input

Representing DFAs

Minimizing DFA

#### An Example for Scanning: Specifications

Let L and D denote the set of all letters and digits, respectively

Pattern	Token		
int	INT		
L(L D)*	ID		
$D^+$	NUM		
=	=		
;	;		

We will scan the input string int int32=5;←



Topic:

Scanning Section:

Industrial

Specifying Scanners

Constructing DFAs

Tokenizing the Input

Representing DFA

Minimizing DFA

# **Example for Scanning: DFA for the Patterns**

Formally, a Deterministic Finite Automaton (DFA) is a five tuple

$$(\Sigma, S, s_0, \delta, F)$$

#### where

- $\bullet$   $\Sigma$  is the input alphabet
- *S* is the set of states
- $s_o \in S$  is a unique start state
- $\delta: S \times \Sigma \to S$  is a transition function
- $F \subseteq S$  is a set of final states



Topic: Scanning

Section:

Specifying Scanners

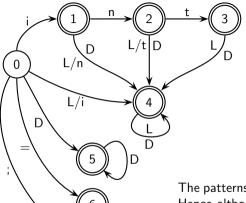
Constructing DEAs

Tokenizing the Input

Representing DFA

Minimizing DFA

# **Example for Scanning: DFA for the Patterns**



States	Action
3	Found INT
1, 2, 4	Found ID
5	Found NUM
6	Found =
7	Found :

The patterns for INT precedes the pattern for ID Hence although state 3 could accept both INT and ID, it is made to accept only INT



Topic: Scanning

Section:

Introduction

Specifying Scanners

Constructing DFAs

Tokenizing the Input

Representing DFA

Minimizing DFAs

#### A Format to Show A Trace of Scanning

Ste	ep No	State	${\sf MatchedString}$	Buffer	NextChar	LastFinalState	${\sf MarkedPos}$	Action

- State (S). Current State
- MatchedString (MS). Prefix of the buffer matched to identify a lexeme
- Buffer.
- NextChar (NC). The next character in the input; it will be shifted to the buffer if there is a valid transition in the DFA
- LastFinalState (LFS). The last final state seen
- MarkedPos (MP). The position of the character (in the buffer) just after the last seen lexeme
- Action.



Topic: Scanning

Section

Tokenizing the Input

#### A Format to Show A Trace of Scanning

Step No | State | MatchedString | Buffer | NextChar | LastFinalState | MarkedPos | Action

- State (S). Current State
- M When there is no transition on Nextchar,
- В • if MarkedPos is -1, no final state is seen, the first character in
- the buffer is discarded, and the second character becomes NextChar.
  - otherwise, the lexeme upto MarkedPos (excluding it) is returned, the character at MarkedPos becomes NextChar
  - In either case, the LastFinalState is set to -1 and the state is set to 0

se

Action



Topic: Scanning

Section:

. . . . . .

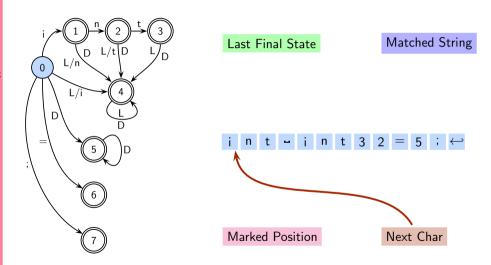
Specifying Scanners

Constructing DFAs

Tokenizing the Input

Representing DFA

Minimizing DFA





Topic: Scanning

Section:

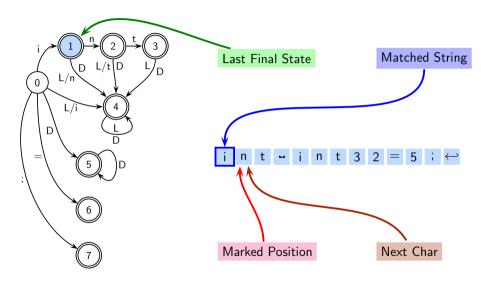
Specifying Scanners

Constructing DFAs

Tokenizing the Input

Representing DFAs

Minimizing DFAs





Topic: Scanning

Section:

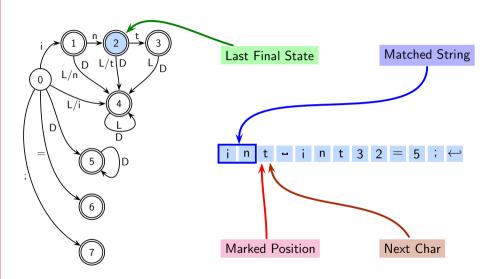
Introduction

Specifying Scanners
Constructing DFAs

Tokenizing the Input

Representing DFA

Minimizing DFAs





Topic: Scanning

Section:

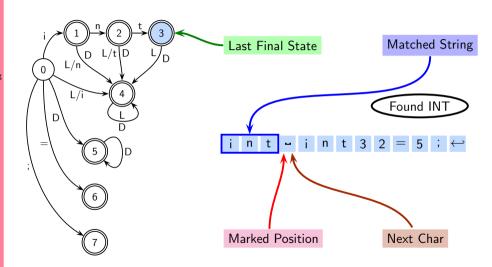
Specifying Scanners

Constructing DFAs

Tokenizing the Input

Representing DFAs

Minimizing DFAs





Topic: Scanning

Section:

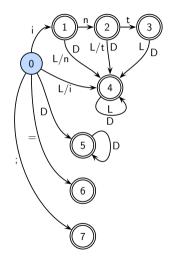
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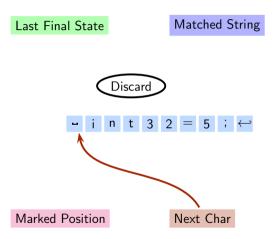
Constructing DFAs

Tokenizing the Input

Representing DFAs

Minimizing DFA







Topic: Scanning

Section:

Introduction

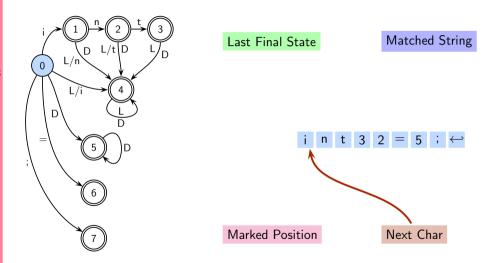
Specifying Scanners

Constructing DFAs

Tokenizing the Input

Representing DFA

Minimizing DFAs





Topic: Scanning

Section:

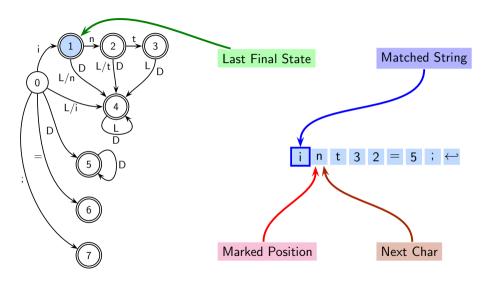
Specifying Scanners

Constructing DFAs

Tokenizing the Input

Representing DFAs

Minimizing DFAs





Topic: Scanning

Section:

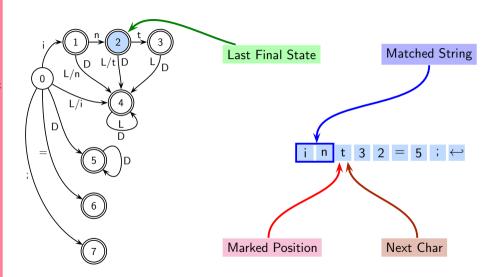
Specifying Scanners

Constructing DFAs

Tokenizing the Input

Representing DFA:

Minimizing DFAs





Topic: Scanning

Section:

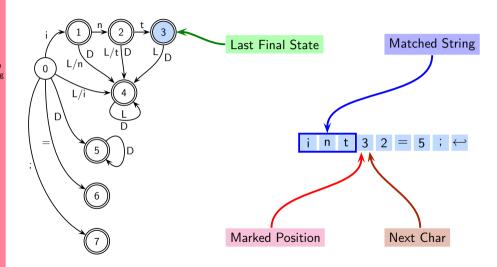
Introduction

Specifying Scanners
Constructing DFAs

Tokenizing the Input

Representing DFAs

Minimizing DFAs





Topic: Scanning

Section:

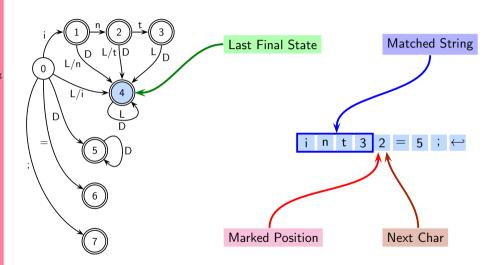
Specifying Scanners

Constructing DFAs

Tokenizing the Input

Representing DFAs

Minimizing DFAs





Topic: Scanning

Section:

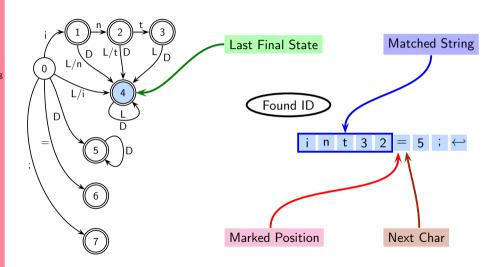
Specifying Scanners

Constructing DFAs

Tokenizing the Input

Representing DFAs

Minimizing DFAs





Topic: Scanning

Section:

Introduction

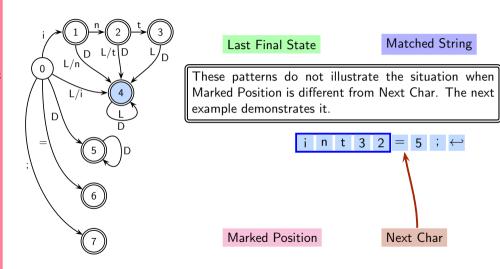
Specifying Scanners

Constructing DFAs

Tokenizing the Input

Representing DFAs

Minimizing DFA





Topic: Scanning

Section:

Introduction

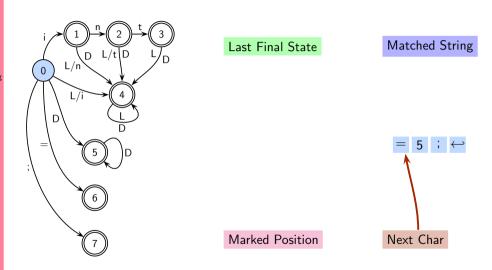
Specifying Scanners

Constructing DFAs

Tokenizing the Input

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Minimining DEA





Topic: Scanning

Section:

Introduction

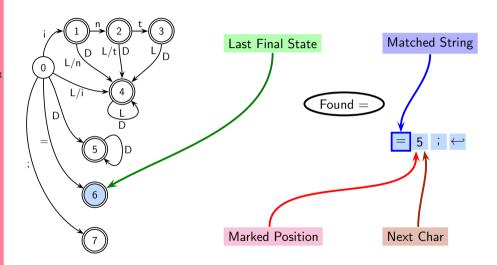
Specifying Scanners

Constructing DFAs

Tokenizing the Input

Representing DFAs

Minimizing DFAs





Topic: Scanning

Section:

Industrial continu

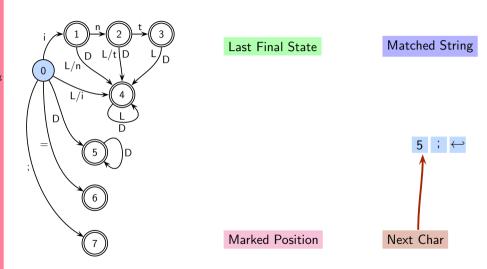
Specifying Scanners

Constructing DFAs

Tokenizing the Input

Representing DFAs

Minimizing DFA





Topic: Scanning

Section:

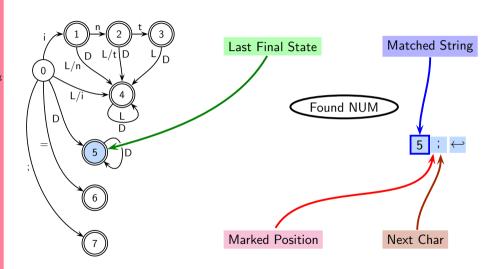
Specifying Scanners

Constructing DFAs

Tokenizing the Input

Representing DFA:

Minimizing DFAs





Topic: Scanning

Section:

Introduction

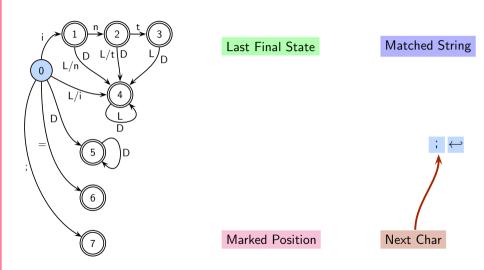
Specifying Scanners

Constructing DFAs

Tokenizing the Input

Representing DFAs

Minimizing DFA





Topic: Scanning

Section:

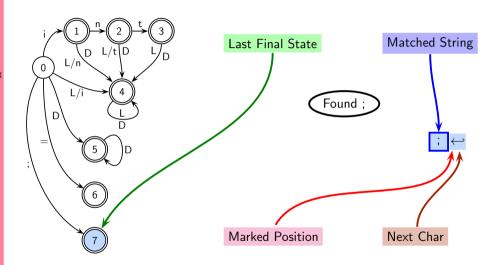
Specifying Scanners

Constructing DFAs

Tokenizing the Input

Representing DFA

Minimizing DFAs





Topic: Scanning

Section:

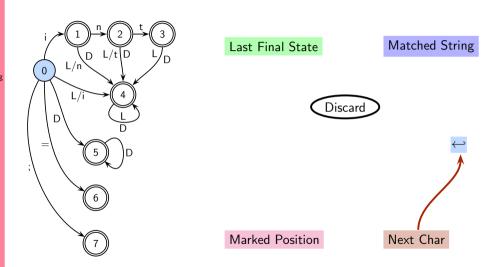
Specifying Scanners

Constructing DFAs

Tokenizing the Input

Representing DFA

Minimizing DFA





Topic: Scanning

Section:

Introduction

Specifying Scanners

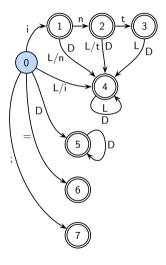
Constructing DFAs

Tokenizing the Input

Representing DFAs

Minimizing DFA

## Scanning the Input "int int32=5;←"



Last Final State

Matched String

Marked Position

Next Char



Topic: Scanning

Section:

Introduction

Specifying Scanners

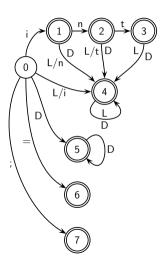
Constructing DFAs

Tokenizing the Input

Representing DFAs

Minimizing DFAs

# The Trace of Scanning the Input "int int32=5; ←"



SNo	S	MS	Buffer	NC	LFS	MP	Action
1	0		int_int32=5;←	i			
2	1	i	int∟int32=5;←	n	1	1	
3	2	in	int_int32=5;←	t	2	2	
4	3	int	int∟int32=5;←	1	3	3	Found INT
5	0		∟int32=5;←	1			Discard 🗅
6	0		int32=5;←	-			
7	1	i	int32=5;←	n	1	1	
8	2	in	int32=5;←	t	2	2	
9	3	int	int32=5;←	3	3	3	
10	4	int3	int32=5;←	2	4	4	
11	4	int32	,	=	4	5	Found ID
12	0		=5;←	=			
13	6	=	=5;←	5	6	1	Found =
14	0		5;←	5			
15	5	5	5;←	;	5	1	Found NUM
16	0		;←	;			
17	7	;	$\leftarrow$	$\rightarrow$	7		Found ;



Topic: Scanning

Section

Introduction

Specifying Scanners

Constructing DFAs

Tokenizing the Input

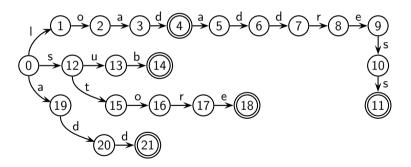
Representing DFA

Minimizing DFA

## **Tutorial Problem On Scanning**

 Find the occurrences of following substrings in a given input string load, loadaddress, add, sub, store

• Use the following automata



ullet Scan two input strings loadsubadd $\longleftrightarrow$  and loadaddsub $\longleftrightarrow$ 



Topic:

Scanning Section:

Specifying Scanners

Constructing DFAs

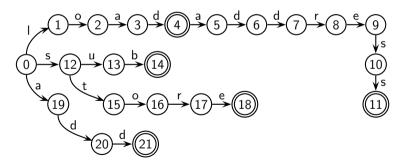
Tokenizing the Input

Representing DFAs

Minimizing DFAs

#### The Role of MarkedPos

Observe the role of MarkedPos for the input | loadaddsub← |





Topic: Scanning

Section:

Introduction

Specifying Scanners

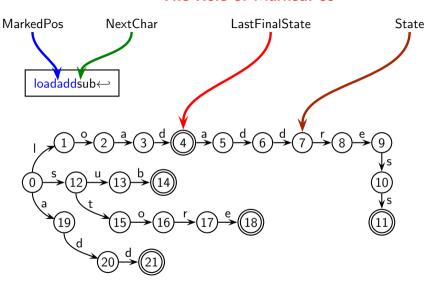
Constructing DFAs

Tokenizing the Input

Representing DFAs

Minimizing DFAs

#### The Role of MarkedPos





Topic:

Scanning Section:

Later desertes

Specifying Scanners

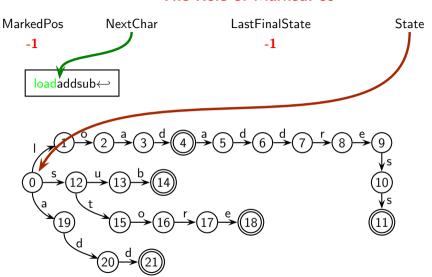
Constructing DFAs

Tokenizing the Input

Representing DFAs

Minimizing DFAs

#### The Role of MarkedPos





Topic:

Scanning

Section:

Specifying Scanners

Constructing DFAs

Tokenizing the Input

Representing DFAs

Minimizing DFAs

#### **Demo of Scan Trace**



Topic:

Scanning

Section:

Specifying Scanners

Constructing DFAs

Tokenizing the Input

Representing DFAs

Minimizing DFAs



Topic:

Scanning Section:

1000

Specifying Scanners

Constructing DFAs

Tokenizing the Input

Representing DFAs

Minimizing DFAs

# Representing DFAs Using Four Arrays



Topic: Scanning

Section:

Lancia de carta

Specifying Scanners

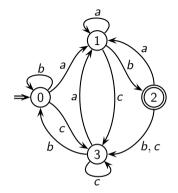
Constructing DFAs

Tokenizing the Inpu

Representing DFAs

Minimizing DFAs

# DFA to be Represented Using Four Arrays: Example 1





Topic: Scanning

Section:

Introduction

Specifying Scanners

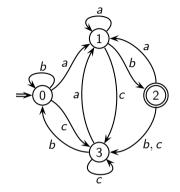
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Tokenizing the Inp

Representing DFAs

Minimizing DFAs

# DFA to be Represented Using Four Arrays: Example 1



	а	b	U
0	1	0	3
1	1	2	3
2	1	3	3
3	1	0	3



Topic: Scanning

Section

. . . .

Specifying Scanners

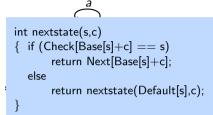
Constructing DFAs

Takanising the lan

Representing DFAs

Minimizing DFA

# **DFA** to be Represented Using Four Arrays: Example 1



States 0 and 3 have identical transitions. Transitions in states 1 and 2 differ from them only on b.

Char	Code
а	0
b	1
С	2

		C	
	а	b	С
0	1	0	3
1	1	2	3

State	Default	Base
0		
1		
2		
3		

	Next	Check
0		
1		
2		
3		
4		
5		



Topic: Scanning

Section

Introduction

Specifying Scanners

Constructing DFA

TORCHIZING the mpi

Representing DFAs

DFA to be Represented Using Four Arrays: Example  ${\bf 1}$ 

int nextstate(s,c)
{ if (Check[Base[s]+c] == s)
 return Next[Base[s]+c];
 else
 return nextstate(Default[s],c);
}

We choose to fill the entries for state 0 first (state 3 could also have been used)

Char	Code
	0
a	1
b	1
С	2

		а	b	С
	0	1	0	3
ľ	1	1	2	3
	2	1	3	3

State	Default	Base
0		
1		
2		
3		

	Next	Check
0		
1		
2		
3		
4		
5		



Topic: Scanning

Section

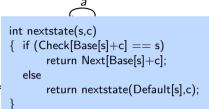
Introduction

Constructing DFA

Representing DFAs

Minimizing DFA

# DFA to be Represented Using Four Arrays: Example 1



The Check array contains 0 to confirm that the corresponding entries in the next array are for state 0

Char	Code
а	0
b	1
С	2

	а	b	С
0	1	0	3
1	1	2	3
7	1	2	2

State	Default	Base	
0			
1			
2			
3			

Next	Check
1	0
0	0
3	0



Topic: Scanning

Section:

Introduction

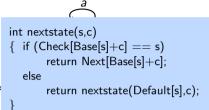
Specifying Scanners

Constructing DFA

Representing DFAs

Minimizing DFA

# DFA to be Represented Using Four Arrays: Example 1



The Check array contains 0 to confirm that the corresponding entries in the next array are for state 0

Char	Code
а	0
b	1
С	2

	C								_N	JXS		Checl	k_
			St	tate	efault و	Base		<b>→</b> 0	$\Box$	1		70	
á	a b	С		0				1	<b>&gt;</b>	0		0	
0 (	1 0	3	<b>&gt;</b>	-				2	$ \top $	3 /		(0)	
1	1 2	3		2				3	T '				
2	1 3	3		3				4			İ		
3	1 0	3		I.			1	5			Ī		



Topic: Scanning

Section

Introduction

Constructing DEA

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Representing DFAs

Minimizing DFAs

# **DFA** to be Represented Using Four Arrays: Example 1



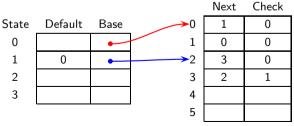
For state 1, we reuse the transitions on a and c from state 0 but need to enter transition on b explicitly. We do this using the next free entry (index 3) in the next array and back calculating the base of state 1.

return nextstate(Default[s],c);



Char	Code
а	0
b	1
С	2

	а	b	С
0	1	0	3
1	1	2	3
2	1	3	3
3	1	0	3





Topic: Scanning

Section

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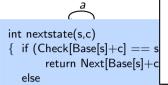
Constructing DFAs

Tokenizing the Inpu

Representing DFAs

Minimizing DFA

# **DFA** to be Represented Using Four Arrays: Example 1



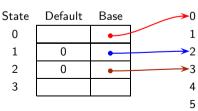
The variation in state 2 is similar to that for state 1. We reuse the transitions on a and c from state 0 but enter transition on b explicitly in the next free entry (index 4) in the next array and back-calculate the base of state 2.



return nextstate(Default[s].c);

Char	Code
а	0
b	1
С	2
С	2

	а	b	С
0	1	0	3
1	1	2	3
2	1	3	3
3	1	0	3



Next	Check	
1	0	
0	0	
3	0	
2	1	
3	2	



Topic: Scanning

Section

Caraif in a Carana

Constructing DFAs

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Representing DFAs

Minimizing DFA

# **DFA** to be Represented Using Four Arrays: Example 1

int nextstate(s,c)
{ if (Check[Base[s]+c] == s
 return Next[Base[s]+c
 else

State 3 is identical to state 0. We have shown here its base as same as for state 0. (In practice, lex begins the entries from index 1 and leaves index 0 free for such entries. We have

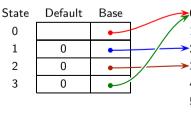
and keeps index 0 free for such entries. We have ignored it because it is a matter of details.)



return nextstate(Defauitisi.c);

[	Char	Code
	а	0
ĺ	b	1
ĺ	С	2

	а	b	С
0	1	0	3
1	1	2	3
2	1	3	3
3	1	0	3



Next	Check	
1	0	
0	0	
3	0	
2	1	
3	2	



Topic: Scanning

Section:

Carate dans Carana

Constructing DFAs

Representing DFAs

Minimizing DEA

## The Intuition Behind Four Array Representation

How to find the appropriate space in Next array for a state?

- View the entries (in the row of the state) that are required to be stored as "pins" separated by the entries that are not required to be stored
- View the positions in the Next array that do not contain a transition as "holes"
- Try to match the pattern (i.e. separation) of pins with that of the available holes



Topic: Scanning

Section:

Introduction

Specifying Scanners

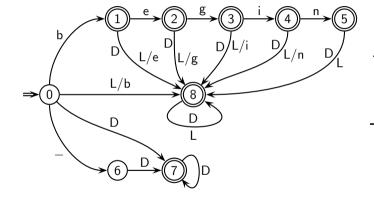
Constructing DFAs

Tokenizing the Inp

Representing DFAs

Minimizing DFAs

# DFA to be Represented Using Four Arrays: Example 2



Set	Characters						
L	a to z						
D	0 to 9						

Pattern	Token
begin	BEGIN
$L(L D)^*$	ID
$(- \epsilon)D^+$	NUM



Topic: Scanning

Section:

Introduction

Specifying Scanners

Constructing DFA

Tokenizing the Input

Representing DFAs

Minimizing DFAs

# **Table Representation for Example 2**

In the following, L denotes any letter from a to z other than b, e, g, i, n because these letters are listed separately

	b	е	g	i	n	L	D	-
0	1	8	8	8	8	8	7	6
1	8	2	8	8	8	8	8	
2	8	8	3	8	8	8	8	
3	8	8	8	4	8	8	8	
4	8	8	8	8	5	8	8	
5	8	8	8	8	8	8	8	
6							7	
7							7	
8	8	8	8	8	8	8	8	

Character	Code
a - z	0 - 25
0 - 9	26 - 35
-	36



Topic:

Scanning Section:

-----

Specifying Scanners

Constructing DFA

Tokenizing the Inpu

Representing DFAs

Minimizing DFAs

## **Choice of Default States for Example 2**

- States 8 and 6 are represented independently
- State 6 is the default state for state 7
- State 8 is the default state for all other states



Topic: Scanning

Section:

Section.

Specifying Scanners

Constructing DFAs

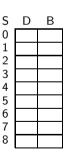
Tokenizing the Inp

Representing DFAs

Minimizing DFAs

# Four Arrays Representation for Example 2

S:	State	
D:	Default	
B:	Base	
N:	Next	
c.	Check	



	Ν	C		Ν	C		Ν	C		Ν	C
0			20			40			60		
1 2 3			21			41			61		
2			22			42			62		
3			23			43			63		
			24			44			64		
5			25			45			65		
4 5 6 7 8			26			46			66		
7			27			47			67		
8			28			48			68		
9			29			49			69		
10			30			50			70		
11			31			51			71		
12			32			52			72		
13			33			53			73		
14			34			54			74		
15			35			55			75		
16			36			56			76		
17			37			57			77		
18			38			58			78		
19			39			59			79		



S: State

B: Base

N: Next

D

3

4

5

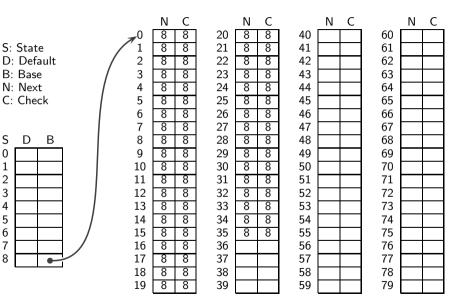
6

Topic: Scanning

Section

Representing DFAs

# Four Arrays Representation for Example 2





Topic: Scanning

Scannin

Section:

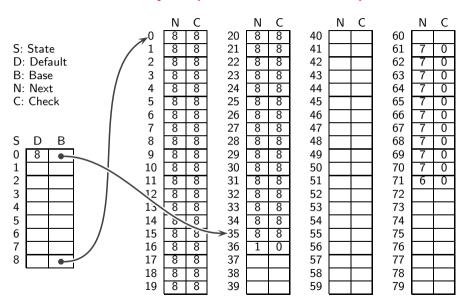
Specificing Scanners

Constructing DFAs

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Representing DFAs

Minimizing DFA





Topic: Scanning

Scannin

Section

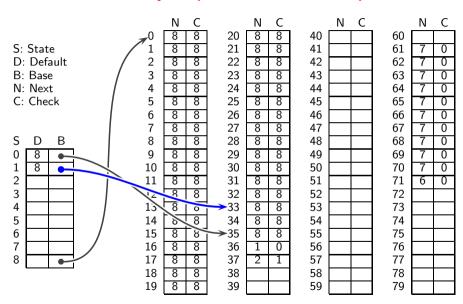
Cassifying Company

Constructing DFAs

Tokenizing the Inc

Representing DFAs

Minimizing DFA

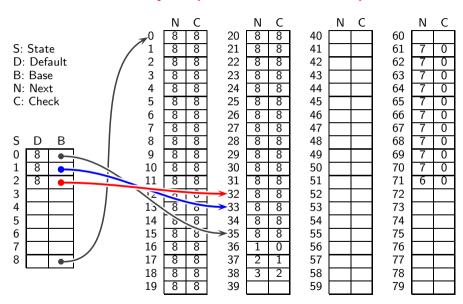




Topic: Scanning

Section

Representing DFAs





Topic: Scanning

Section

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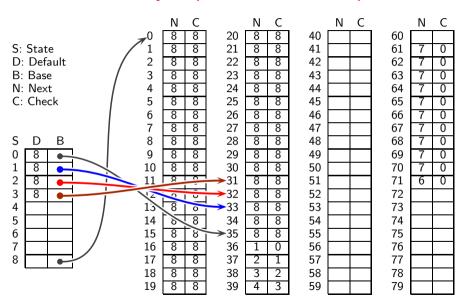
Cassifying Company

Constructing DFAs

Takanining the land

Representing DFAs

Minimizing DFA





Topic: Scanning

Section

Introduction

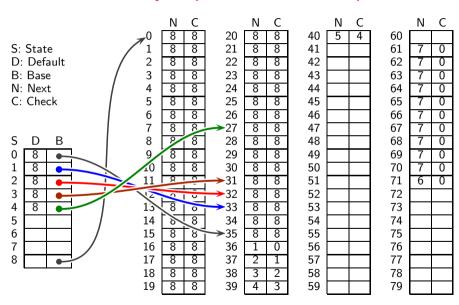
Specifying Scanner

Constructing DFAs

Tokenizing the Inc

Representing DFAs

Minimizing DFA

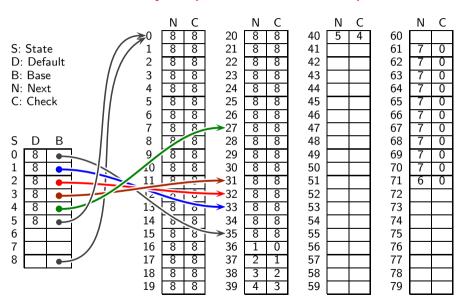




Topic: Scanning

Section

Representing DFAs

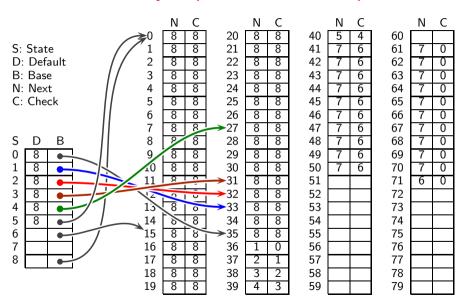




Topic: Scanning

Section

Representing DFAs





Topic: Scanning

Section

Introduction

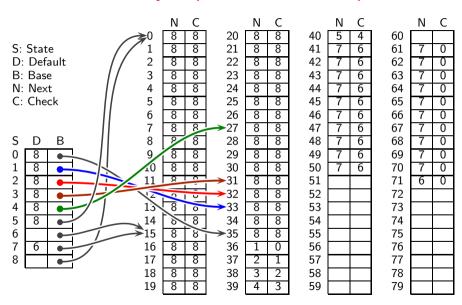
Specifying Scanner

Constructing DFAs

Tokenizing the Ir

Representing DFAs

Minimizing DFA





Topic: Scanning

Section:

Introduction

Specifying Scanners

Constructing DFAs

Tokenizing the Inpu

Representing DFAs

Minimizing DFA

### Size Comparison for Example 2

• Space for a 2 dimensional table

rows 
$$\times$$
 columns =  $9 \times 36 = 324$ 

• Space for four arrays representation

Array	Size
Next	71
Check	71
Default	9
Base	9
Total	160

 If a large graph seen as adjacency matrix is stored using four arrays, it would have the need of pointers and dynamic memory allocation
 This would imply good cache behaviour



Topic: Scanning

Section:

Introduction

Specifying Scanners

Constructing DFAs

Tokenizing the In

Representing DFAs

Minimizing DFA

### Further Compression Using Equivalence Classes

- The four arrays handle similarity in the rows of the 2-D table
- Several columns could have a lot of similarity too
- We can define equivalence classes of characters that have identical transitions Identical columns are collapsed into a single column
- The equivalence classes are given contiguous codes thereby eliminating several "holes" in the Next and Check arrays



Topic: Scanning

Section

Section

Specifying Scanners

Constructing DFA

Tokonizing the Inc

Representing DFAs

Minimizing DFA

### Further Compression Using Equivalence Classes for Example 2

$EC \to$	0	1	2	3	4	5	6	7
	b	е	g	i	n	L	D	-
0	1	8	8	8	8	8	7	6
1	8	2	8	8	8	8	8	
2	8	8	3	8	8	8	8	
3	8	8	8	4	8	8	8	
4	8	8	8	8	5	8	8	
5	8	8	8	8	8	8	8	
6							7	
7							7	
8	8	8	8	8	8	8	8	

```
int nextstate(s,c)
{    if (Check[Base[s]+c] == s)
        return Next[Base[s]+c];
    else
        return nextstate(Default[s],c);
}
```

Now c represents the class of a character instead of the character



Topic: Scanning

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Section:

Specifying Scanners

Constructing DFAs

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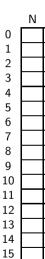
Representing DFAs

Minimizing DFAs

# Four Arrays Representation Using Equivalence Classes for Example 2

$EC \to$	0	1	2	3	4	5	6	7
	b	е	g	i	n	L	D	ı
0	1	8	8	8	8	8	7	6
1	8	2	8	8	8	8	8	
2	8	8	3	8	8	8	8	
3	8	8	8	4	8	8	8	
4	8	8	8	8	5	8	8	
5	8	8	8	8	8	8	8	
6							7	
7							7	
8	8	8	8	8	8	8	8	

D: 3: <b>N</b> :	Stat Def Bas Nex Che	ault e t	
5	D	В	
)			
2 2			
3			
}			
5			
7			



16



Topic:

Section

Introduction

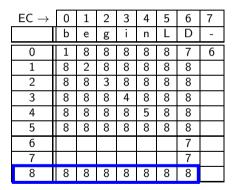
Specifying Scanners

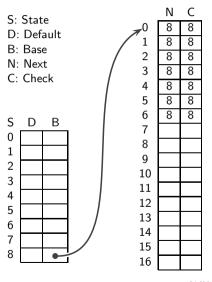
Constructing DFAs

Takanining the lan

Representing DFAs

Minimizing DFA







Topic: Scanning

Section:

Introduction

Specifying Scanners

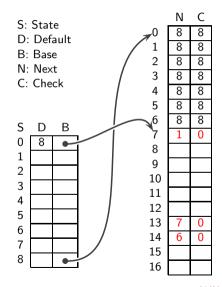
Constructing DFAs

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Representing DFAs

Minimizing DEA

$EC \to$	0	1	2	3	4	5	6	7
	b	е	ф	i	n	L	D	-
0	1	8	8	8	8	8	7	6
1	8	2	8	8	8	8	8	
2	8	8	3	8	8	8	8	
3	8	8	8	4	8	8	8	
4	8	8	8	8	5	8	8	
5	8	8	8	8	8	8	8	
6							7	
7							7	
8	8	8	8	8	8	8	8	





Topic:

Scanning

Section:

Specifying Scanners

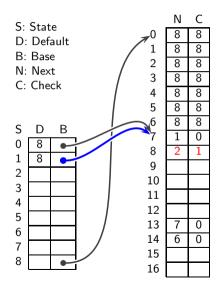
Constructing DEA

constructing D1745

Representing DFAs

Minimizing DEA

$EC \to$	0	1	2	3	4	5	6	7
	b	е	g	i	n	L	D	-
0	1	8	8	8	8	8	7	6
1	8	2	8	8	8	8	8	
2	8	8	3	8	8	8	8	
3	8	8	8	4	8	8	8	
4	8	8	8	8	5	8	8	
5	8	8	8	8	8	8	8	
6							7	
7							7	
8	8	8	8	8	8	8	8	





Topic: Scanning

Section:

Introduction

Specifying Scanners

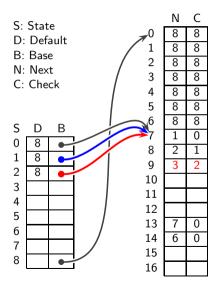
Constructing DFAs

Tokenizing the Inni

Representing DFAs

Minimizing DFA

$EC \to$	0	1	2	3	4	5	6	7
	b	е	g	i	n	L	D	-
0	1	8	8	8	8	8	7	6
1	8	2	8	8	8	8	8	
2	8	8	3	8	8	8	8	
3	8	8	8	4	8	8	8	
4	8	8	8	8	5	8	8	
5	8	8	8	8	8	8	8	
6							7	
7							7	
8	8	8	8	8	8	8	8	





Topic: Scanning

Section:

Introduction

Specifying Scanners

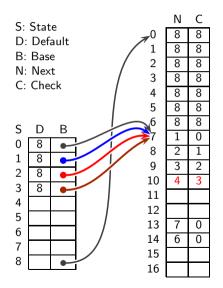
Constructing DFAs

Tokenizing the Inpu

Representing DFAs

Minimizing DFAs

$EC \to$	0	1	2	3	4	5	6	7
	b	е	g	i	n	L	D	-
0	1	8	8	8	8	8	7	6
1	8	2	8	8	8	8	8	
2	8	8	3	8	8	8	8	
3	8	8	8	4	8	8	8	
4	8	8	8	8	5	8	8	
5	8	8	8	8	8	8	8	
6							7	
7							7	
8	8	8	8	8	8	8	8	





Topic: Scanning

Section:

Introduction

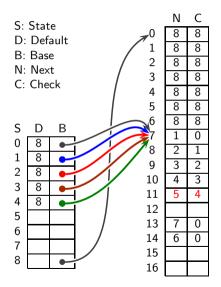
Specifying Scanners

Constructing DFA

Representing DFAs

Minimizing DEA

$EC \to$	0	1	2	3	4	5	6	7
	b	е	g	i	n	L	D	-
0	1	8	8	8	8	8	7	6
1	8	2	8	8	8	8	8	
2	8	8	3	8	8	8	8	
3	8	8	8	4	8	8	8	
4	8	8	8	8	5	8	8	
5	8	8	8	8	8	8	8	
6							7	
7							7	
8	8	8	8	8	8	8	8	





Topic: Scanning

Section:

Introduction

Specifying Scanners

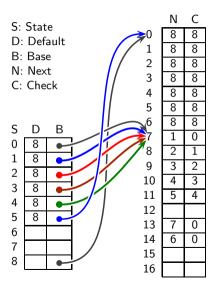
Constructing DFA

Tokenizing the Inni

Representing DFAs

Minimizing DEA

$EC \to$	0	1	2	3	4	5	6	7
	b	е	ф	i	n	L	D	-
0	1	8	8	8	8	8	7	6
1	8	2	8	8	8	8	8	
2	8	8	3	8	8	8	8	
3	8	8	8	4	8	8	8	
4	8	8	8	8	5	8	8	
5	8	8	8	8	8	8	8	
6							7	
7							7	
8	8	8	8	8	8	8	8	





Topic:

Section

Introduction

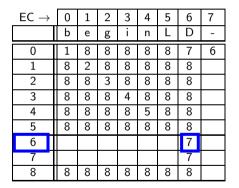
Specifying Scanners

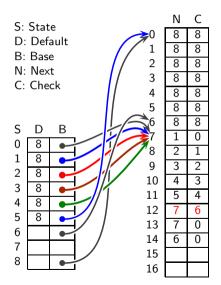
Constructing DFA

Tokenizing the Inpu

Representing DFAs

Minimizing DEA







Topic:

Section

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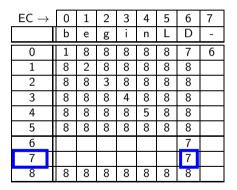
Specifying Scanners

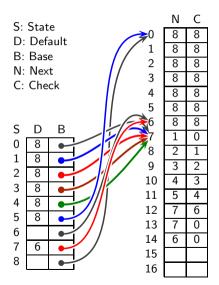
Constructing DFA

Tokenizing the Inpu

Representing DFAs

Minimizing DEA







Topic:

Scanning

Section:

Specifying Scanners

Constructing DFAs

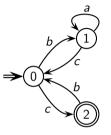
Tokenizing the Input

Representing DFAs

Minimizing DFAs

#### **Tutorial Problem**

Represent the following DFA using 4-arrays notation as compactly as possible



Character	Code
а	0
Ь	1
C	2



Topic:

Scanning Section:

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Specifying Scanners

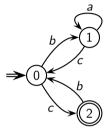
Constructing DFAs

Representing DFAs

Minimizing DFAs

#### **Tutorial Problem**

Represent the following DFA using 4-arrays notation as compactly as possible



Character	Code
а	0
Ь	1
C	2

State	Base	Default
0	2	
1	0	
2	0	

	Next	Check	
0	1	1	
1	0	2	
2	0	1	
3	1	0	
4	2	0	
5			



Topic:

Scanning

Section:

Specifying Scanners

Constructing DFAs

Tokenizing the Input

Representing DFAs

Minimizing DFAs



Topic: Scanning

Section:

Introduction

Specifying Scanners

Constructing DFAs

Tokenizing the Input

Representing DFAs

Minimizing DFAs

### Minimizing DFAs



Topic: Scanning

Section:

Introduction

Specifying Scanners

Constructing DFAs

Tokenizing the Input

Representing DFAs

Minimizing DFAs

### **Minimizing DFAs**

Prof. Sanyal's slides (scanning-slides-sanyal-part3.pdf)