

NI VeriStand™

Real-Time Testing and Simulation Software

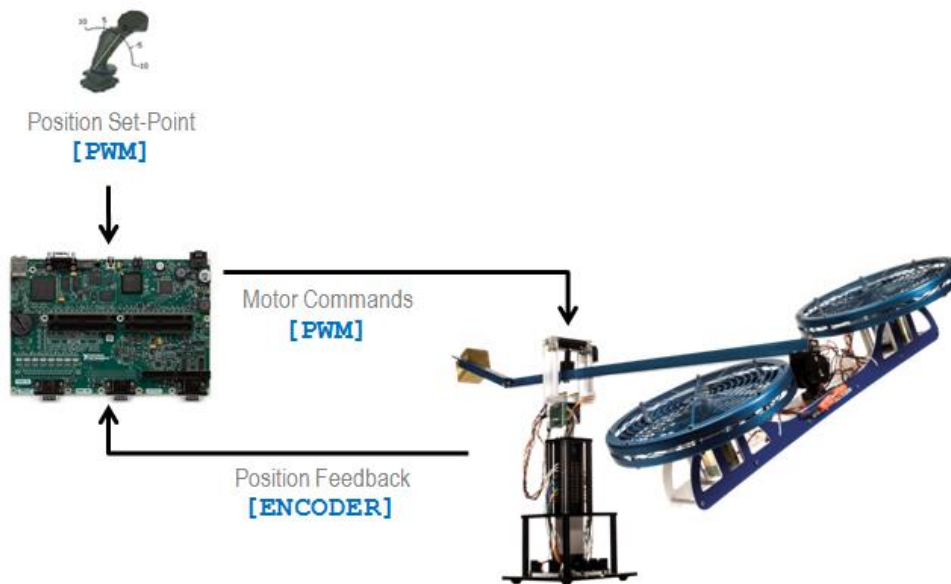


2014 Hands-On

NI VeriStand Hands-On Exercise

During this hands-on exercise you will learn how NI VeriStand can be used to create real-time testing applications.

In this exercise you will be building a model-in-the-loop (MIL) test system to test a helicopter control system. The system consists of an electronic control unit (ECU) that includes embedded software which uses the position set-point and position feedback (elevation, travel, motor pitch) from the helicopter to calculate the appropriate motor commands to provide the desired controllability and stability for the helicopter system.



Model-in-the-loop (MIL) testing is primarily performed when the control algorithm is still being designed. It uses software models of the system that accurately represent the system behavior to perform virtual tests in a pure simulation environment. To implement the MIL test system, you will connect the control software to the helicopter software model in NI VeriStand to evaluate the expected system behavior. At the end of this exercise, there are instructions for how you can replace the simulated control software with the actual ECU hardware and use physical I/O interfaces to connect it to a real-time simulation of the helicopter software model to produce a hardware-in-the-loop (HIL) test system.

Please keep in mind - most of the techniques that you use to create this example will be relevant to any real-time testing application (creating user-interface, calculated channels, hardware interfaces ...). Therefore, even if you do not create MIL or HIL test systems, this exercise will still provide you with a hands-on introduction to NI VeriStand.

Background

Unit Under Test

In this exercise, you will be testing a helicopter control algorithm. The control algorithm (for MIL testing) or electronic control unit (ECU) with deployed control software (for HIL testing) will therefore be the unit under test (UUT). The control algorithm was created using the LabVIEW Control Design and Simulation Module. The control unit consists of a Single-Board RIO (sbRIO) preprogrammed with a cascaded PID flight control algorithm that will achieve a user specified elevation and rotational position. The control algorithm uses 6 inputs and 2 outputs as listed below:

Inputs

- Elevation Set Point
- Travel Set Point
- Pitch Encoder Pulses
- Elevation Encoder Pulses
- Travel Encoder Pulses
- Reset

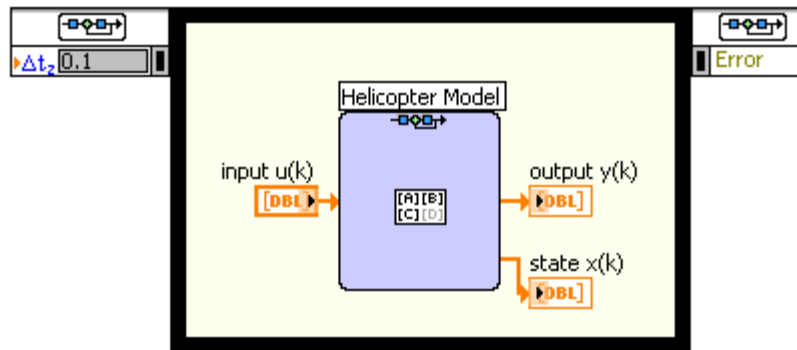
Outputs

- Left Motor Power
- Right Motor Power

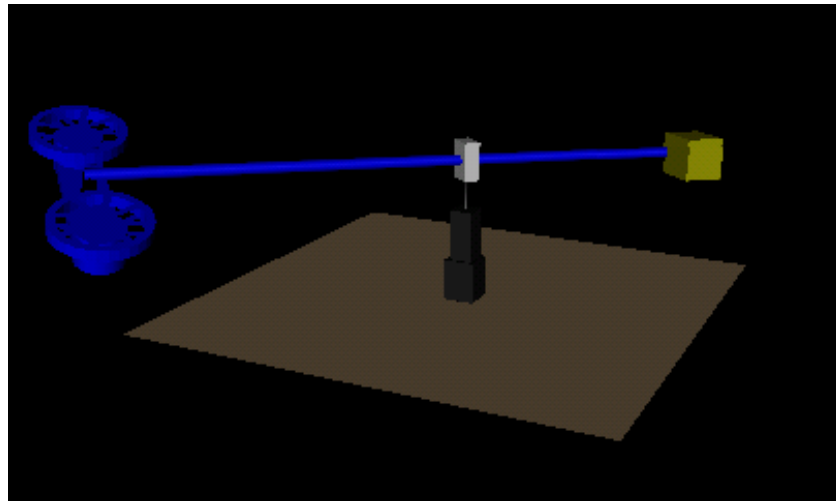
Notice that there is no set point for Pitch. This is because the controller will always attempt to level the pitch once the other two set points have been achieved. The reset input is used to reset the controller's current values for encoder position.

System Dynamics Model

To properly test the control algorithm, we will need a model that accurately simulates the dynamic behavior of the helicopter system by generating the appropriate outputs based on the given inputs and the effects of the environments (wind, gravity, etc). For this example you have been provided a pre-built model that was created using the LabVIEW Control Design and Simulation Module. The model has 2 inputs (left and right motor power) as well as 6 outputs (pitch, travel, and elevation position and velocity). The model will produce the appropriate outputs to simulate the response of the system to the control algorithms motor power values.



To better visualize the behavior of the system dynamics model, you have also been provided the necessary files to produce a 3D image of the helicopter system as shown below.



This 3D image will be connected to the outputs of the system dynamics model to animate it appropriately.

FPGA Personality

In order to properly create the HIL system, the embedded control unit will communicate with the model running on the PXI RT controller with actual physical hardware signals just like it would with a real helicopter. To accomplish this task, your NI VeriStand configuration will utilize an R-series FPGA card loaded with a LabVIEW FPGA personality designed for this application.

Step-by-Step Instructions

NOTE: "<Public Documents>" refers to a specific windows directory on your computer. This folder contains NI VeriStand files to be used with this document.

By default, on Windows XP it is located at: "C:\Documents and Settings\All Users\Documents"

On Windows Vista and 7 it is located at: "C:\Users\Public\Documents"

I. Import Physical I/O into NI VeriStand Configuration

1. Launch NI VeriStand by navigating to **Program Files » National Instruments » NI VeriStand 2014 » NI VeriStand**.
2. Create a new project.
 - a. Select **File » New Project**.
 - b. Name your project under **Project Name**.
 - c. Set the **Project Root Folder** to <Public Documents>\National Instruments\NI VeriStand 2014\Projects\Hands On
 - d. Uncheck **Create folder for project**.
 - e. Select **OK**.



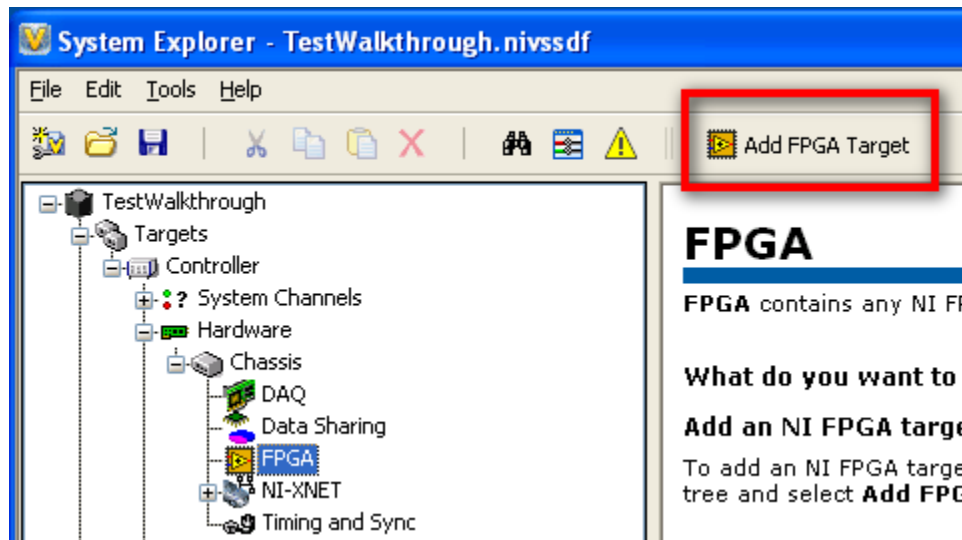
The **Project Explorer** is used to configure your entire test setup. From the project explorer you can operate your test system and control system deployment. The project explorer contains all of the key elements to running and automating your system, as well as any custom files you choose to add.

3. Expand the **System Definition File** section.
4. Right click the <ProjectName>.nivssdf file and select **Launch System Explorer**.



The **System Explorer** is used to create your NI VeriStand system definition. A system definition is a file that configures the NI VeriStand Engine to behave in a specified manner. You can configure a system definition by adding, removing, and modifying options in the configuration tree located on the left of the System Explorer window. Using the System Explorer, you can define various components of the system, including NI VeriStand Engine execution settings, hardware I/O, calculated channels, simulation and control models, alarms, procedures, and mappings between these engine resources. Once complete, the system definition is deployed to the NI VeriStand Engine and begins executing.

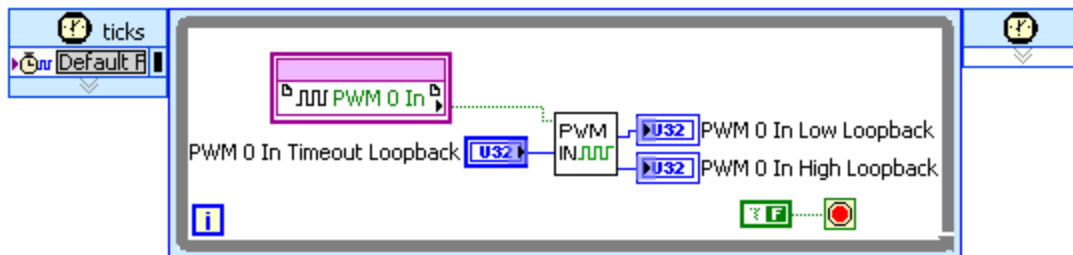
5. Add an FPGA Target
 - a. Expand the System Explorer tree to **Controller » Hardware » Chassis » FPGA**.
 - b. Click **FPGA** and select **Add FPGA Target**.



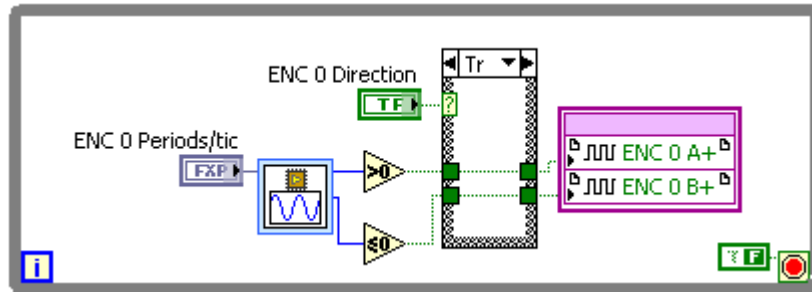
- c. Browse to **<Public Documents>\National Instruments\NI VeriStand 2014\Projects\Hands On\FPGA Personalities** and select the fpgaconfig file that matches the model number of your R series DAQ card.
 - **Note:** You will either have a PXI-7811R, PXI-7831R, PXI-7833R, PXI-7852R or a PXI-7854R in your chassis. Select the corresponding file to your hardware.



This FPGA target personality was created using the shipping NI VeriStand LabVIEW FPGA template. This template increases development productivity by minimizing programming architecture and focuses on the creation of the necessary I/O personality. The in ports and out ports seen in the system explorer are defined by an XML file (.fpgaconfig) that describes how the FPGA code interfaces to the NI VeriStand Engine.



FPGA PWM Input Code



FPGA Encoder Output Code



NI VeriStand provides built in personalities for many common FPGA I/O types, Analog Input, Analog Output, Digital I/O, Pulse Trains, Pulse Width Modulation, and more. Note that In addition to user-defined FPGA personalities, Multifunction Data Acquisition, LIN, CAN, FlexRay, Timing and Sync hardware, and more can be configured directly in NI VeriStand.

6. Select **Controller** in the System Explorer tree.
7. Observe the Execution Settings.
8. Modify the **Target Rate** to 1000 Hz (1 kHz).

Target Specification

Name: Controller Operating System: Windows IP Address: localhost

Processor Assignments

Primary Control Loop mode: Automatic Processor: -2 Data Processing Loop mode: Automatic Processor: -2

Target Decimations

Data Processing Loop: 1 DAQ digital lines: 1

Other Settings

Maximum streamed channels: 512 Execution mode: Parallel ☐ Filter DAQ Errors ☐ Filter Watchdog Errors

Timing Source Settings

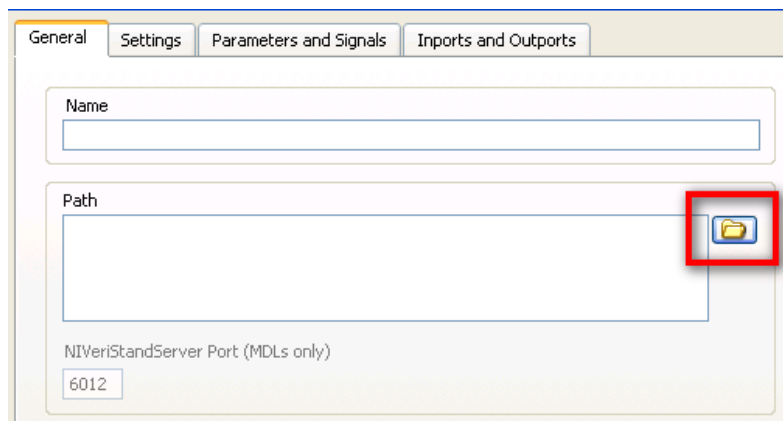
Primary Control Loop timing source: Automatic Timing

Target Rate: 1000 Hz Timeout: 120 sec

II. Create HIL Test System

In this exercise we will connect our Unit Under Test (UUT) to our PXI and build the HIL test system.

1. Using the cable provided, if not already connected, connect **Port 1 of the R-series to the sbRIO (UUT)**.
2. Select **Simulation Models** from the explorer tree.
 - a. Select **Add Simulation Model**.

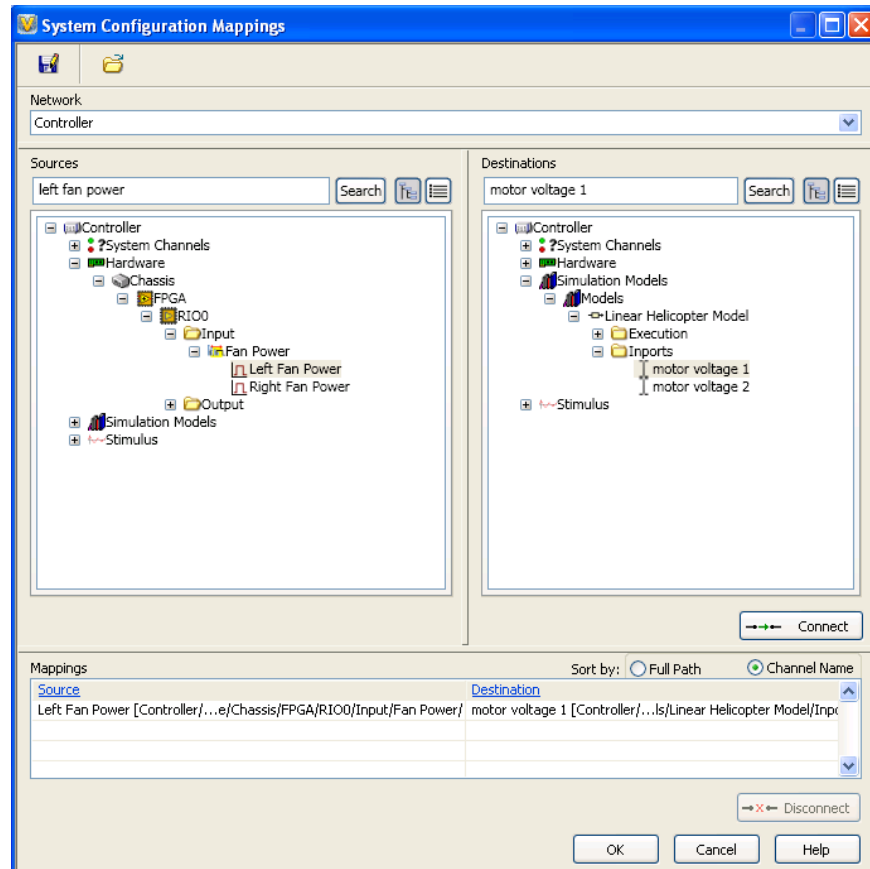


- b. Browse to **<Public Documents>\National Instruments\NI VeriStand 2014\Projects\Hands On\Models\Linear Helicopter Model.lvmodel**.
 - c. Select **OK**.




Recall that this model was created using the LabVIEW Control Design and Simulation Module, but any supported modeling environment or C code can be used. The model has 2 inputs (left and right motor power) as well as 6 outputs (the pitch, travel, and elevation position and velocity). Using the 2 inputs, the model will produce the appropriate outputs to simulate the response of the system to the embedded controller's input.

3. Now we will map our system physical inputs and outputs to those of the model
 - a. Select **Tools » Edit Mappings** from the menu.
 - b. Select or search for **Left Fan Power** from the source window, and **Motor Voltage 1** from the destination window.
 - c. Select the **Connect** button to create the mapping.









NI VeriStand also provides an import and export mapping tool. This can expedite the creation of large system mappings.

4. Select the  icon from System Configuration Mappings toolbar.
- Browse to **<Public Documents>\National Instruments\NI VeriStand 2014\Projects\Hands On\helicopter mapping.txt**.
 - Select **Import** to import the rest of the mappings into NI VeriStand.
 - Select **Exit** and you should see 4 additional mappings added to the mappings table.
 - The complete mappings for the HIL system are shown in the table below:

Source	Destination
Left Fan Power	Motor Voltage 1
Right Fan Power	Motor Voltage 2
Elevation speed (deg/sec)	Elevation Velocity
Pitch speed (deg/sec)	Pitch Velocity
Travel speed (deg/sec)	Travel Velocity

- Select **OK**
5. We also need to create a procedure to reset our UUT so we can start from a known state.
- Select **Procedures**.
 - Select **Add Procedure**.
 - Name the procedure **Reset HIL**.
 - Create a procedure step by selecting **Add** then **Set Multiple Variables**.
 - Double click the newly added step and configure with these parameters:
 - Name: **Stop Model & Controller**
 - Number of Channels to Set: **2**
 - Channel 1: **Controller » Simulation Models » Linear Helicopter Model » Execution » Model Command**
 - Value 1: **2**
 - Channel 2: **Controller » Hardware » Chassis » FPGA » RIO0 » Output » Digital » Controller Reset**
 - Value 2: **1**
 - From the Reset HIL page, select **Add** then **Dwell**.
 - **Constant Value: 0.1**
 - Continue adding steps, using set variable and dwell to match the following:

Label	Command
 Stop Model & Controller	[Model Command] = 2.000000 [Controller Reset] = 1.000000
 Dwell	Dwell for 0.100000 seconds
 Start Model	[Model Command] = 0.000000
 Dwell 1	Dwell for 0.500000 seconds
 Start Controller	[Controller Reset] = 0.000000


6. Back in procedures, let's make our HIL Reset trigger on start up of the NI VeriStand engine.
 - a. Select **Procedures** from the left pane of the system explorer window.
 - b. Check **On start execute procedure** at the bottom of the Procedure window.
 - c. Select **Reset HIL** from the drop down menu.
- Now that we have imported our model and mapped it to physical I/O, we can deploy our configuration and have the controller (UUT) control the model. We can define our set points and watch our controller move the model of the helicopter system to that point.
 7. Launch Measurement and Automation Explorer by navigating to **Start » All Programs » National Instruments » Measurement and Automation Explorer**.
 8. Select the PXI RT target that is listed under **Remote Systems**.
 9. Note the **IP address**.
 10. Back in the NI VeriStand System Explorer, select **Controller**.
 11. Change **Operating System** to PharLap.
 12. Enter the **IP Address** you noted in MAX.
 13. **Save** and **close** the System Explorer.
 14. Select **Operate » Run** to deploy and run your configuration. In addition to deploying your system configuration to the real-time target, the NI VeriStand Workspace will open. The NI VeriStand workspace is a run-time editable user interface that can be used to monitor and display any channel in the deployed system definition.
 15. Rename this new screen by selecting **Screen » Screen Properties...**
 - a. Name the screen **HIL**.
 - b. Select **OK**.
 16. Switch to Edit Mode. Select **Screen » Edit Mode** or CTRL+M.
 17. From the Workspace Controls (on the left), click and drag to add a **Graph(Simple)**.
 - a. Graph Title: **Fan Power**
 - b. Select items:
Controller » Hardware » Chassis » FPGA » RIO0 » Input » Fan Power » Left Fan Power
Controller » Hardware » Chassis » FPGA » RIO0 » Input » Fan Power » Right Fan Power
 - c. Select  to add these channels to the graph.
 - d. Select the **Format & Precision** tab.

- e. Change the Y scale **Maximum** and **Minimum** to be 1 and -1 respectively.
18. From the Workspace Controls, add a **Numeric Control(Slide)**
- Channel: **Controller » Hardware » Chassis » FPGA » RIO0 » Output » Set Points » Elevation Set Point**
 - Control Label: **Elev.**
19. From the Workspace Controls, Add a **Numeric Control(Dial)**
- Channel : **Controller » Hardware » Chassis » FPGA » RIO0 » Output » Set Points » Travel Set Point**
 - Format & Precision Tab: Scale Min: **-100**
- To better visualize the behavior of our model, we will add a custom workspace object created using LabVIEW. This indicator uses the LabVIEW 3D picture control to provide a 3D animation of the system. We will connect the 3D picture control inputs to the outputs of our model that provides the current state (position) of the helicopter model.
20. Add our Custom Indicator **Helicopter(3dView)**
- Elevation: **Controller » Simulation Models » Models » Linear Helicopter Model » Outports » Elevation angle (deg)**
 - Pitch: **Controller » Simulation Models » Models » Linear Helicopter Model » Outports » Pitch angle (deg)**
 - Travel: **Controller » Simulation Models » Models » Linear Helicopter Model » Outports » Travel angle (deg)**
21. Disable Edit Mode. Select **Screen » Edit Mode** (Ctrl + M).
22. Change the Elevation and Travel controls and watch the 3D Picture Control Move to the set point and the fan power modulate. You are now telling the controller where to move the model and are performing Hardware-In-the-Loop testing of the controller.

III. Build Simple Real-Time Stimulus Profile

Now that we have learned how to use the NI VeriStand Workspace to create our UI, the next step is to create and run automated tests to qualify our controller unit. NI VeriStand has many tools including an Alarm Monitor, Channel Fault Manager, TDMS File viewer, and Stimulus Profile Editor. In this phase of the hands-on, we create a Real-Time test profile using the stimulus profile editor. To learn more about the other tools provided by NI VeriStand, see the NI VeriStand documentation or open them and try them out.

Using the NI VeriStand stimulus profile editor, you can create a profile to create sophisticated test sequences that can represent any real-world condition. The Stimulus to profile editor includes looping, branching, composable mathematical functions, and multitasking. You can also use it to replay waveforms, complete a series of steps, perform data logging, or pass/fail analysis. At the end of each profile, the stimulus profile editor will generate a report that summarizes the results of the tests. This exercise walks you through creating a test profile using the Stimulus Profile Editor. Each of the individual sequences that are called from the Stimulus Profile Editor run deterministically (when deployed to a real-time target), but the stimulus profile itself runs on the Windows host.

1. If you did not finish the last phase, please open the **Phase II Solution** for this exercise and use it to complete this phase.
 - a. Select the **Phase II Solution** project on the left side of the NI VeriStand Getting Started Window.
 - If **Phase II Solution** is not located here, select the **Browse** folder, open the **<Public Documents>\National Instruments\NI VeriStand 2014\Projects\Hands On** folder, and select **Phase II Solution.nivproj**.
 - b. Select **Configure Project** to open the Project Explorer.
 - c. Click the  to deploy and run your configuration.

Note: You may need to change the FPGA Configuration to match your R-Series DAQ board. In the System Explorer, select **Controller » Hardware » Chassis » FPGA » RIO0** and open the configuration from the desktop Hands On folder.



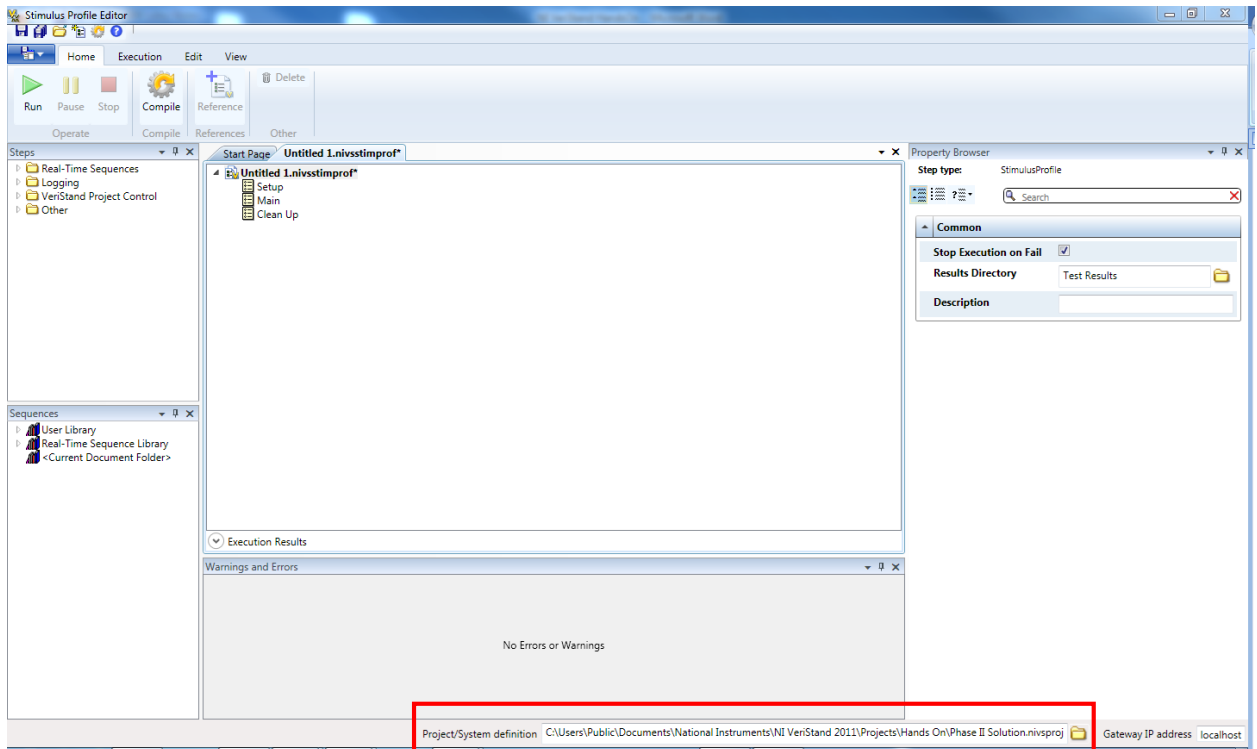
2. Open the Stimulus Profile Editor.
 - a. From the Workspace, select **Tools » Stimulus Profile Editor**.



This will open the NI VeriStand Stimulus Profile Editor, which is a stand-alone executable that runs independently of NI VeriStand. The NI VeriStand Stimulus Profile Editor is composed of two main components, stimulus profiles and Real-Time Sequences. Stimulus profiles run on the Windows host and act as a test executive for real-time tests. They can call Real-Time Sequences, perform NI VeriStand tasks such as opening and closing of NI VeriStand projects and Workspaces, and perform data logging.



The second component of the stimulus profile editor is the Real-Time Sequence. Real-Time Sequences are deployed to the real-time target and run deterministically to conduct real-time tests. There are many tools available in Real-Time Sequences for generating sophisticated test patterns. Some of these tools include looping, branching, multi-tasking, and composable mathematical functions. You can also use Real-Time Sequences as sub-sequences, so that you can create libraries of Real-Time Sequences to make real-time test sequence creation more efficient.



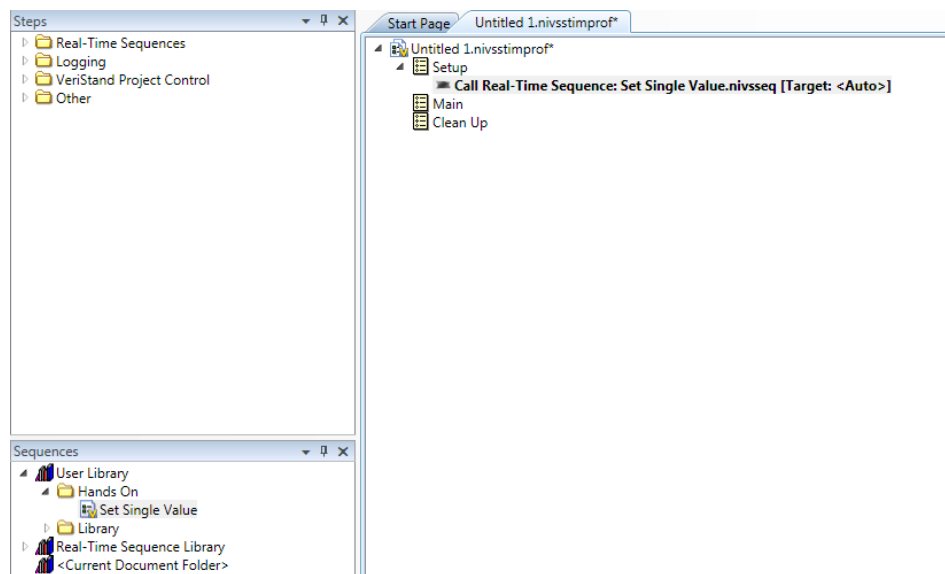
3. We will now create a profile to control the helicopter's elevation and travel. The first thing that we will need to do is to verify that our stimulus profile is bound to the current System Definition.
 - a. From the start menu, select **New Stimulus Profile**.
 - b. Verify that the file path (shown in the image above) is set to the current NI VeriStand Project. The image above demonstrates where to look in the Stimulus Profile Editor to verify this file path.
 - c. All of the channels in this NI VeriStand System Definition will now be available to us in the Stimulus Profile.


4. First, we will control the Elevation Setpoint of our system definition. We will automate the elevation set point to initialize to 0, wait for 5 seconds, rise to 11, wait for 10 seconds, and then rise to 19.



The User Library is a place to store custom real-time sequences, so that they can be used across stimulus profiles. If a real-time sequence is placed in the <Public Documents>\National Instruments\NI VeriStand 2014\Stimulus Profiles\ directory, it will automatically populate in the User Library of the Stimulus Profile Editor. This allows you to create custom libraries of real-time sequences that can be easily reused across real-time testing projects.

- a. In the Sequences Palette, expand the **User Library » Hands On** menus and drag the **Set Single Value** Sequence into the **Setup** section of the stimulus profile.



- b. In the Parameters window, map the **Channel** parameter to the **Elevation Setpoint** channel in the System Definition.
 - Click on the Channel radio button associated with **Channel**
 - Click the  and use the search bar to find **Elevation Setpoint**.
 - Select **Elevation Setpoint**

Property Browser

Step type: RealTimeSequenceCallStep

Search

Common

File Path: C:\Users\Public\Documents\National Instruments\NI VeriStand 2011\Stimulus Profiles\Hands On\Set Single Value.nvsseq

Target Name: [Dropdown]

Timeout [ms]: 10000

Description: [Text Box]

Pass/Fail Evaluation

Type: AlwaysPass

Parameters: No Parameters

Parameters

Channel: Constant 0

Channel: Targets/Controller/Hardware/Chassis/FPGA/RIO0/Output/Set Points/Elevation Set Point

NewValue: Constant 0

Channel: [Dropdown]

Update Parameters

- c. In the Sequences Palette, expand the **Real-Time Sequences Library » Standard » Timing** menus and drag the Drag a **Wait** sequence into the **Setup** section of the stimulus profile under the Set Single Value sequence.
 - Set **Duration** to 5.
 - In the **Pass/Fail Evaluation** section, change the Type to **Always Pass**

The screenshot shows a configuration window for a test sequence. The 'Common' tab is selected, displaying the following fields:

- File Path:** C:\Program Files\National Instruments\VeriStand 2011\nivs.lib\Sequence Library\Standard\Timing\Wait.nvsseq
- Target Name:** (empty dropdown)
- Timeout [ms]:** 10000
- Description:** (empty text box)

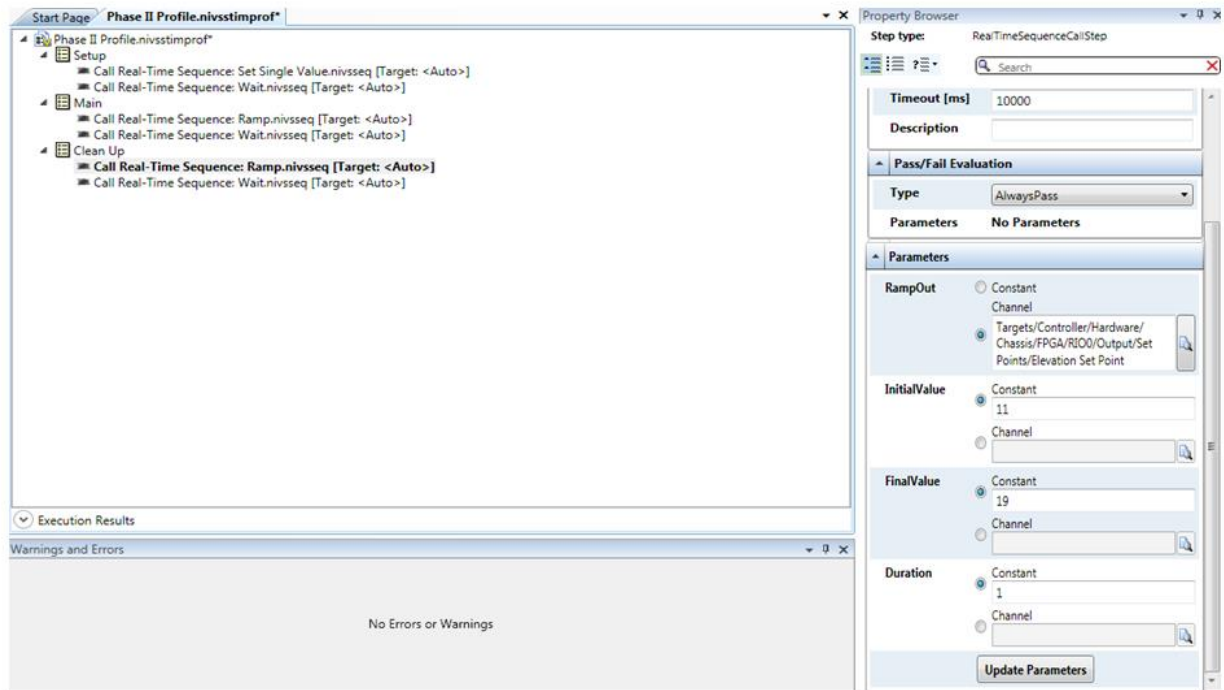
The 'Pass/Fail Evaluation' tab is highlighted with a red box. It contains:

- Type:** AlwaysPass (dropdown menu)
- Parameters:** No Parameters

The 'Parameters' tab is also visible below, showing:

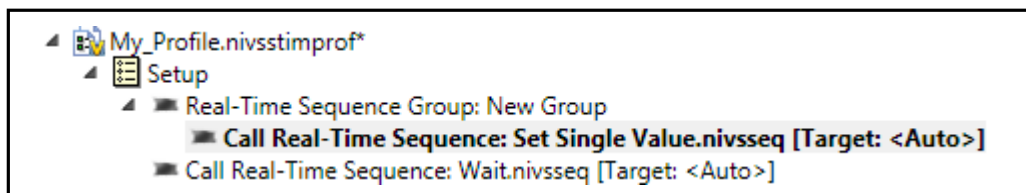
- Duration:** Constant (selected radio button) with a value of 5 in the adjacent text box. The 'Channel' option is unselected.
- Update Parameters:** (button)


- d. In the Sequences Palette, expand the **Real-Time Sequences Library » Standard » Waveforms** and drag a **Ramp** sequence into the **Main** section of the stimulus profile.
 - Set the **RampOut** Parameter to **Elevation Setpoint** channel by using the same procedure as in step 4b.
 - Set **Initial Value** to 0
 - Set **Final Value** to 11
 - Set **Duration** to 1
- e. Drag a **Wait** sequence into the **Main** section of the stimulus profile under the Ramp Sequence
 - Set **Duration** to 10
 - In the **Pass/Fail Evaluation** section, change the Type to **Always Pass**
- f. Drag a **Ramp** sequence into the **Clean Up** Section of the stimulus profile
 - Set the **RampOut** Parameter to **Elevation Setpoint** channel by using the same procedure as in step 4b.
 - Set **Initial Value** to 11
 - Set **Final Value** to 19
 - Set **Duration** to 1
- g. Drag a **Wait** sequence into the **Clean Up** section of the stimulus profile under the Ramp Sequence
 - Set **Duration** to 10
 - In the **Pass/Fail Evaluation** section, change the Type to **Always Pass**



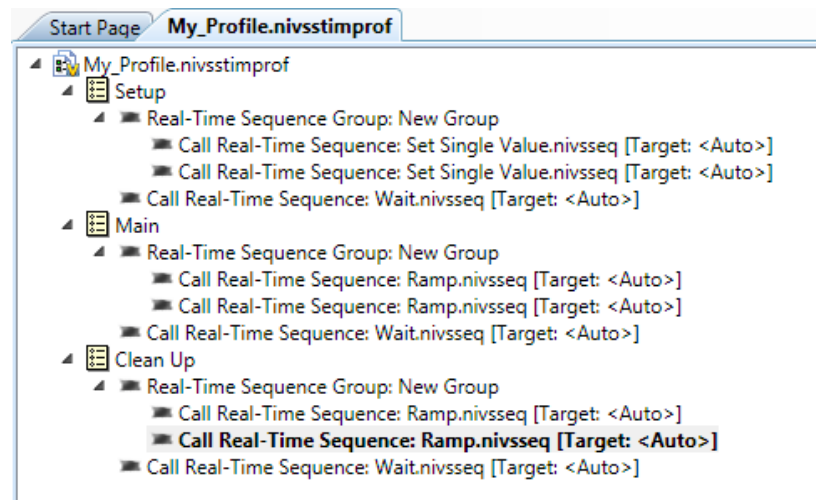
We can have multiple Real-Time Sequences running in parallel. We will run two sets of Real-Time Sequences in parallel to stimulate the controller's (UUT) elevation and travel set points simultaneously. We can add Real-Time Sequence Groups wherever we need to run two Real-Time Sequences in parallel. Anything under a Real-Time Sequence group will run in parallel.


5. We will now add Real-Time Sequences to control Travel Set Point. We will automate the elevation set point to initialize to 0, wait for 5 seconds, ramp to 33, wait for 10 seconds, and ramp to -12.
 - a. Drag a **Real-Time Sequence Group** to the top of the **Setup** section of the stimulus profile.
 - Drag the existing **Set Single Value** Sequence into the Real-Time Sequence group so that it becomes a child of the sequence group

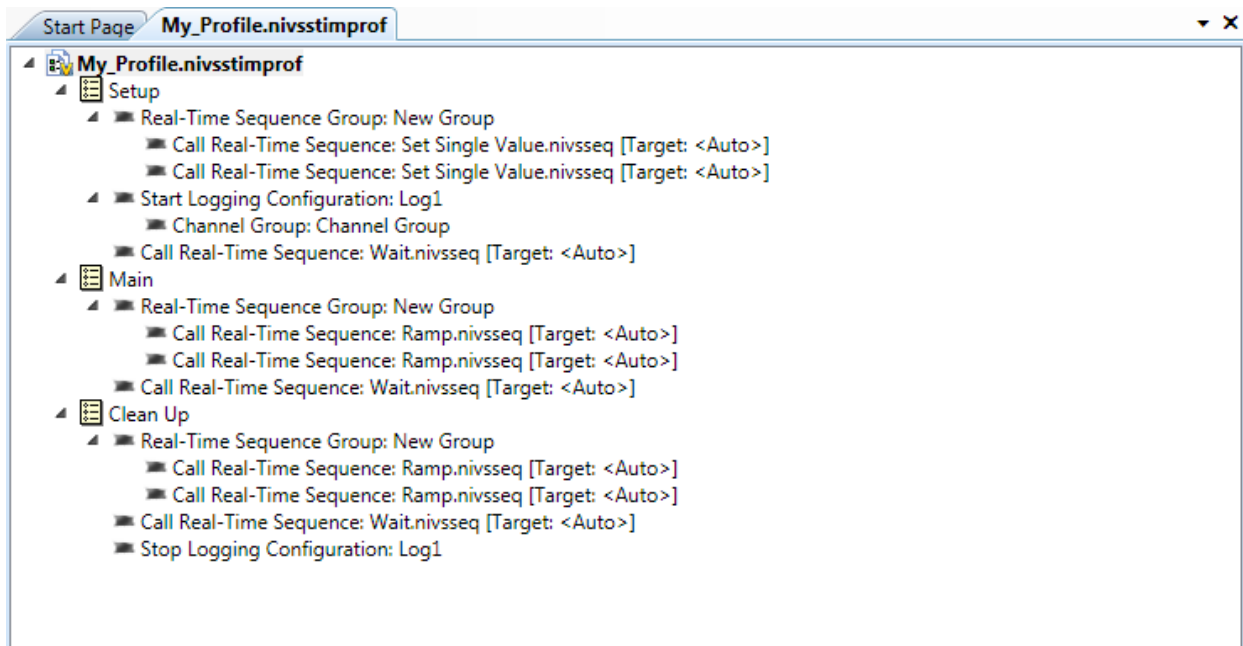


- b. Drag a **Set Single Value** sequence into the new Real-Time Sequence group
 - In the Parameters window, map the **Channel** parameter to the **Travel Setpoint** channel in the System Definition.
 - Click on the Channel radio button associated with **Channel**
 - Click the  and use the search bar to find **Travel Setpoint**.
 - Select **Travel Setpoint**
 - c. Drag a **Real-Time Sequence Group** to the top of the **Main** section of the stimulus profile.

- Drag the existing Ramp Sequence into the Real-Time Sequence group so that it becomes a child of the sequence group
- d. Drag a **Ramp** sequence into the new Real-Time Sequence Group in the **Main** section of the stimulus profile.
 - Set the RampOut Parameter to **Travel Setpoint** by using the same procedure as in step 5b.
 - Set **Initial Value** to 0
 - Set **Final Value** to 33
 - Set **Duration** to 1
- e. Drag a **Real-Time Sequence Group** to the top of the **Clean Up** section of the stimulus profile.
 - Drag the existing Ramp Sequence into the Real-Time Sequence group so that it becomes a child of the sequence group
- f. Drag a **Ramp** sequence into the new Real-Time Sequence Group in the **Clean Up** section of the stimulus profile.
 - Set the RampOut Parameter to **Travel Setpoint** by using the same procedure as in step 5b.
 - Set **Initial Value** to 33
 - Set **Final Value** to -12
 - Set **Duration** to 1
- g. When complete, the profile should look as seen below:




6. We will now configure our stimulus profile to log the Travel Setpoint and Travel Angle channels in our system.
 - a. Drag a **Start Logging** Step into the **Setup** section of the stimulus profile directly above the **Wait** step.
 - b. Click on **Channel Group** directly below the Start Logging step.
 - c. In the Property Browser, click on the  and navigate to **Targets » Controller » Hardware » Chassis » FPGA » RIO0 » Output » Set Points** and Select the **Travel Setpoint**. Also navigate to **Targets » Controller » Simulation Models » Models » Linear Helicopter Model » Outputs » output** and select **Travel Angle(deg)**
 - d. Drag a **Stop Logging** Step to the bottom of the **Clean Up** section of the stimulus profile.
7. Configure the logging file
 - a. The log file will go to the results directory determined by clicking on the stimulus profile header (the top of the stimulus profile). By default, this directory will be <Your Stimulus File Path>\Test Results\UUT 1\
 - b. Leave this as the file directory.



Note: You can have multiple files for logging separate groups of signals or trigger off of different conditions. You can also choose different log rates for each file to allow slower changing channels to be logged separately for optimized performance and disk usage.

8. Save the stimulus profile by clicking the  in the top left corner.

9. Click the  in the top right corner of the Stimulus Profile Editor menu to shrink the window, so that you can view both the Stimulus Profile Editor and the NI VeriStand Workspace simultaneously.
10. Click **Run** on the Stimulus Profile Editor to run the profile.
 - Notice that the Stimulus Profile Editor highlights the active sequence, so that you can tell which part of the profile is running.
 - Observe the helicopter actions and fan power graph.

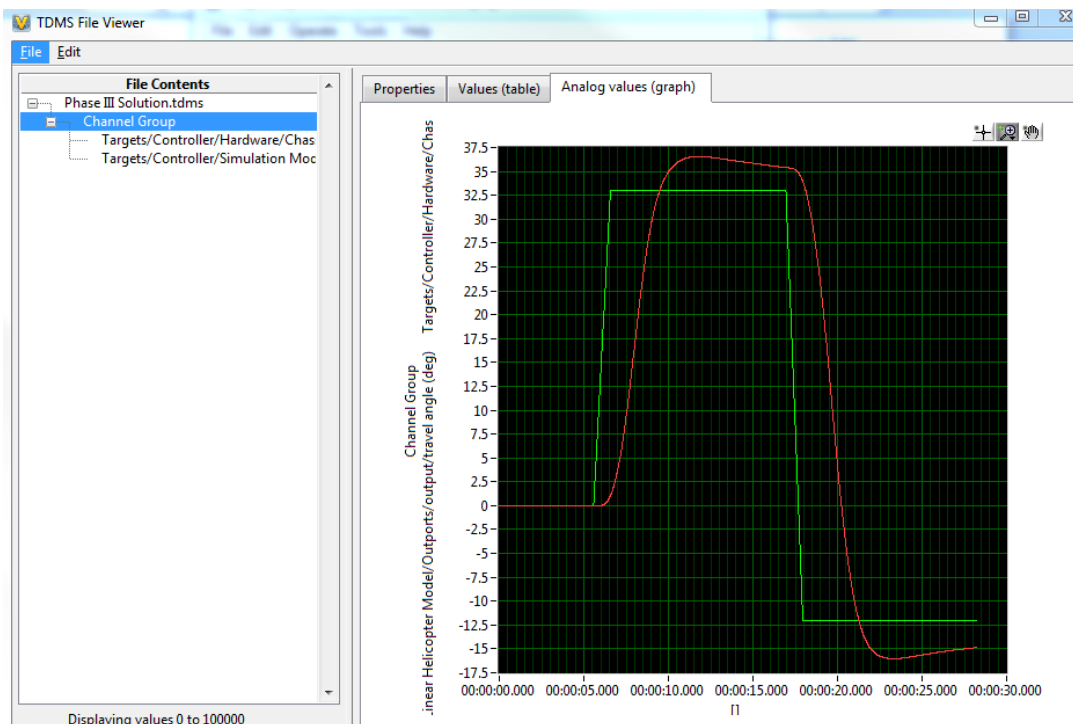


Note: There is a report that gets generated once the Stimulus Profile has completed. This report will display in a web browser, and it includes pass/fail analysis, time stamp information, and general test setup information.

11. Use the TDMS File Viewer Tool to observe the logged data from the profile when it is finished.
 - a. Open the TDMS File Viewer by selecting **Tools » TDMS File Viewer** from the workspace menu.
 - b. Select **File » Open**.
 - c. Find the log file you created at **<Public Documents>National Instruments\NI VeriStand 2014\Projects\Hands On\Stimulus Profiles\Test Results\UUT 1**
 - d. Select the top most tree item in the left pane.
 - e. Select the **Analog values (graph)** tab.



NI DIAdem or Excel can also be used to examine the data log files. Using NI DIAdem, you can load multiple files simultaneously, perform interactive and automated analysis, and configure re-usable report templates for enhanced test data processing.

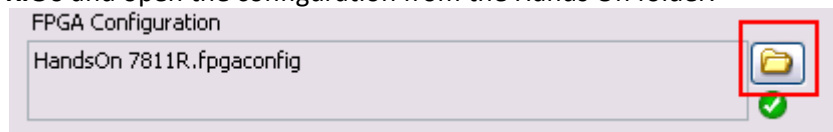


IV. Open & Run Advanced Stimulus Profile *(Optional)*


In this phase, we will look at a pre-made solution which illustrates additional functionality of NI VeriStand. This phase is an example of the additional functionality of Real-Time Sequences, alarms, and procedures. Stimulus profiles can call Real-Time Sequences for the portions of the test that needs to run deterministically. Some or your entire test could be contained in a Real-Time Sequence. This example demonstrates a complete real-time test contained within a Real-Time Sequence and called by a Stimulus Profile.

1. Close the NI VeriStand project.
2. From the Getting Started Window select the **Phase IV Solution**.
 - a. If **Phase IV Solution** is not located here, select the **Browse** folder, open the **<Public Documents>\National Instruments\NI VeriStand 2014\Projects\Hands On** folder, and select **Phase IV Solution.nivssproj**.
 - b. Select **Configure Project** to open the project.

Note: You may need to change the FPGA Configuration to match you R-Series DAQ board. In the **System Explorer**, select **Controller » Hardware » Chassis » FPGA » RIO0** and open the configuration from the Hands On folder.




3. Navigate through the System Explorer to view the additional functionality:
 - a. Expand the **System Definition File** section.
 - b. Right click the **Phase IV Solution.nivssdf** file and select **Launch System Explorer**.
 - c. Select **User Channels**, **Procedures**, and **Alarms** to see the new additions to the configuration.
 - The Alarm, “Reset has been pressed”, is used to monitor the User Channel, “Reset HIL.” When this User Channel has exceeded the alarm limits, the alarm triggers and calls the “Reset HIL” Procedure.
4. **Close** the System Explorer.
5. Select **Operate » Run** to deploy the Phase IV solution configuration to the Real-Time system and open the workspace.

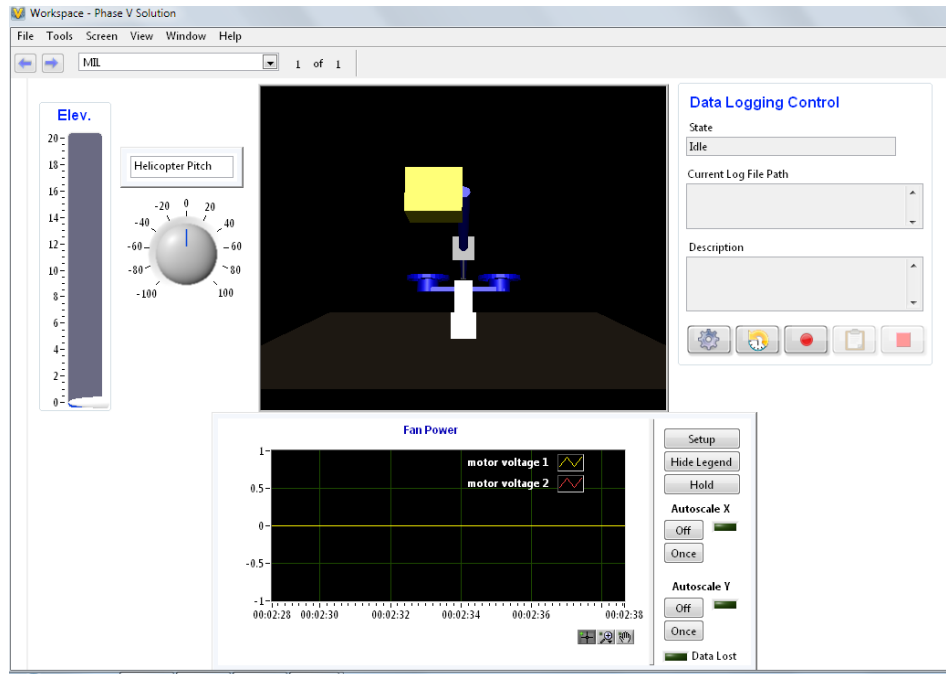
6. Open the pre-made Phase IV Solution.nivsstimprof stimulus profile.
 - a. Expand the **Profiles** section of the project explorer.
 - b. Double click **Phase IV Solution.nivsstimprof**.
7. The Stimulus Profile Editor will open and allow you to view the stimulus profile.
 - This stimulus profile resets the model and helicopter, and then calls a Real-Time Sequence to send the helicopter to four different travel and elevation set point pairs.
8. In the Sequences Palette, expand the **<Current Document Folder>** menu and double click **CheckElevationAndTravel.nivsseq** to open and view its functionality.
 - Set point groups are used to organize the sequence
 - Inside each set point group, the Real-Time Sequence sets setpoint channel values and waits until the helicopter is within specific tolerances of the desired value. Once the helicopter is within these tolerances for 3 seconds (or a 30 second timeout), the profile moves on to the next set point pair.
 - Each group also keeps track of pass fail status and the Real-Time Sequence returns the final pass/fail status at the end
9. Select the  in the top right corner of the Stimulus Profile Editor menu to shrink the window, so that you can view both the Stimulus Profile Editor and the NI VeriStand Workspace simultaneously.
10. Select **Run**.


V. Configure Logging and Analyze Data in DIAdem

We have learned how to configure and execute stimulus profiles, which include signal generation, test automation, and data logging. Now we will learn how to configure data logging directly from the Workspace, and we will see how NI VeriStand can be used with NI DIAdem to perform post processing of data and generate reports. Using NI VeriStand and DIAdem together enables you to automate data analysis and report generation, so that every test produces consistent data that can be easily compared to previous test results.

1. Open DIAdem and open NI VeriStand if it is not already open.
2. In NI VeriStand, open your completed Phase II project. If you did not finish Phase II, please open the **Phase II Solution** for this exercise and use it to complete this phase.
 - a. Select the **Phase II Solution** project file on the left side of the NI VeriStand Getting Started Window.
 - b. If **Phase II Solution** is not located here, select the **Browse** folder, open the **<Public Documents>\National Instruments\NI VeriStand 2014\Projects\Hands-On** folder, and select **Phase II Solution.nivproj**.
3. Select **Configure Project** to open the Project Explorer.

4. Select the  to deploy and run your configuration.
5. Enable Edit Mode. Select **Screen » Edit Mode** (Ctrl + M).
6. From the Workspace Controls (on the left), click and drag to add a Logging Control.
7. Disable edit mode. Select **Screen » Edit Mode** (Ctrl + M).

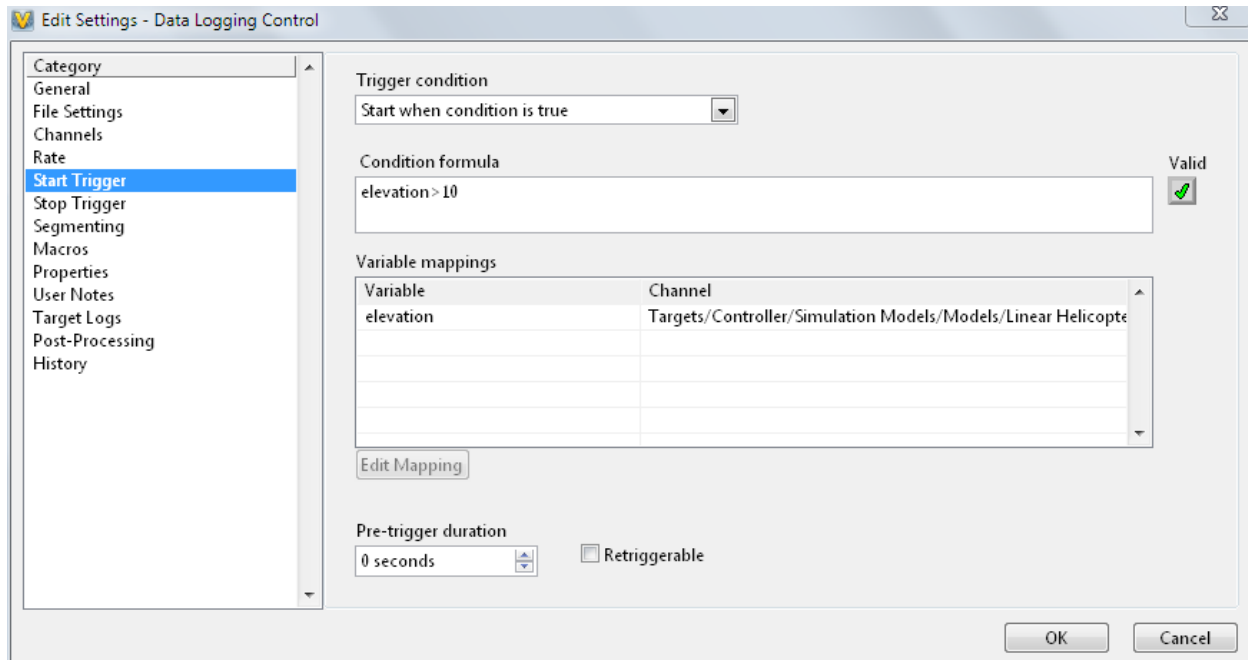


8. Select the  on the data logging control to open the data logging configuration window.

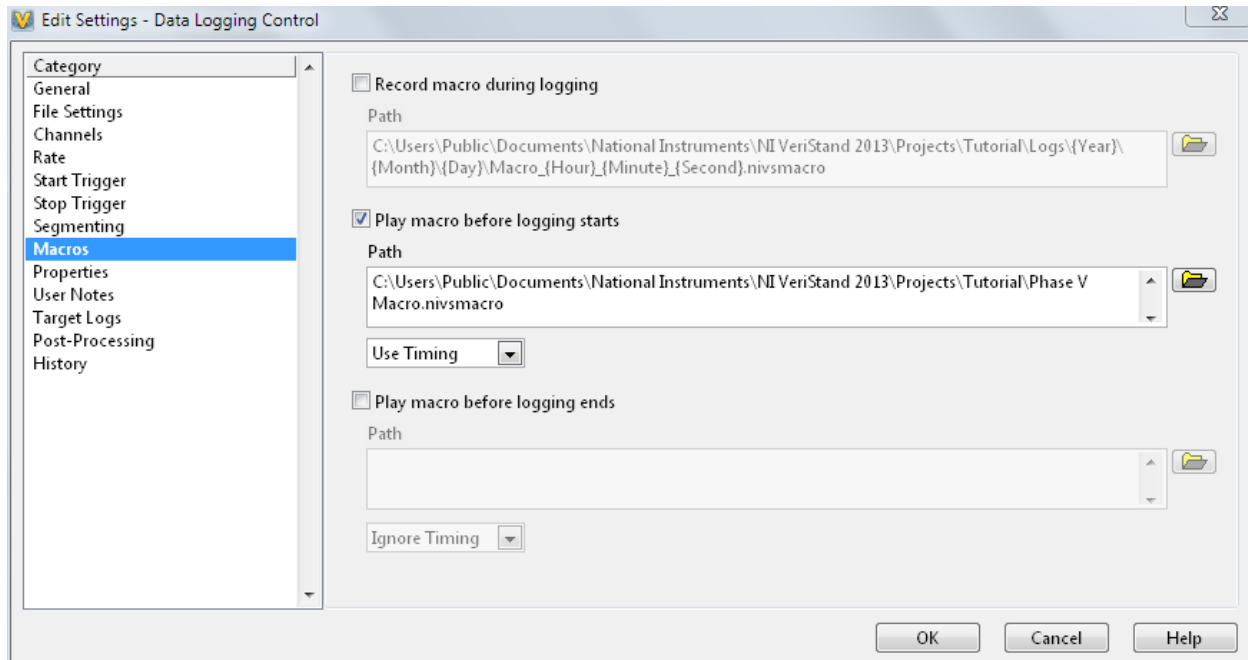



The data logging configuration window allows you to set many different data logging options. While this Hands-On will walk through some of the options, please look through all of the different menus to see all of the different settings that can be configured. For a demonstration about how to use this data logging control, watch the Combining NI VeriStand Software and NI DIAdem Software to Improve Real-Time Test Data Management video at <http://www.ni.com/white-paper/14830/en/>.

9. Select the **Channels Menu**
 - a. Select **Add Channels**
 - b. Navigate to **Simulation Models » Linear Helicopter Model » Inports** and expand the **Inports** menu. Select **motor voltage 1** and **motor voltage 2**.
 - c. Select **OK**
10. Select **Start Trigger**.



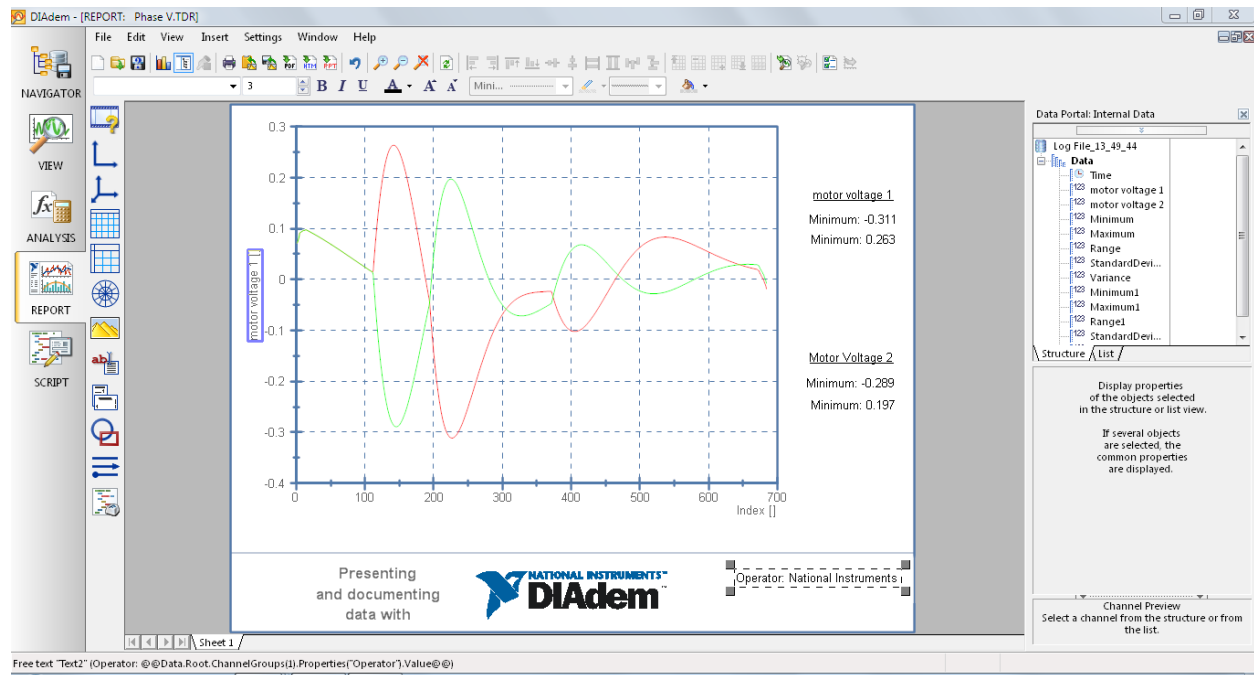
- a. Under Trigger condition, select the drop down arrow and select **Start when condition is true**.
 - b. Select the box labeled **Condition formula**.
 - c. Type **elevation>10** in the box.
 - d. Select the box labeled **Variable mappings**. Notice that the elevation variable appears.
 - e. Select the **elevation variable**
 - f. Select **Edit mapping**.
 - g. In the Channel selection box, search for **Elevation Set Point** and select it.
 - h. Select **OK**.
11. Select **Stop Trigger**.
- a. Under Trigger condition, select the drop down arrow and select **Start when condition is true**.
 - b. Select the box labeled **Condition formula**.
 - c. Type **elevation<10** in the box.
 - d. Select the box labeled **Variable mappings**. Notice that the elevation variable appears.
 - e. Select the **elevation variable**
 - f. Select **Edit mapping**.
 - g. In the Channel selection box, search for **Elevation Set Point** and select it.
 - h. Select **OK**.
12. Select **Macros**.



- a. Check the box that says **Play macro before logging starts**.
 - b. Browse to the <**Public Documents**>\National Instruments\NI VeriStand **2014\Projects\Hands-On** and select the **Phase V Macro.nivsmacro**.
 - c. Select **OK**
 - d. In the drop down below the file path, select the drop down arrow and then select **Use Timing**.
13. Select **Properties**
- a. Select the check box that says **Prompt user to enter log file properties when starting a log**.
 - b. Select **Add**
 - c. In the New Property Window, type **Operator** in the **Property Name** box.
14. Select **Post-Processing**
- a. Select the drop down arrow below *Action to take at the end of log session* and select **Load File in DIAdem**.
 - b. Check the box that says **Run script**.
 - c. Browse to <**Public Documents**>\National Instruments\NI VeriStand **2014\Projects\Hands-On\DIAdem Files** and select **Phase V.vbs**.
 - d. Select the drop down arrow below *Report generation options* and select **Generate PDF report**.
 - e. Browse to <**Public Documents**>\National Instruments\NI VeriStand **2014\Projects\Hands-On\DIAdem Files** and select **Phase V.tdr**.
15. Select **OK**
16. Select the  button
17. In the Log Properties dialog box, type your name underneath value.
- a. Select **OK** to close the window.

Note: The macro will now begin to run, and the log control will wait for a trigger condition. Remember that your trigger condition is to begin logging when the elevation setpoint is greater than 10.

18. Raise the value of the **elevation setpoint** above 10.
 - a. If the macro has finished, you will begin to log data. If the macro is still running, hold the elevation setpoint above 10 until after the macro has finished and observe that data logging has begun.
19. Let NI VeriStand log data for a few seconds and then lower the value of **the elevation setpoint** below 10. Data logging will finish and DIAdem will pop up showing a report.
20. Go back to NI VeriStand and observe the File History window.
21. Double click on the bottom file in the Reports folder. This will open up the PDF report that was created from your data log.



Summary

This hands-on exercise demonstrated some features and capabilities of NI VeriStand. If you would like to learn more about NI VeriStand, please visit ni.com/VeriStand for demonstration videos, whitepapers, Hands-Ons, and add-ons such as custom workspace elements and custom devices to provide additional functionality for your NI VeriStand applications. Here you can download a fully featured evaluation version of NI VeriStand that you can use free for 30 days. For more information about how NI VeriStand can help you create your real-time testing applications more efficiently, request a free on-site consultation from your local NI Field Engineer.