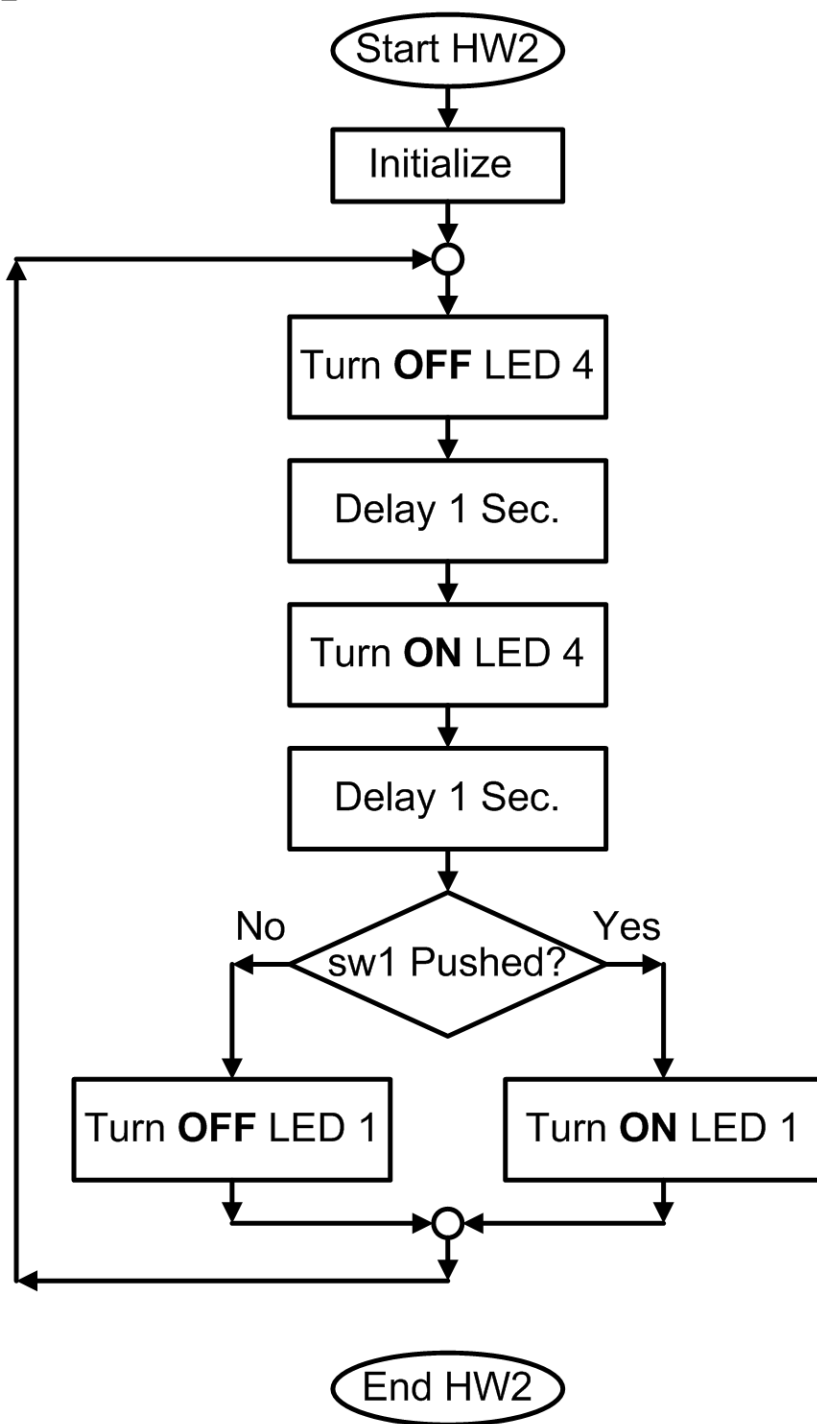
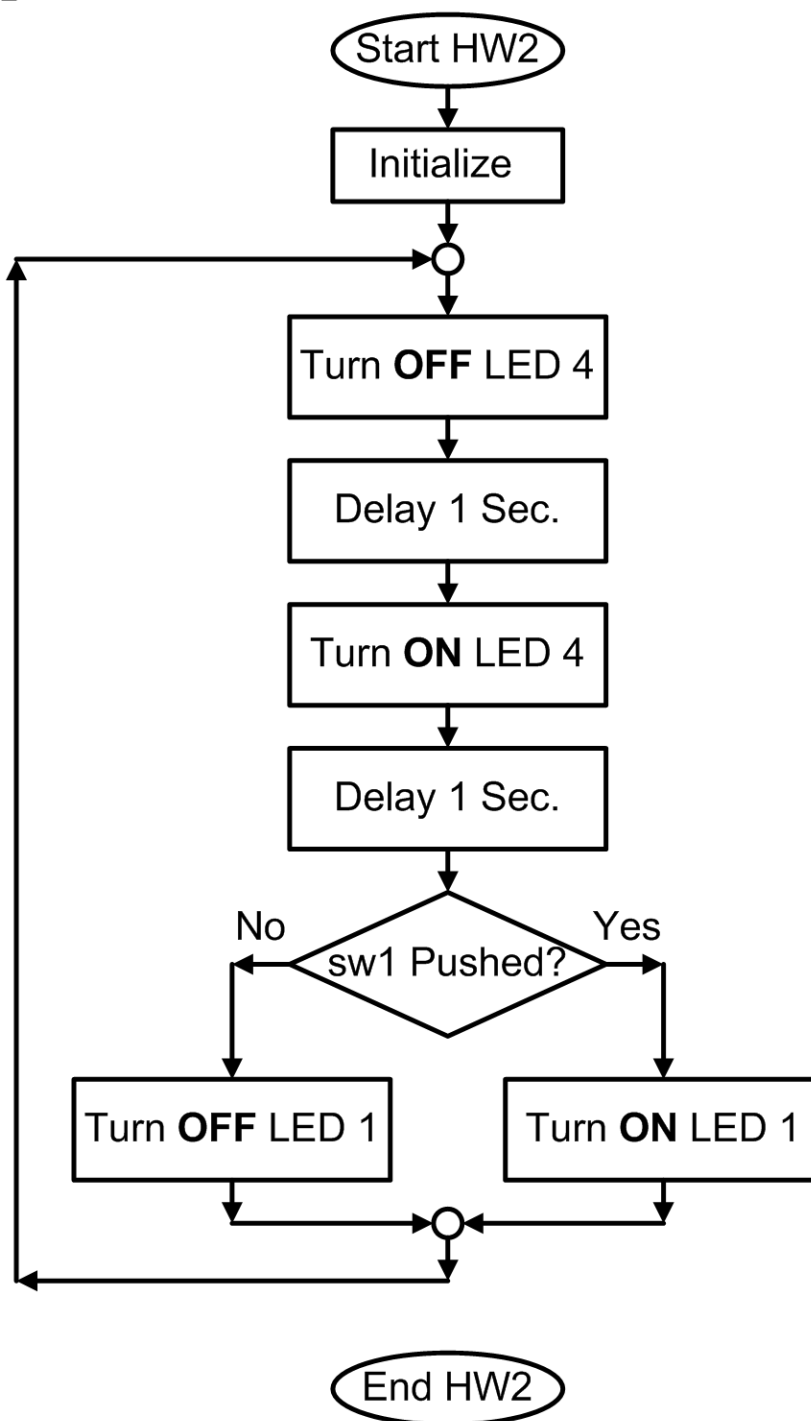


Stack  
PORTB  
PORTP



Stack  
PORTB  
PORTP

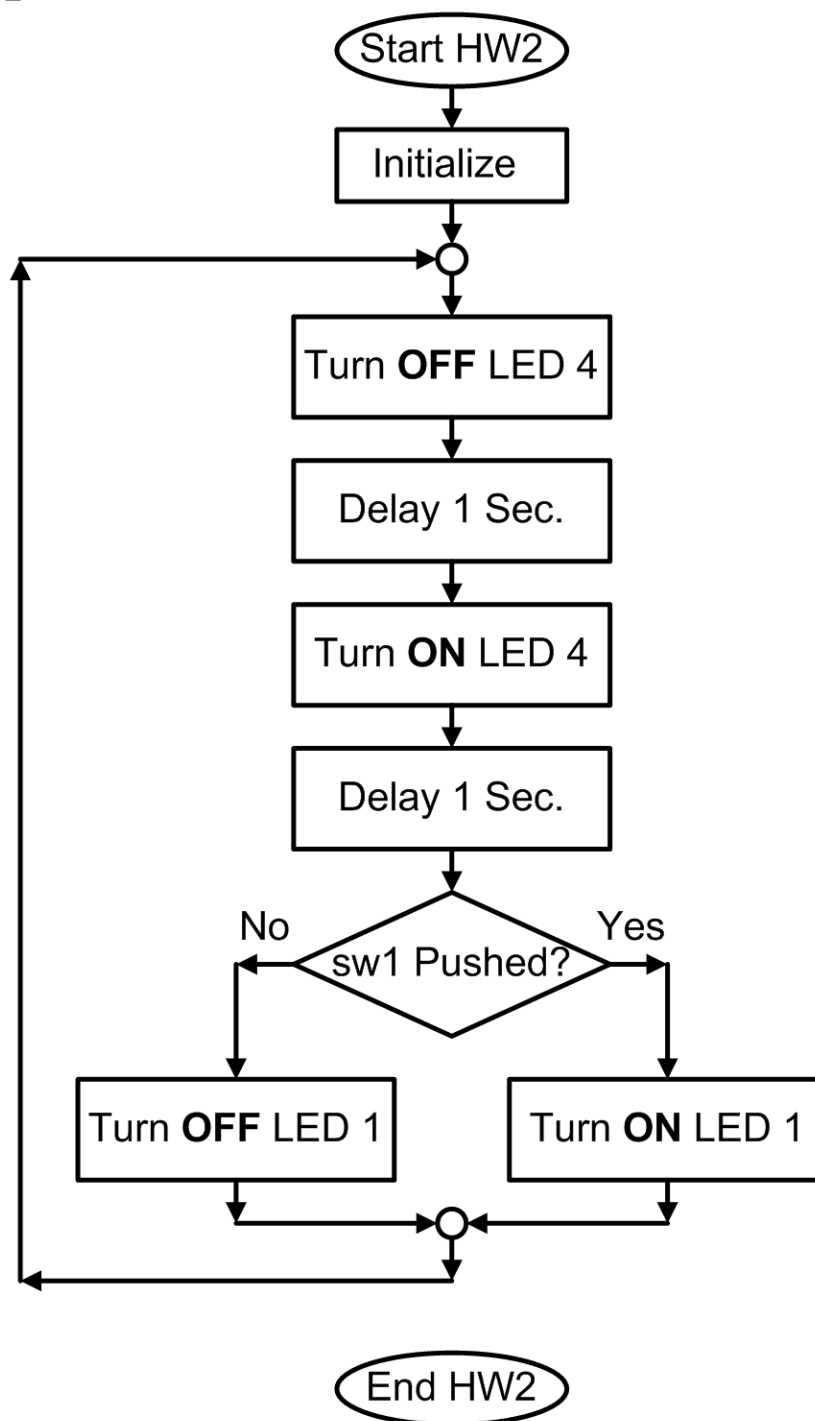
BSET PORTB,%10000000



Stack  
PORTB  
PORTP

BSET PORTB,%10000000

JSR delay1sec



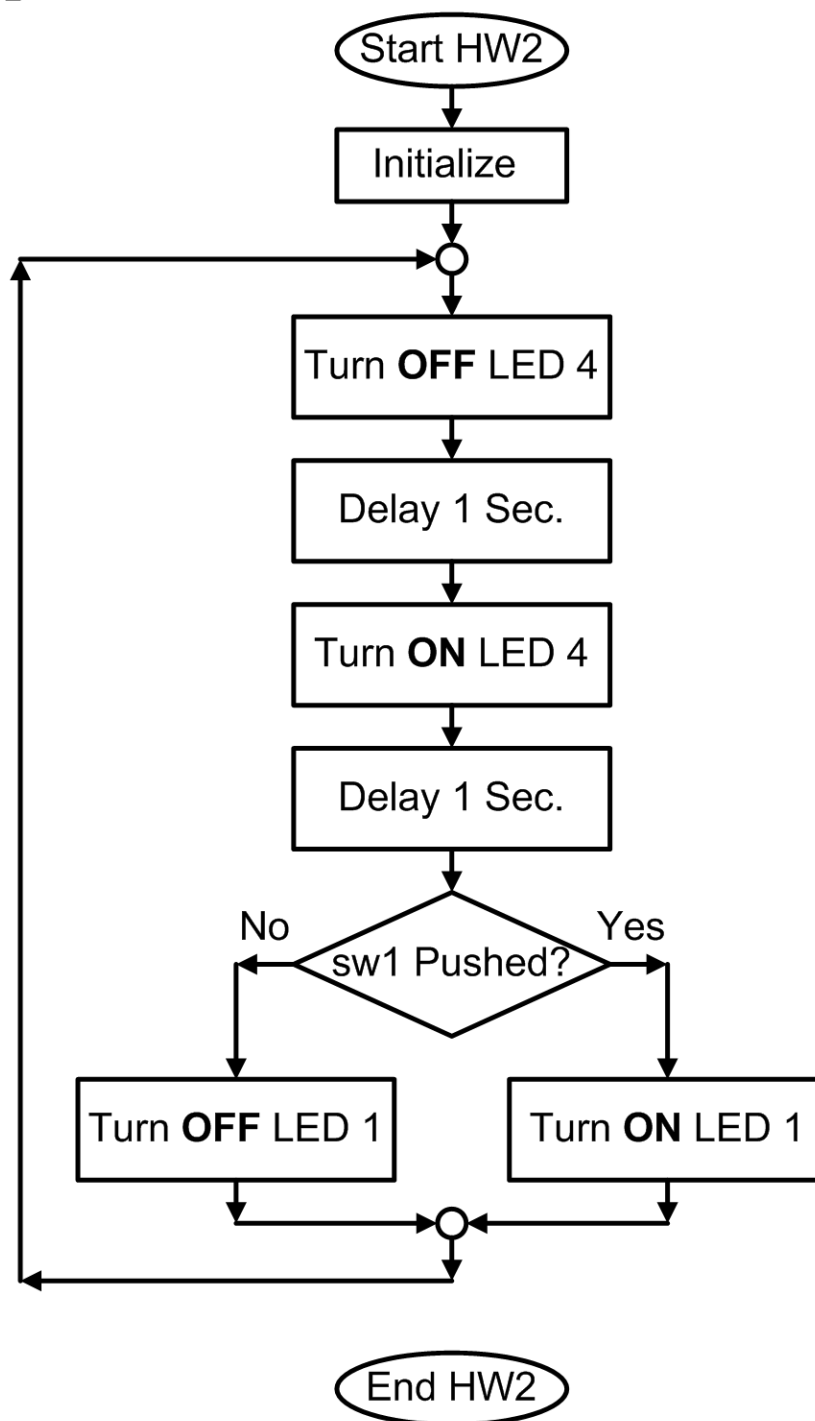
Stack  
PORTB  
PORTP

BSET PORTB,%10000000

JSR delay1sec

BCLR PORTB,%10000000

JSR delay1sec



Stack  
PORTB  
PORTP

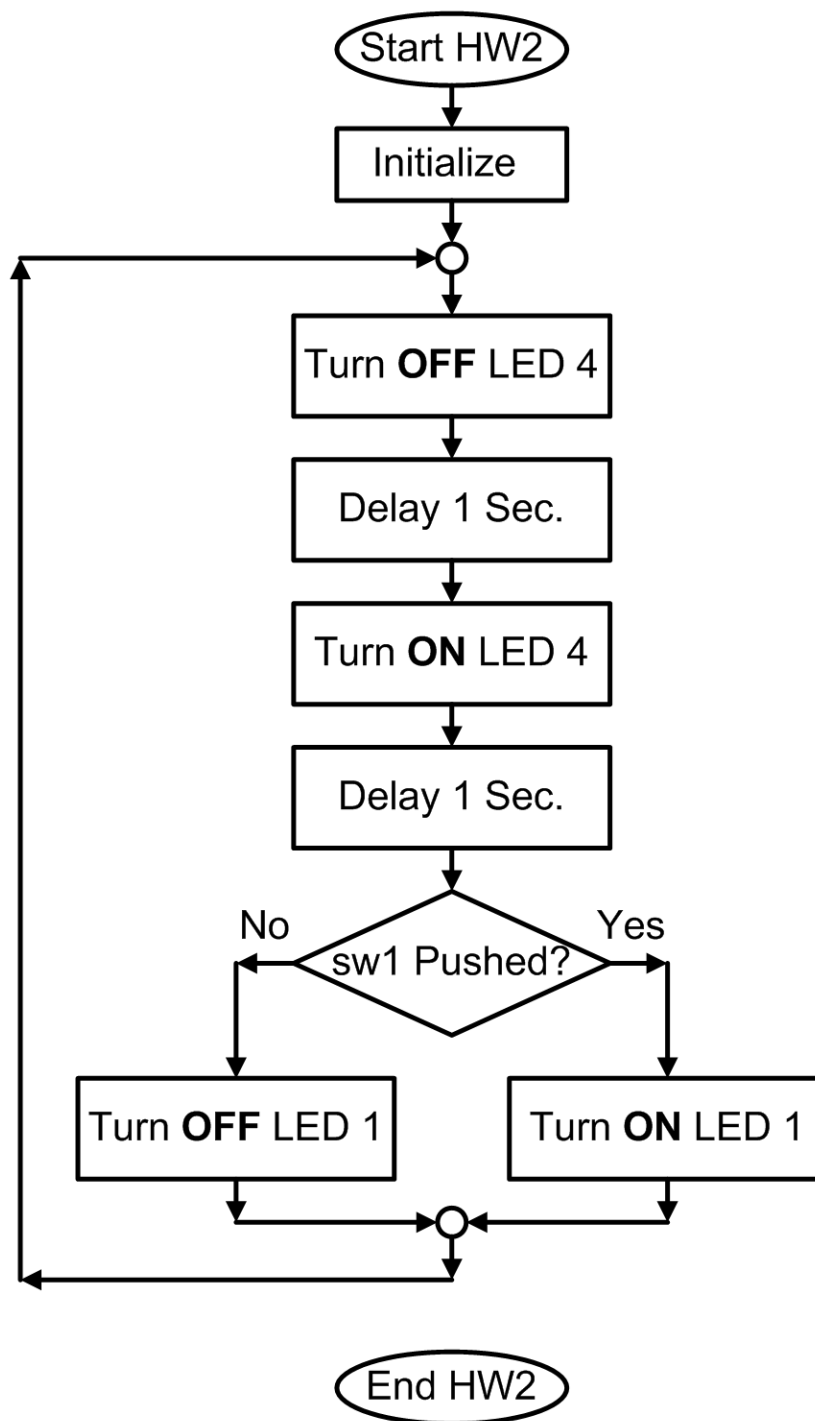
BSET PORTB,%10000000

JSR delay1sec

BCLR PORTB,%10000000

JSR delay1sec

LDAA PTIP  
ANDAA #%00000001  
BEQ sw1pushed



Stack  
PORTB  
PORTP

BSET PORTB,%10000000

JSR delay1sec

BCLR PORTB,%10000000

JSR delay1sec

LDAA PTIP  
ANDAA #%00000001  
BEQ sw1pushed

BSET PORTB,%00010000  
BRA mainLoop

BCLR PORTB,%00010000  
BRA mainLoop

