

Header 7

Pin	Signal
1	DEBUG TCK
2	DEBUG TMS
3	DEBUG TDO
4	DEBUG TDI
5	
6	PA0/U0RX VCP TXD
7	PA1/U0TX VCP RXD

Pin 6 is connected to GND.

Diagram showing the connection for the PE0 and PE1 pins. PE0 (RX) is connected to 5V. PE1 (TX) is connected to a 10k pull-down resistor (C24) to ground. Header 4 is also connected to ground.

[illegible]

Diagram of the 74HC245 buffer IC. The IC is shown with its pin numbers 1 through 10 on the left and 20 through 11 on the right. The connections are as follows:

- Pin 1: PB6_PWM0
- Pin 2: PB7_PWM1
- Pin 3: PB4_PWM2
- Pin 4: PB5_PWM3
- Pin 5: PB0_LDDISP0
- Pin 6: PB1_LDDISP1
- Pin 7: PC7_LDDISP2
- Pin 8: PC4_LDDISP3
- Pin 9: GND
- Pin 10: GND
- Pin 20: VCC
- Pin 19: VCC
- Pin 18: GND
- Pin 17: GND
- Pin 16: GND
- Pin 15: GND
- Pin 14: GND
- Pin 13: GND
- Pin 12: GND
- Pin 11: GND
- Pin 10: GND
- Pin 9: GND
- Pin 8: GND
- Pin 7: GND
- Pin 6: GND
- Pin 5: GND
- Pin 4: GND
- Pin 3: GND
- Pin 2: GND
- Pin 1: GND

The first diagram shows header P1 connected to pins A0, B0, and the ground pin. A 104 capacitor is connected between the header ground and the VCC pin. The second diagram shows header P4 connected to pins A1, B1, and the ground pin. A 104 capacitor is connected between the header ground and the VCC pin.

Diagram of Header 5 pin connections:

- Pin 1: PB6_PWM0
- Pin 2: PB7_PWM1
- Pin 3: PB4_PWM2
- Pin 4: PB5_PWM3
- Pin 5: GND

Title				TIVA MAINBOARD CONTROL			
Size B		Number			Revision V1.0		
Date: 20/08/2017		Sheet of		Drawn By: <u>Dang Thanh Tung</u>			
File: C:\Users\...Tiva Control.SchDoc							