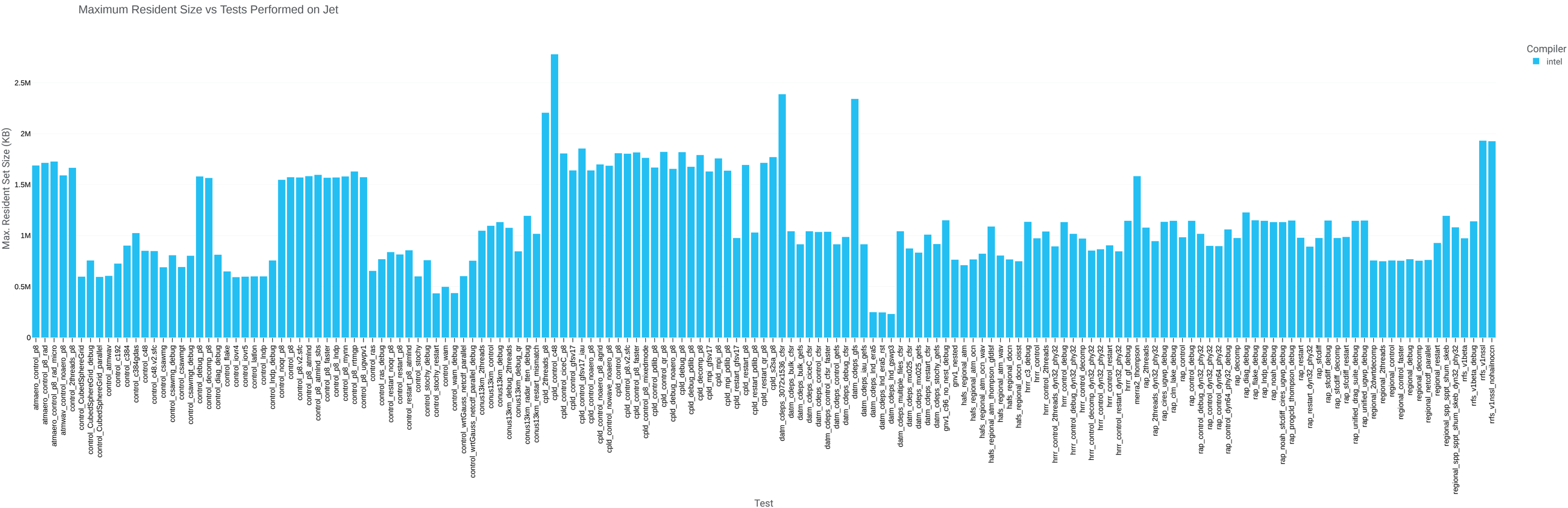
Maximum Resident Size vs Tests Performed on Jet
Compiler
intel
Max. Resident Set Size (KB)
2.5M
2M
1.5M
1M
0.5M
0
Test
atmaero_control_p8
atmaero_control_p8_rad
atmaero_control_p8_rad_micro
atmwav_control_noaero_p8
control_2threads_p8
control_CubedSphereGrid
control_CubedSphereGrid_debug
control_CubedSphereGrid_parallel
control_atmwav
control_c192
control_c384
control_c384gdas
control_c48
control_c48.v2.sfc
control_csawmg
control_csawmg_debug
control_csawmgt
control_csawmgt_debug
control_debug_p8
control_decomp_p8
control_diag_debug
control_flake
control_iovr4
control_iovr5
control_latlon
control_lndp
control_lndp_debug
control_noqr_p8
control_p8
control_p8.v2.sfc
control_p8_atmind
control_p8_atmind_sbs
control_p8_faster
control_p8_lndp
control_p8_mynn
control_p8_rrtmgp
control_p8_ugwpv1
control_ras
control_ras_debug
control_restart_noqr_p8
control_restart_p8
control_restart_p8_atmind
control_stochy
control_stochy_debug
control_stochy_restart
control_wam
control_wam_debug
control_wrtGauss_netcdf_parallel
control_wrtGauss_netcdf_parallel_debug
conus13km_2threads
conus13km_control
conus13km_debug
conus13km_debug_2threads
conus13km_debug_qr
conus13km_radar_ften_debug
conus13km_restart_mismatch
cpld_2threads_p8
cpld_control_c48
cpld_control_diceC_p8
cpld_control_gfsv17
cpld_control_gfsv17_rau
cpld_control_noaero_p8
cpld_control_noaero_p8_agrid
cpld_control_nowave_noaero_p8
cpld_control_p8
cpld_control_p8.v2.sfc
cpld_control_p8_faster
cpld_control_p8_mixedmode
cpld_control_pdlib_p8
cpld_control_qr_p8
cpld_debug_noaero_p8
cpld_debug_p8
cpld_debug_pdlib_p8
cpld_decomp_p8
cpld_mpi_gfsv17
cpld_mpi_p8
cpld_mpi_pdlib_p8
cpld_restart_gfsv17
cpld_restart_p8
cpld_restart_pdlib_p8
cpld_restart_qr_p8
cpld_s2sa_p8
datm_cdeps_3072x1536_cfsr
datm_cdeps_bulk_cfsr
datm_cdeps_bulk_gefs
datm_cdeps_diceC_cfsr
datm_cdeps_control_cfsr
datm_cdeps_control_cfsr_faster
datm_cdeps_control_gefs
datm_cdeps_debug_cfsr
datm_cdeps_gfs
datm_cdeps_iau_gefs
datm_cdeps_lnd_era5
datm_cdeps_lnd_era5_rst
datm_cdeps_lnd_gswp3
datm_cdeps_multiple_files_cfsr
datm_cdeps_mx025_cfsr
datm_cdeps_mx025_gefs
datm_cdeps_restart_cfsr
datm_cdeps_stochy_gefs
gnv1_c96_no_rest_debug
gnv1_nested
hafs_regional_atm
hafs_regional_atm_ocn
hafs_regional_atm_ocn_wav
hafs_regional_atm_thompson_gfdlsf
hafs_regional_atm_wav
hafs_regional_docn
hafs_regional_docn_oisst
hrrr_c3_debug
hrrr_control
hrrr_control_2threads
hrrr_control_2threads_dyn32_phy32
hrrr_control_debug
hrrr_control_debug_dyn32_phy32
hrrr_control_decomp
hrrr_control_decomp_dyn32_phy32
hrrr_control_dyn32_phy32
hrrr_control_restart
hrrr_control_restart_dyn32_phy32
hrrr_gf_debug
merra2_thompson
rap_2threads
rap_2threads_dyn32_phy32
rap_cires_ugwp_debug
rap_clm_lake_debug
rap_control
rap_control_debug
rap_control_debug_dyn32_phy32
rap_control_dyn32_phy32
rap_control_dyn64_phy32
rap_control_dyn64_phy32_debug
rap_decomp
rap_diag_debug
rap_flake_debug
rap_lndp_debug
rap_noah_debug
rap_noah_sfcdiff_cires_ugwp_debug
rap_progcld_thompson_debug
rap_restart
rap_restart_dyn32_phy32
rap_sfcdiff
rap_sfcdiff_debug
rap_sfcdiff_decomp
rap_sfcdiff_restart
rap_unified_diag_suite_debug
rap_unified_ugwp_debug
regional_2dwrtdecomp
regional_2threads
regional_control
regional_control_faster
regional_debug
regional_decomp
regional_netcdf_parallel
regional_restart
regional_spp_sppt_shum_skeb
regional_spp_sppt_shum_skeb_dyn32_phy32
rrfs_v1beta
rrfs_v1beta_debug
rrfs_v1nssl
rrfs_v1nssl_nohailnoccn