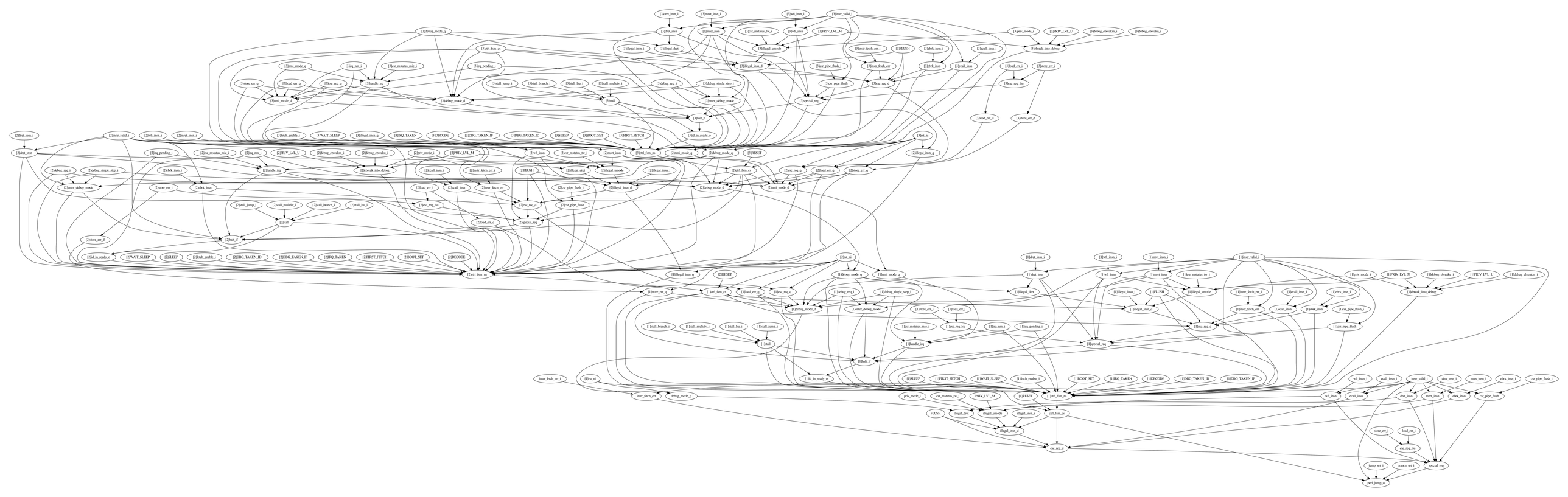
(3)dret_insn_i
(3)mret_insn_i
(3)wfi_insn_i
(3)instr_valid_i
(3)debug_mode_q
(3)dret_insn
(3)mret_insn
(3)csr_mstatus_tw_i
(3)wfi_insn
(3)PRIV_LVL_M
(3)priv_mode_i
(3)PRIV_LVL_U
(3)debug_ebreaku_i
(3)debug_ebreakm_i
(3)illegal_insn_i
(3)illegal_dret
(3)illegal_mret
(3)break_into_debug
(3)ctrl_fsm_cs
(3)illegal_insn_d
(3)instr_fetch_err_i
(3)FLUSH
(3)ebrk_insn_i
(3)ecall_insn_i
(3)csr_pipe_flush_i
(3)instr_fetch_err
(3)ebrk_insn
(3)ecall_insn
(3)load_err_i
(3)store_err_i
(3)csr_pipe_flush
(3)exc_req_i
(3)exc_req_lsu
(3)mmi_mode_q
(3)store_err_q
(3)load_err_q
(3)exc_req_q
(3)irq_nm_i
(3)irq_pending_i
(3)bundle_irq
(3)jump_set_i
(3)branch_set_i
(3)load_err_i
(3)store_err_i
(3)debug_mode_d
(3)jump_set_i
(3)branch_set_i
(3)debug_single_step_i
(3)debug_req_i
(3)mmi_mode_d
(3)stall
(3)special_req
(3)halt_if
(3)id_in_ready_o
(3)FIRST_FETCH
(3)BOOT_SET
(3)SLEEP
(3)DBG_TAKEN_ID
(3)DBG_TAKEN_IF
(3)IRQ_TAKEN
(3)DECODE
(3)WAIT_SLEEP
(3)fetch_enable_i
(2)dret_insn_i
(2)wfi_insn_i
(2)mret_insn_i
(2)instr_valid_i
(2)st_ns
(2)dret_insn
(2)wfi_insn
(2)mret_insn
(2)csr_mstatus_tw_i
(2)illegal_insn_q
(2)irq_pending_i
(2)irq_nm_i
(2)PRIV_LVL_U
(2)debug_ebreaku_i
(2)debug_ebreakm_i
(2)priv_mode_i
(2)PRIV_LVL_M
(2)ebrk_insn
(2)ctrl_fsm_cs
(2)debug_mode_q
(2)RESET
(2)break_into_debug
(2)illegal_dret
(2)illegal_mret
(2)illegal_insn_i
(2)debug_req_i
(2)debug_single_step_i
(2)ebrk_insn_i
(2)ecall_insn_i
(2)instr_fetch_err_i
(2)FLUSH
(2)exc_req_q
(2)load_err_q
(2)store_err_q
(2)interr_debug_mode
(2)store_err_i
(2)ebrk_insn
(2)load_err_i
(2)ecall_insn
(2)instr_fetch_err
(2)csr_pipe_flush_i
(2)illegal_insn_d
(2)debug_mode_d
(2)mmi_mode_d
(2)exc_req_lsu
(2)exc_req_d
(2)csr_pipe_flush
(2)stall_jump_i
(2)stall_multdiv_i
(2)stall_branch_i
(2)stall_lsu_i
(2)load_err_d
(2)special_req
(2)stall
(2)store_err_d
(2)halt_if
(2)id_in_ready_o
(2)WAIT_SLEEP
(2)SLEEP
(2)fetch_enable_i
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(2)DBG_TAKEN_IF
(2)IRQ_TAKEN
(2)FIRST_FETCH
(2)BOOT_SET
(2)DECODE
(2)ctrl_fsm_ns
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(1)store_err_q
(1)ctrl_fsm_cs
(1)load_err_q
(1)exc_req_q
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(1)debug_single_step_i
(1)debug_mode_d
(1)interr_debug_mode
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(1)load_err_i
(1)csr_mstatus_mie_i
(1)exc_req_lsu
(1)irq_nm_i
(1)irq_pending_i
(1)handle_irq
(1)special_req
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(1)stall_multdiv_i
(1)stall_lsu_i
(1)stall_jump_i
(1)stall
(1)halt_if
(1)id_in_ready_o
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(1)wfi_insn_i
(1)mret_insn_i
(1)instr_valid_i
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(1)wfi_insn
(1)mret_insn
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(1)PRIV_LVL_M
(1)debug_ebreaku_i
(1)PRIV_LVL_U
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(1)illegal_mret
(1)illegal_insn_i
(1)FLUSH
(1)illegal_insn_d
(1)break_into_debug
(1)instr_fetch_err_i
(1)ecall_insn_i
(1)ebrk_insn_i
(1)instr_fetch_err
(1)ecall_insn
(1)ebrk_insn
(1)csr_pipe_flush_i
(1)csr_pipe_flush
(1)exc_req_d
(1)special_req
(1)SLEEP
(1)FIRST_FETCH
(1)WAIT_SLEEP
(1)fetch_enable_i
(1)BOOT_SET
(1)IRQ_TAKEN
(1)DECODE
(1)DBG_TAKEN_ID
(1)DBG_TAKEN_IF
(1)ctrl_fsm_ns
instr_fetch_err_i
(1)st_ns
instr_fetch_err
debug_mode_q
priv_mode_i
csr_mstatus_tw_i
PRIV_LVL_M
(1)RESET
FLUSH
illegal_dret
illegal_mret
illegal_insn_i
ctrl_fsm_cs
illegal_insn_d
exc_req_d
wfi_insn_i
ecall_insn_i
instr_valid_i
dret_insn_i
mret_insn_i
ebrk_insn_i
csr_pipe_flush_i
dret_insn
mret_insn
ebrk_insn
csr_pipe_flush
store_err_i
load_err_i
exc_req_lsu
jump_set_i
branch_set_i
special_req
perf_jump_o