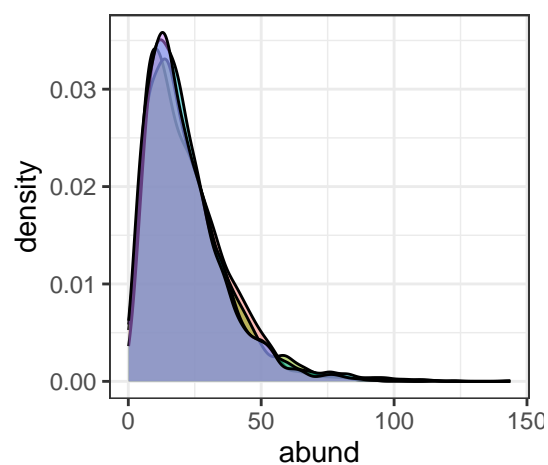
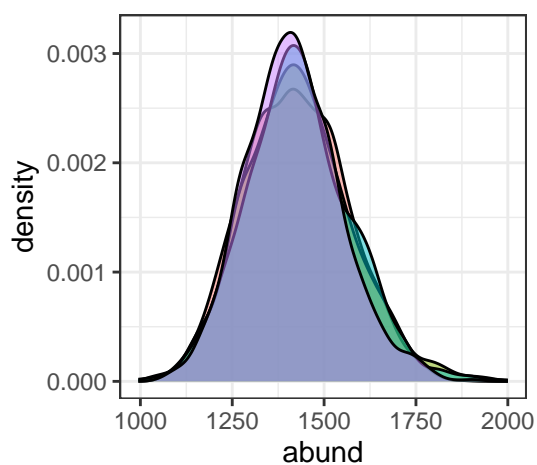


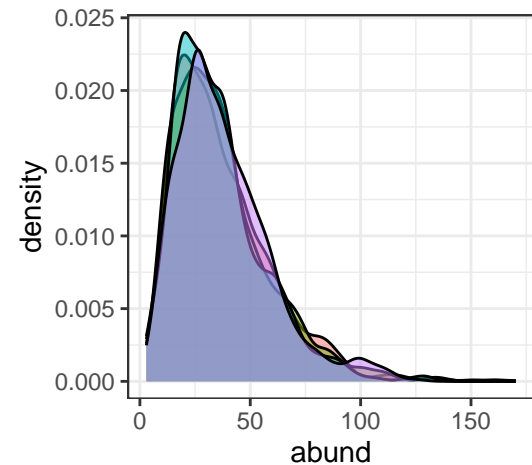
ACB



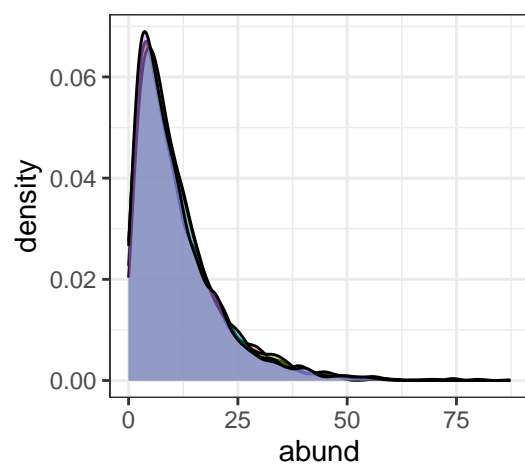
BRC



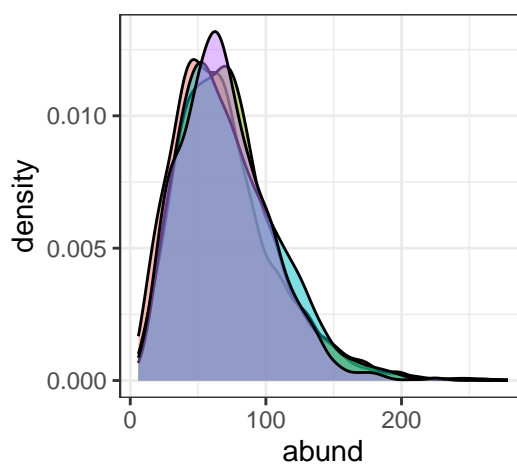
CLC



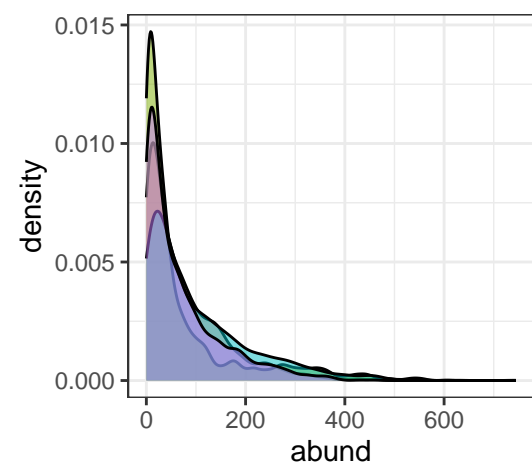
ACB_bb



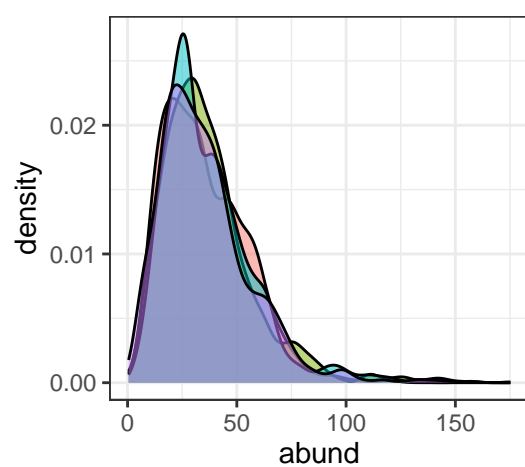
BSC



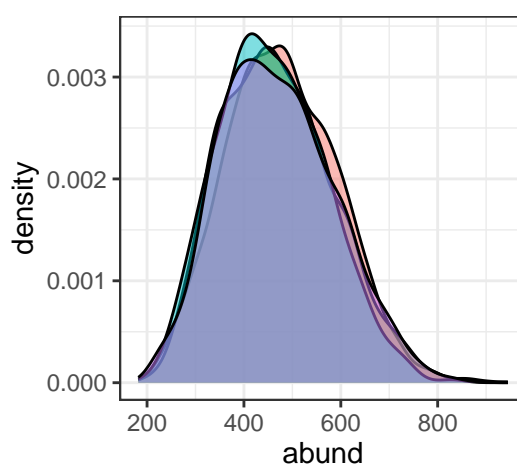
CRA



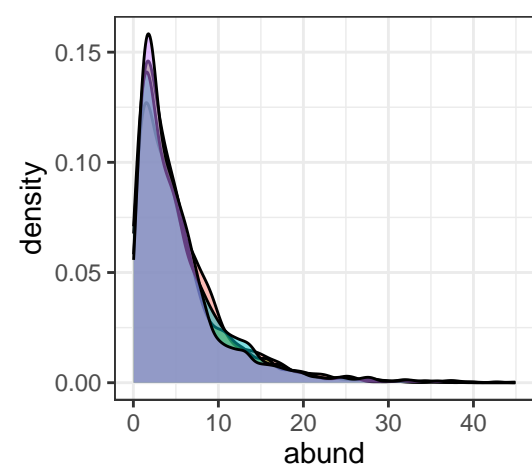
ACM



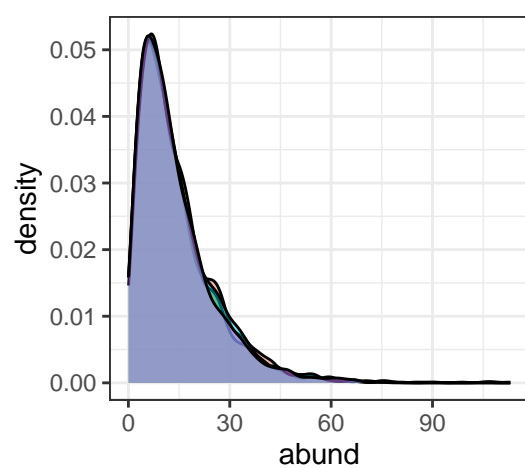
CCU



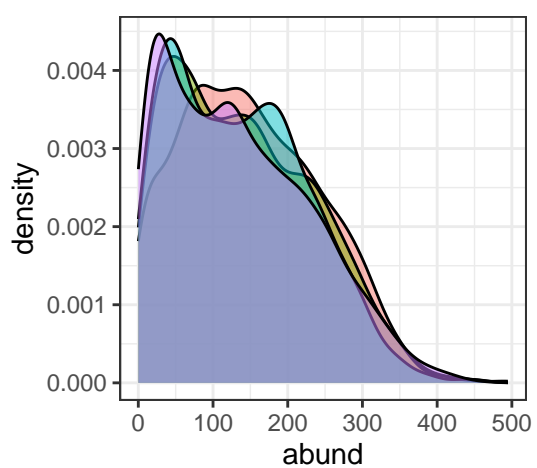
DRM



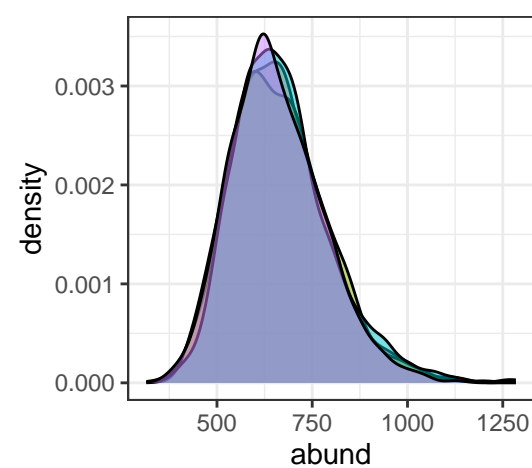
ACM_bb



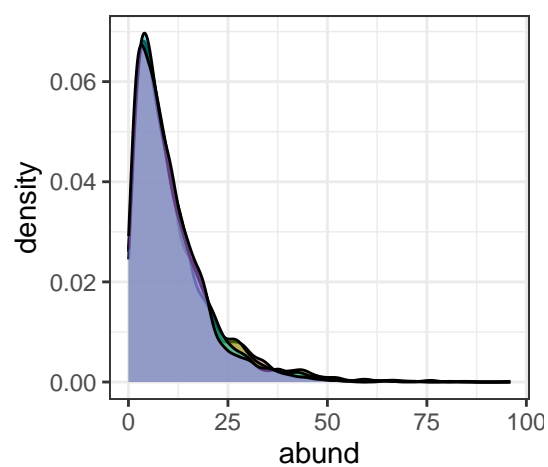
CCU_bb



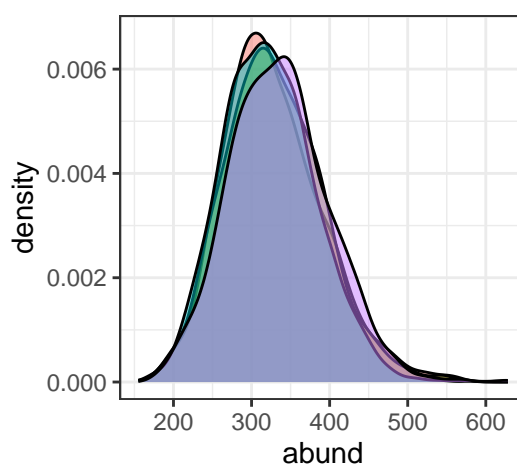
ESS



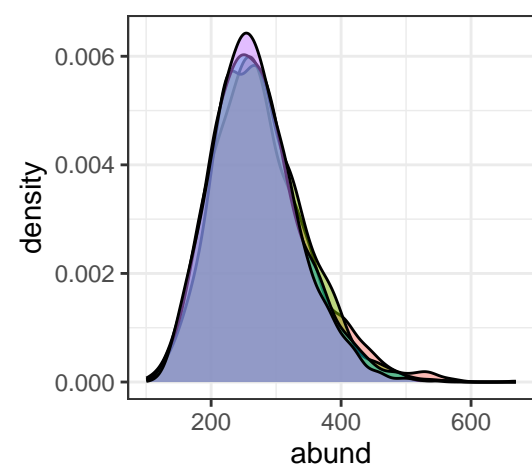
AFC

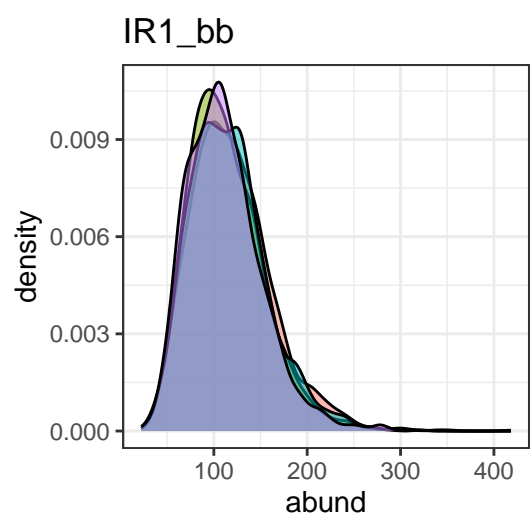
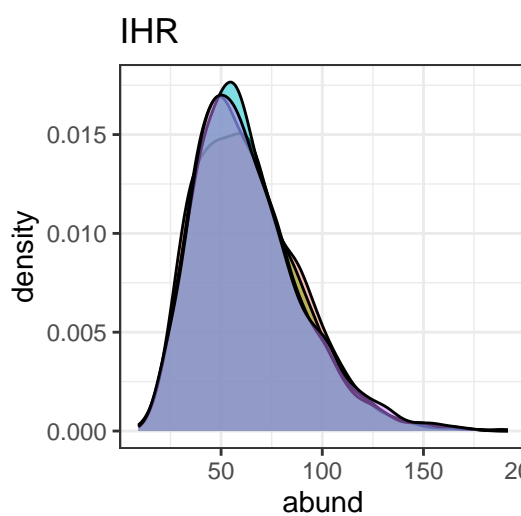
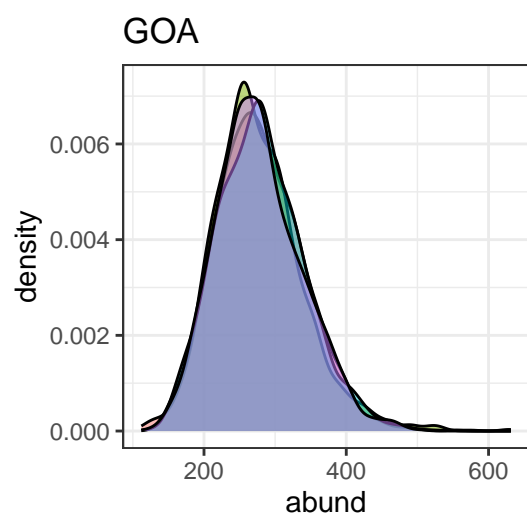
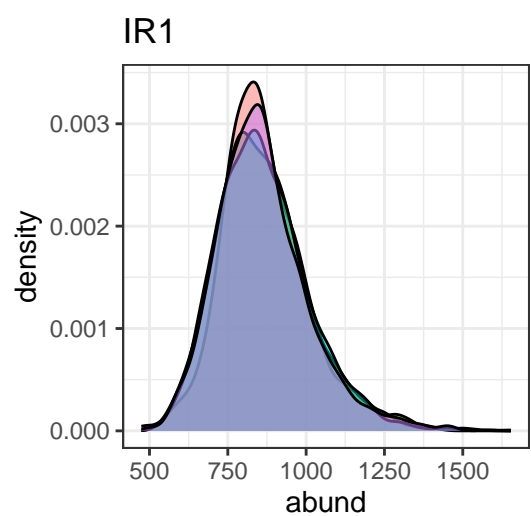
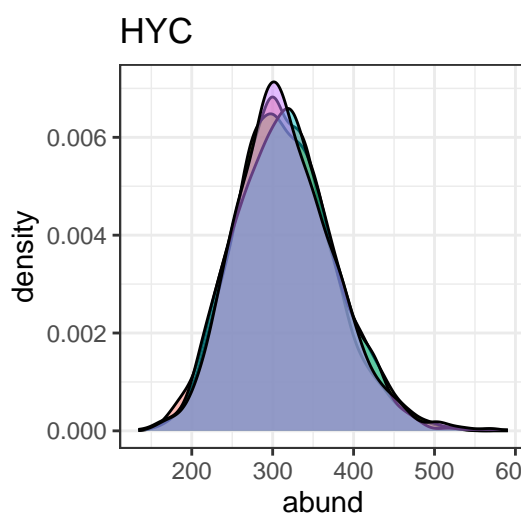
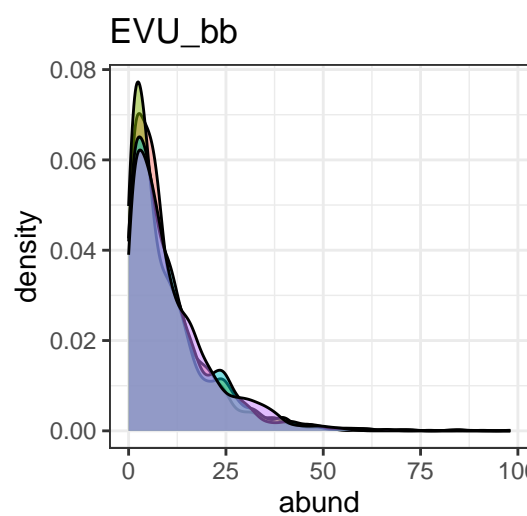
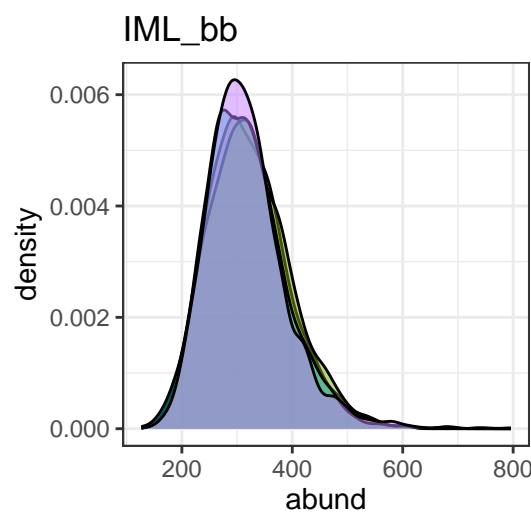
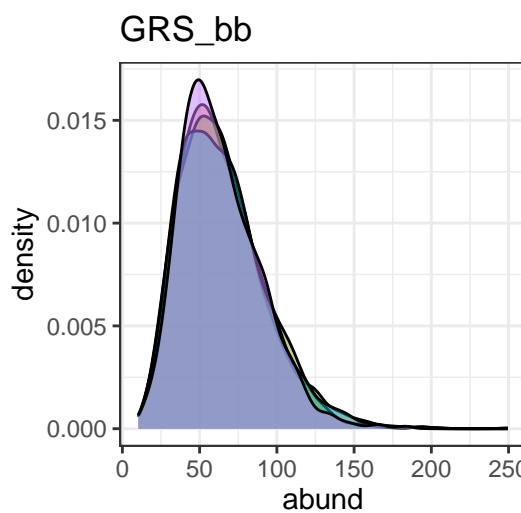
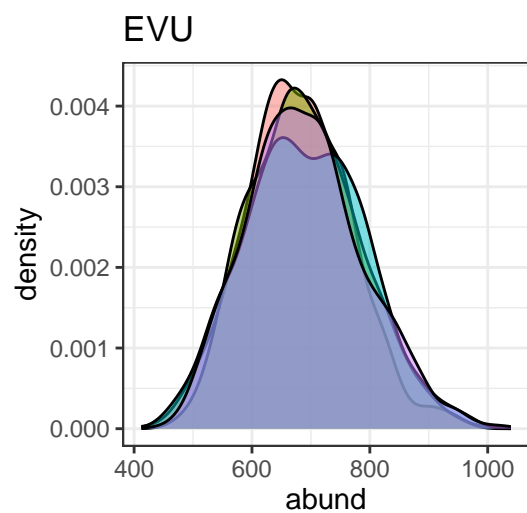
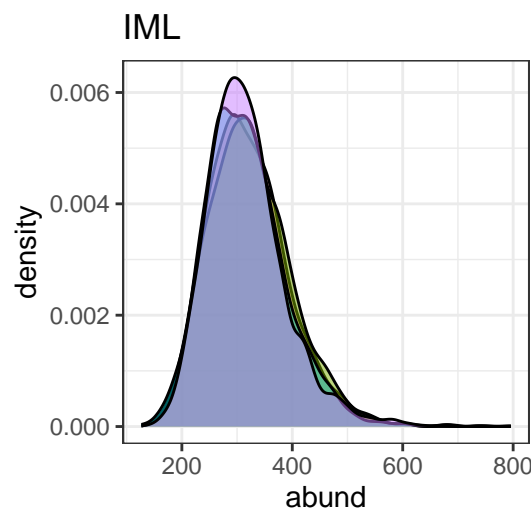
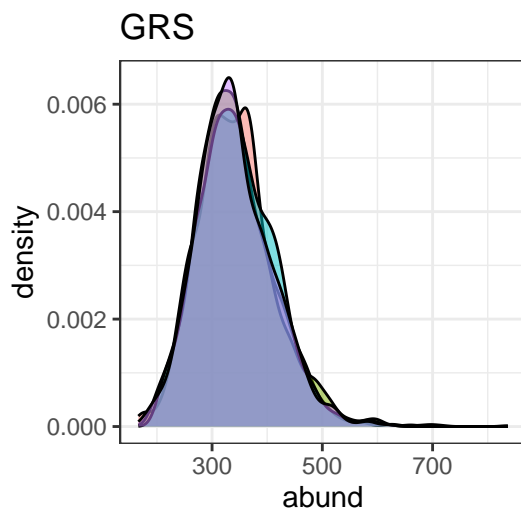
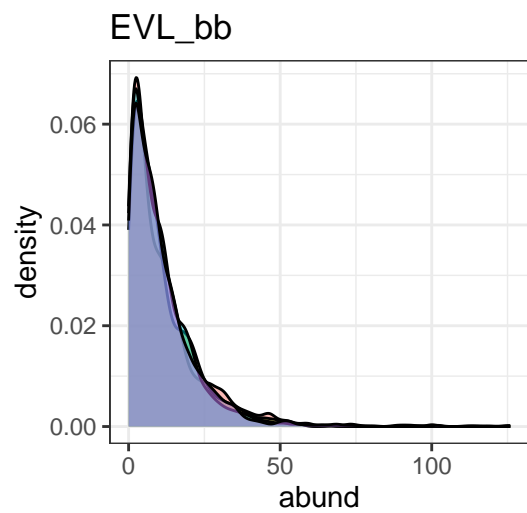
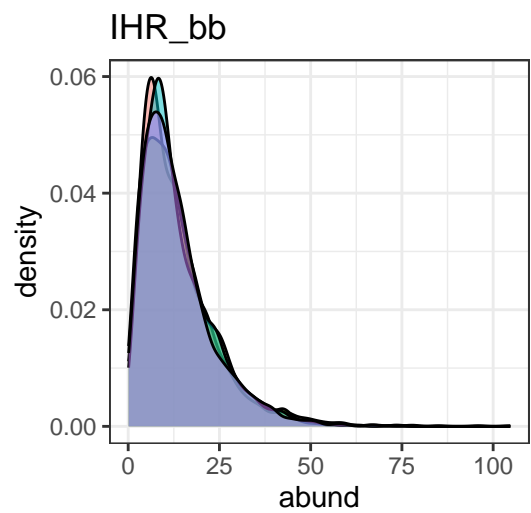
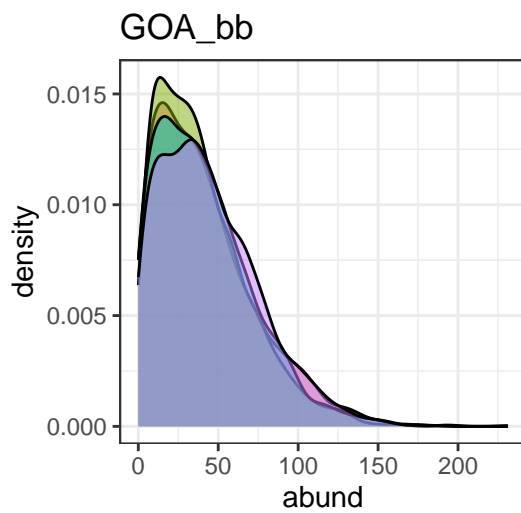
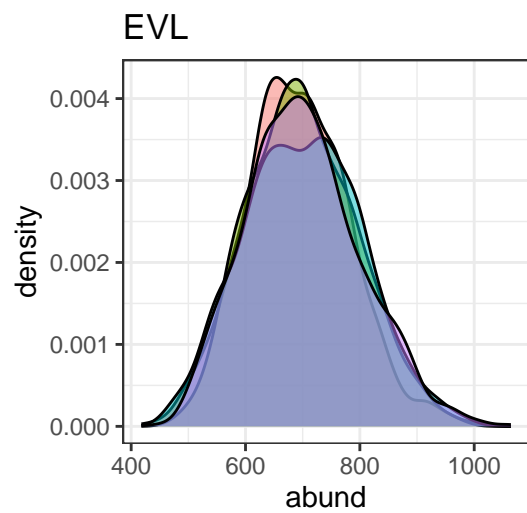


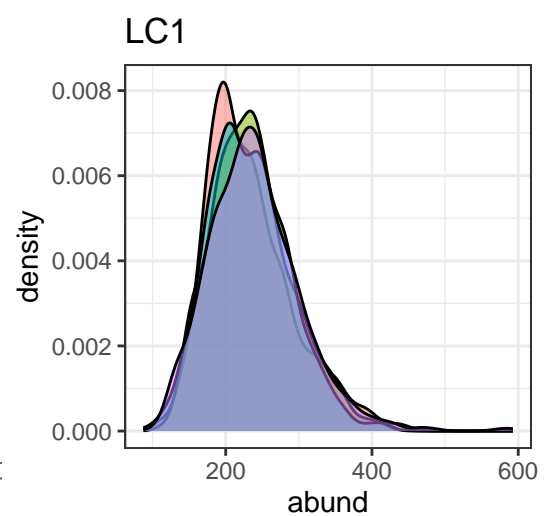
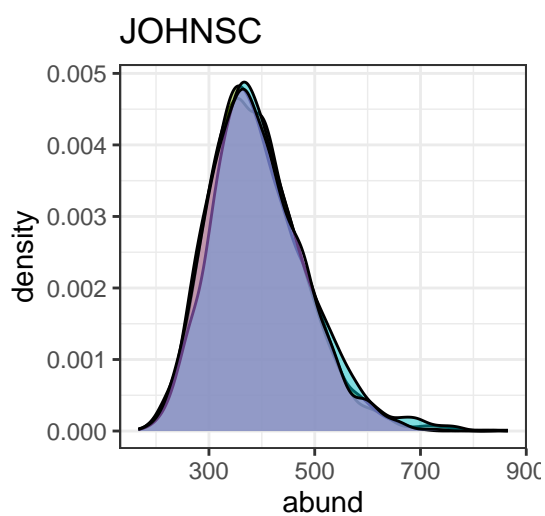
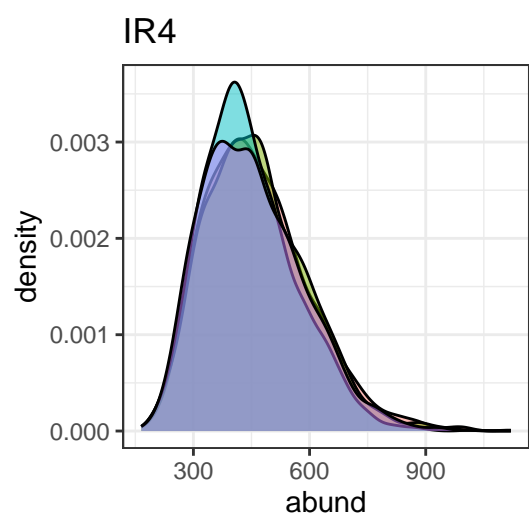
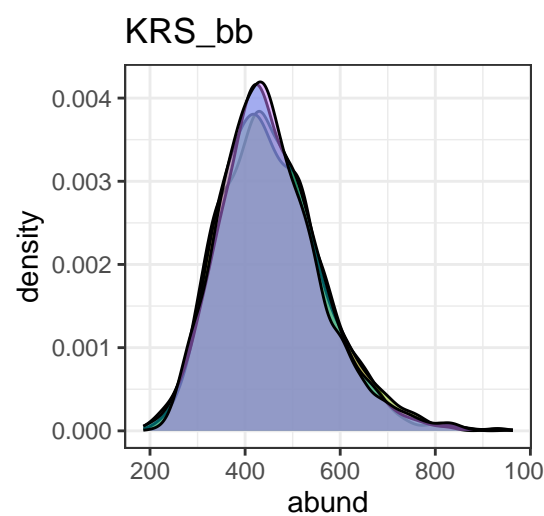
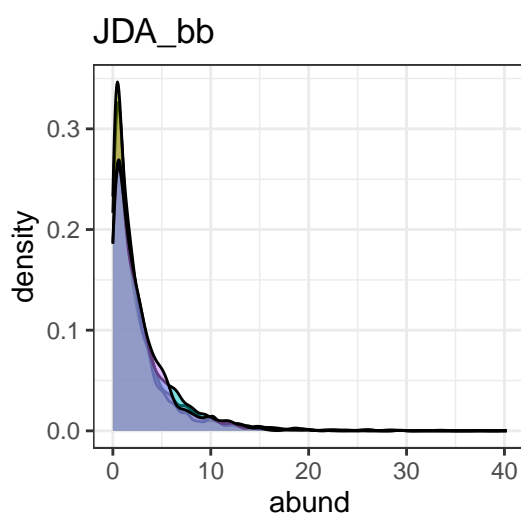
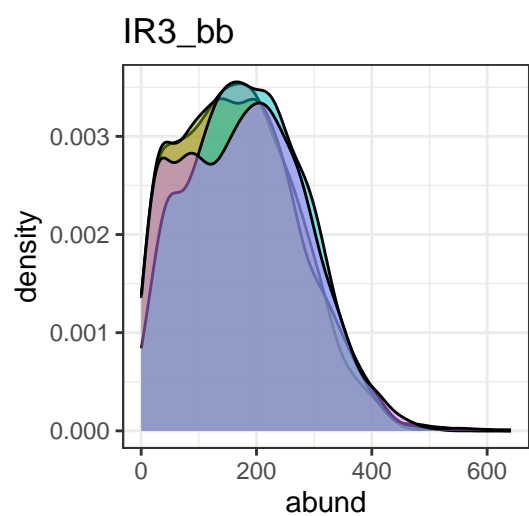
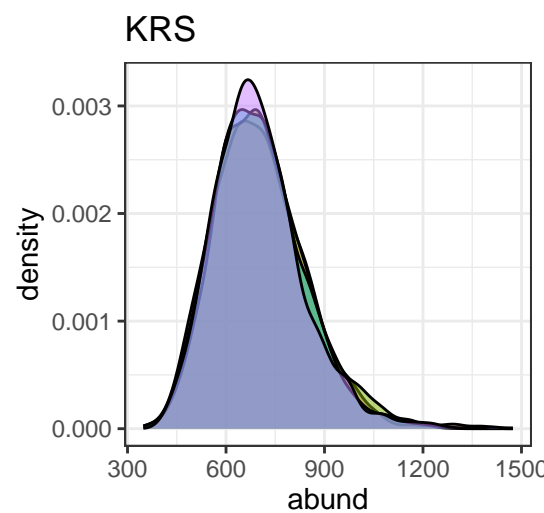
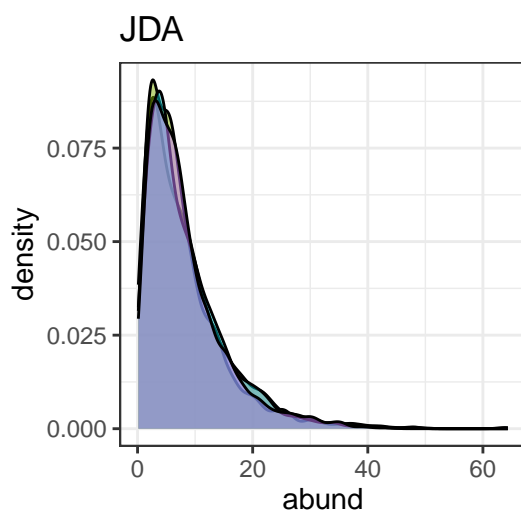
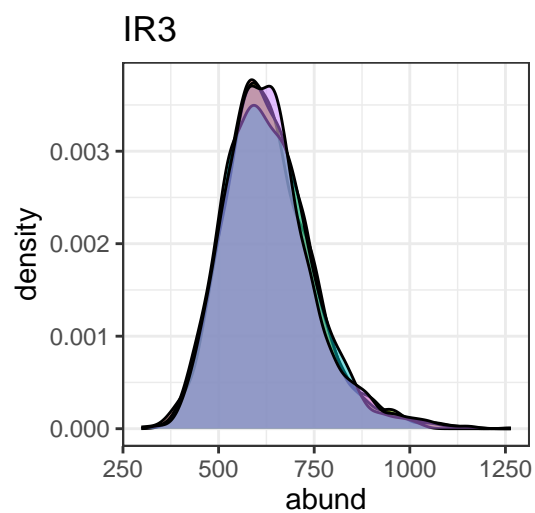
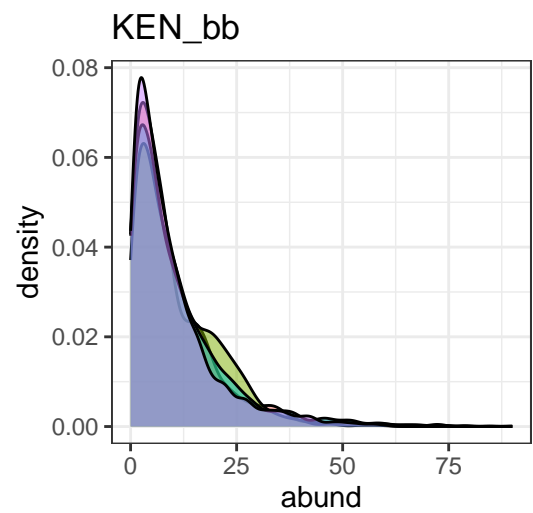
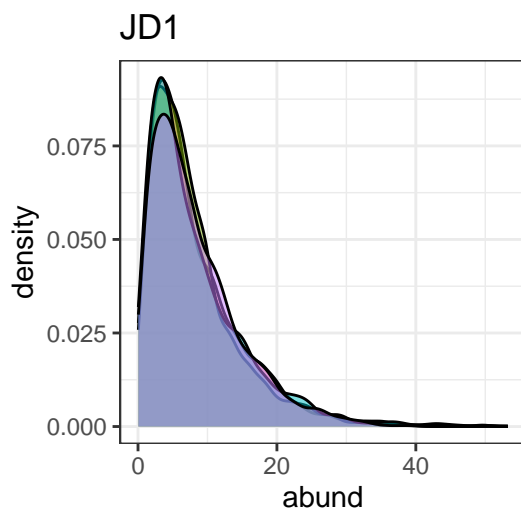
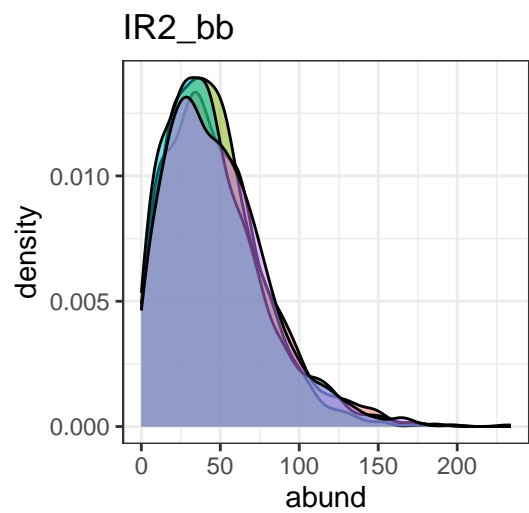
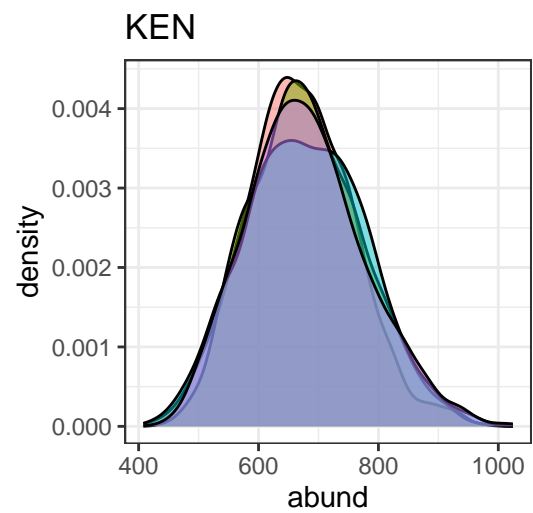
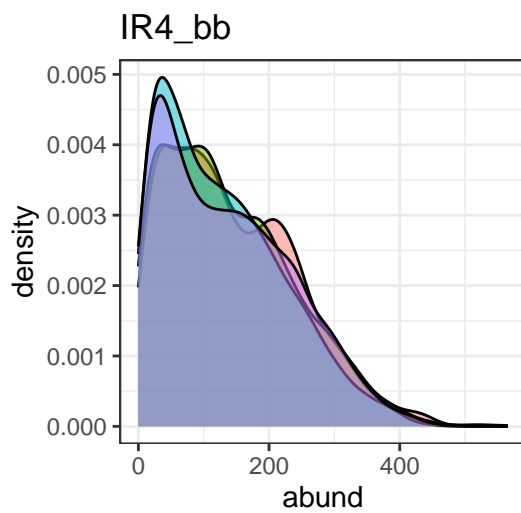
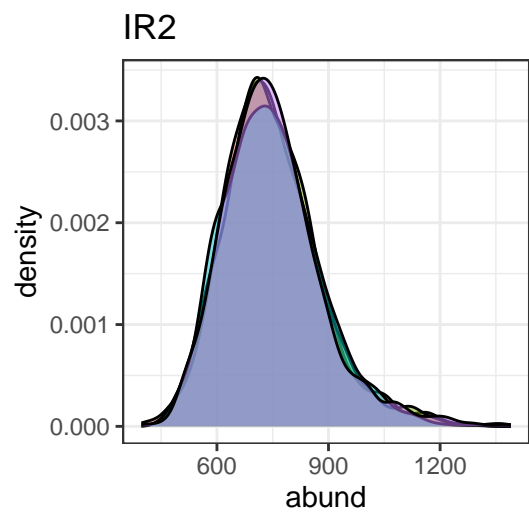
CCW

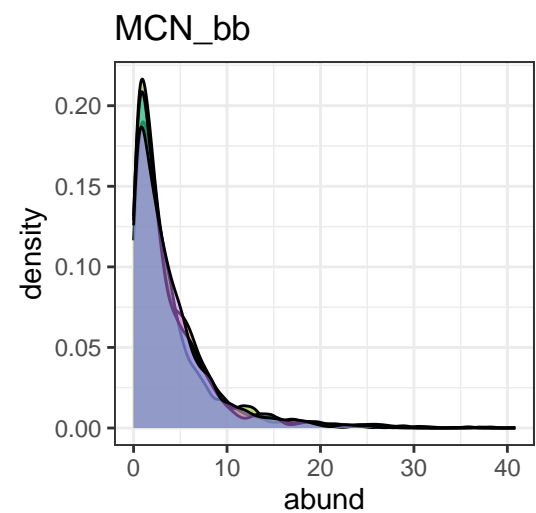
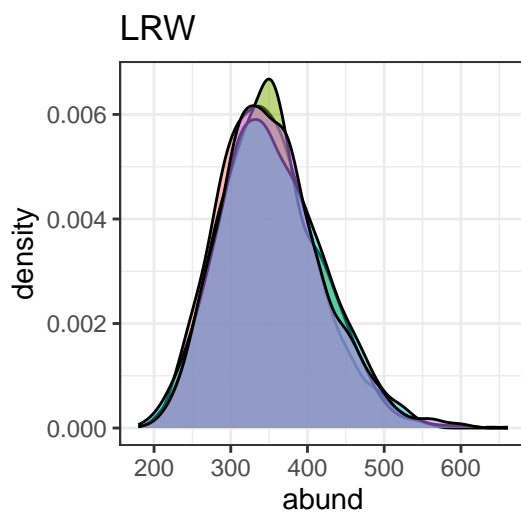
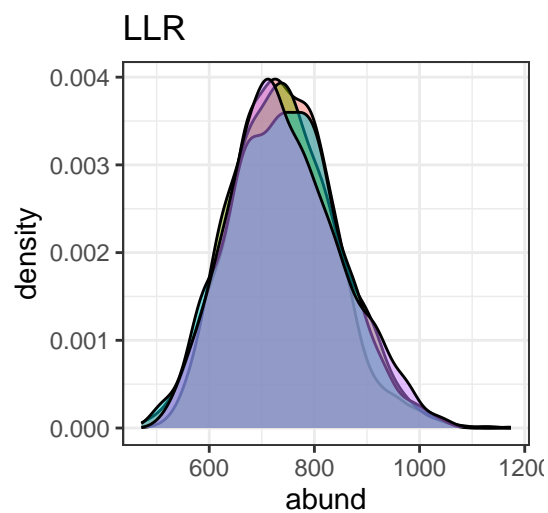
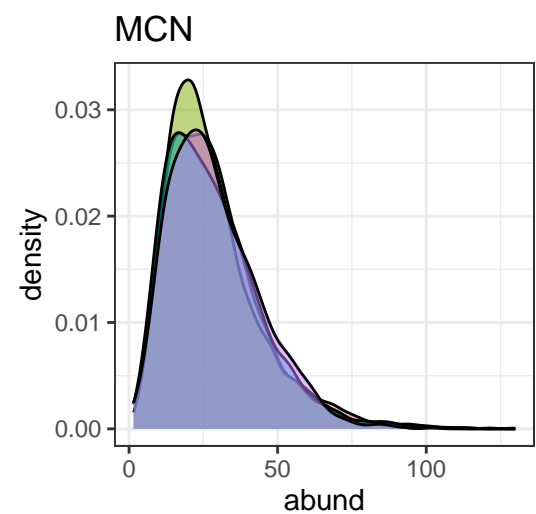
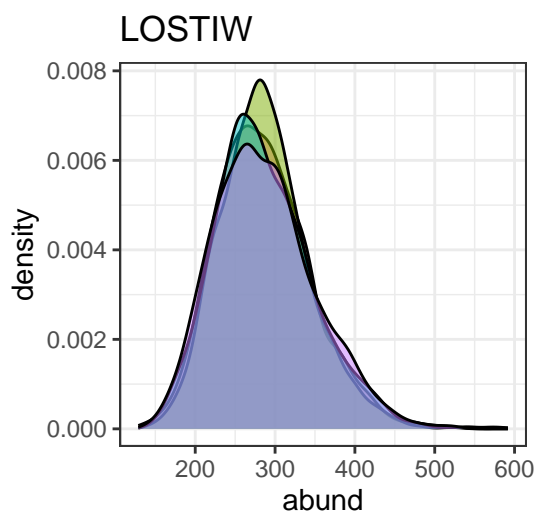
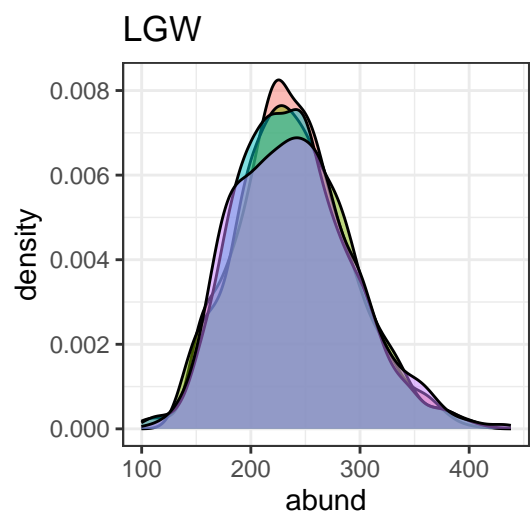
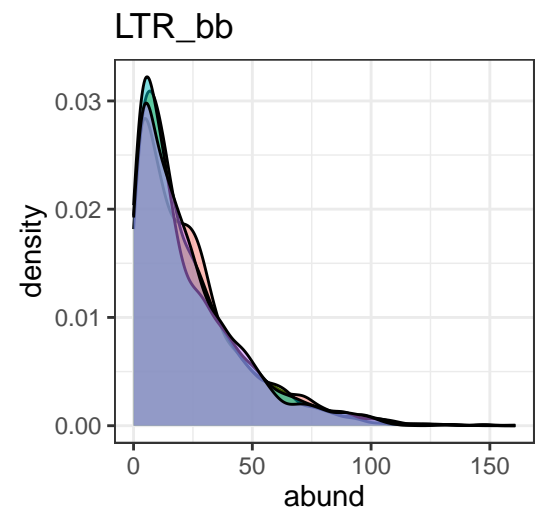
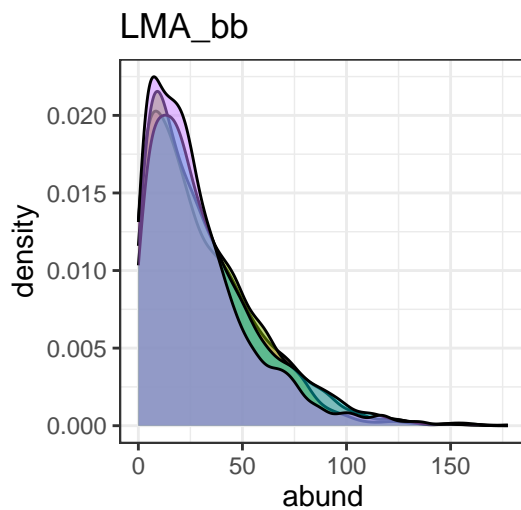
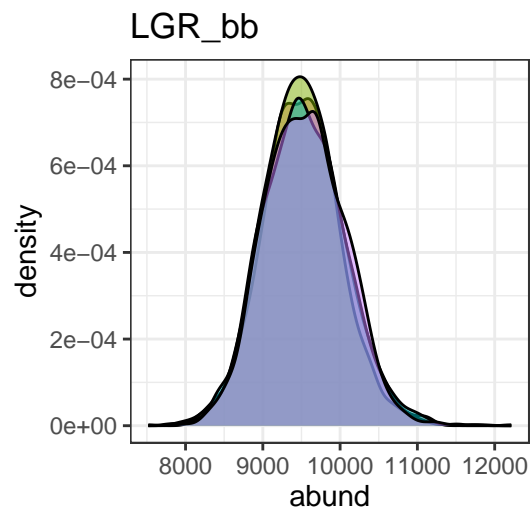
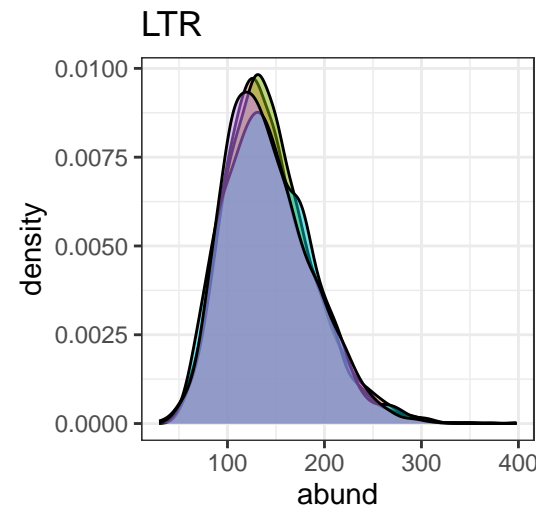
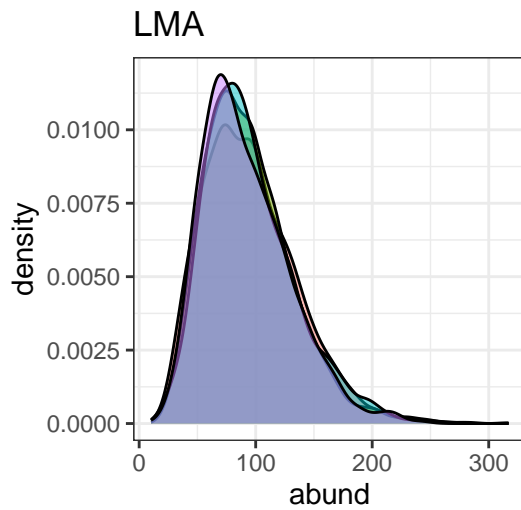
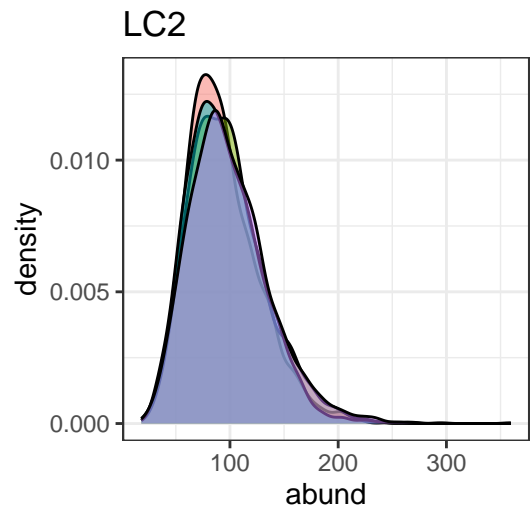
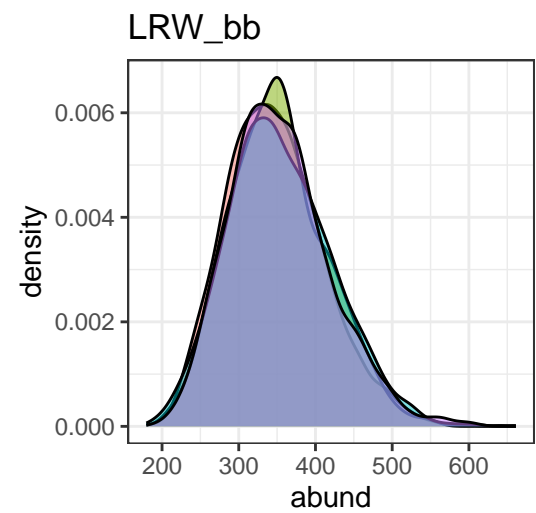
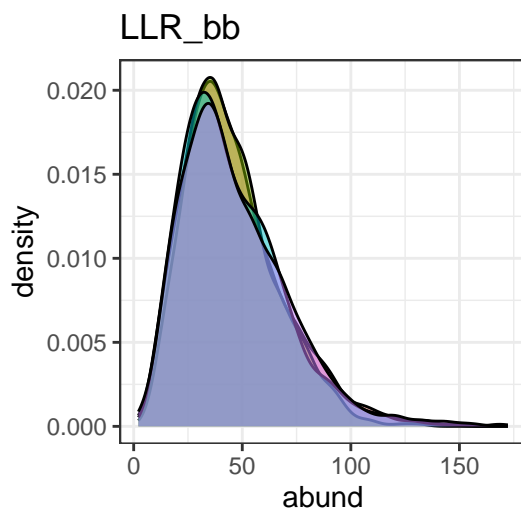
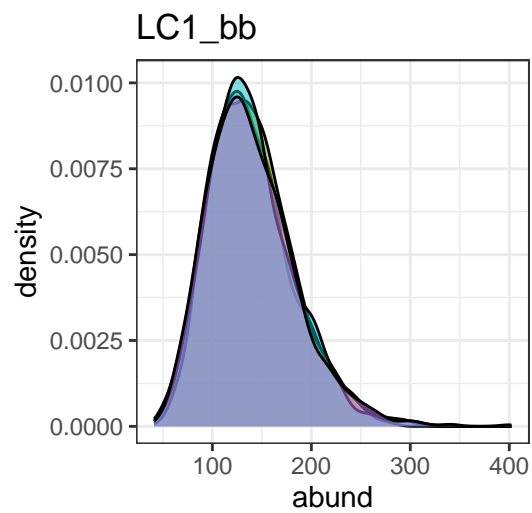


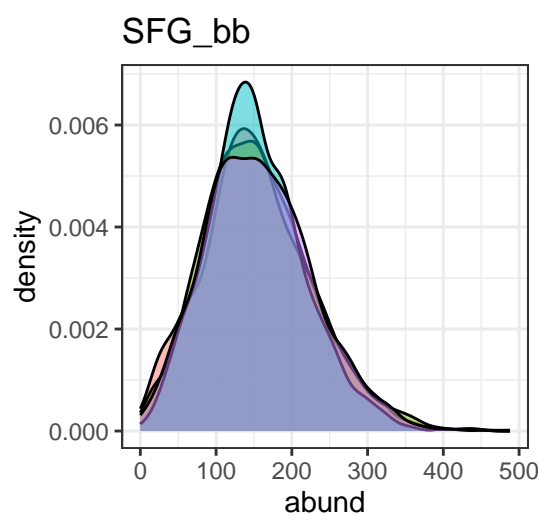
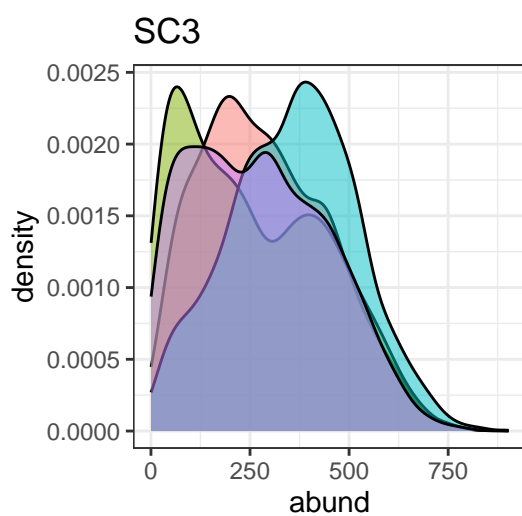
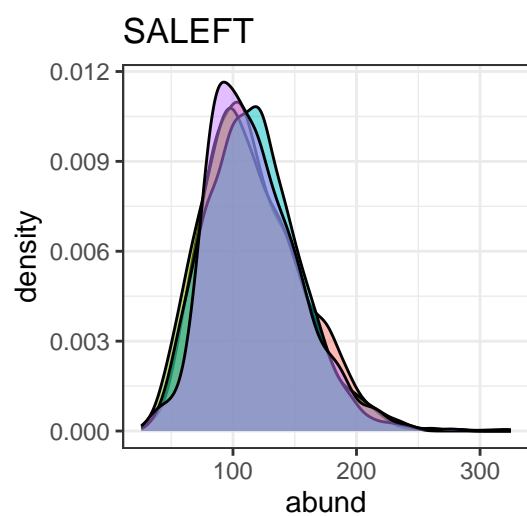
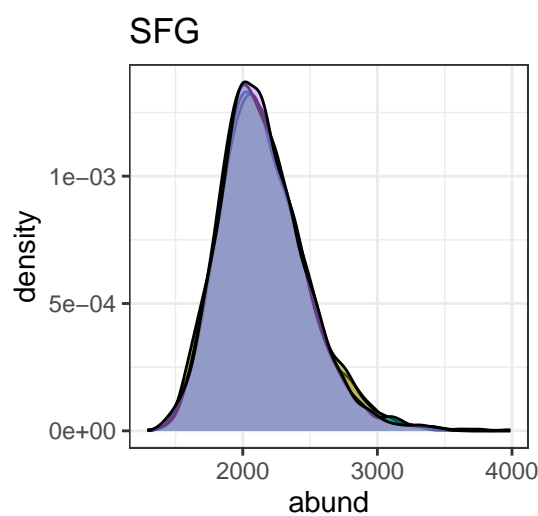
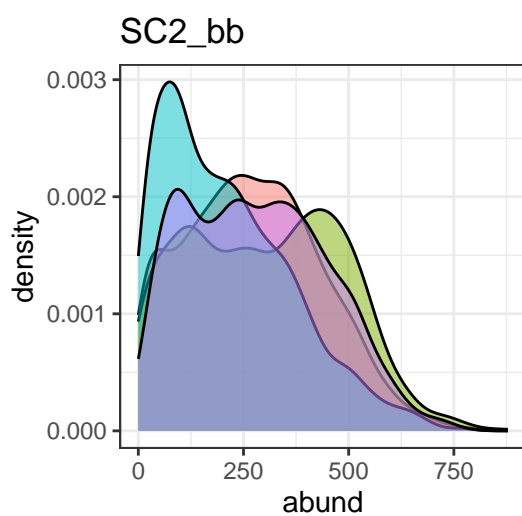
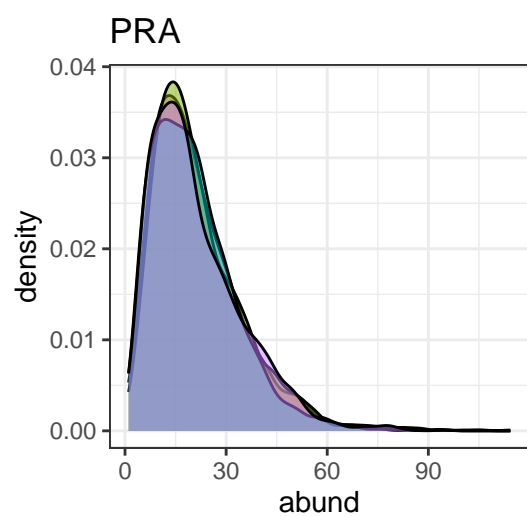
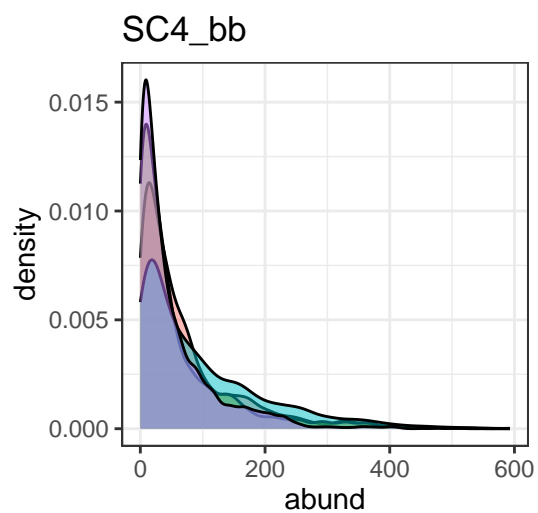
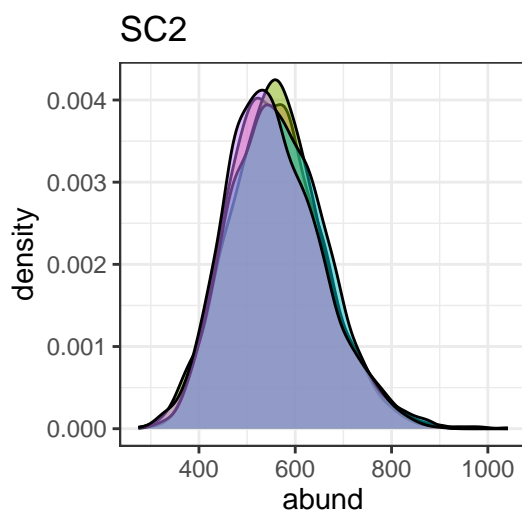
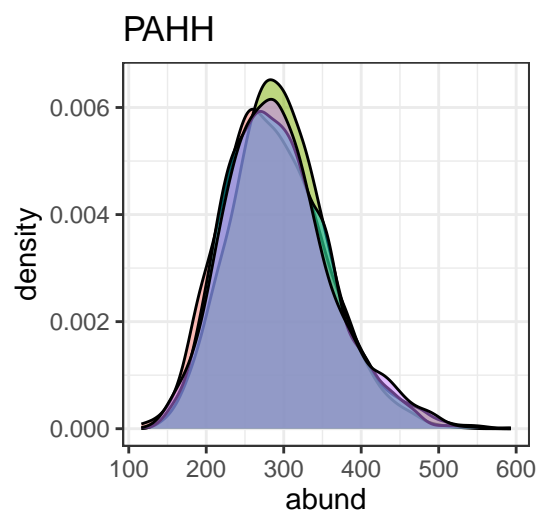
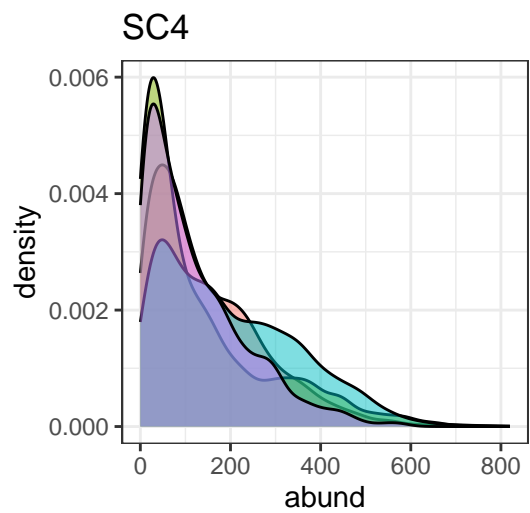
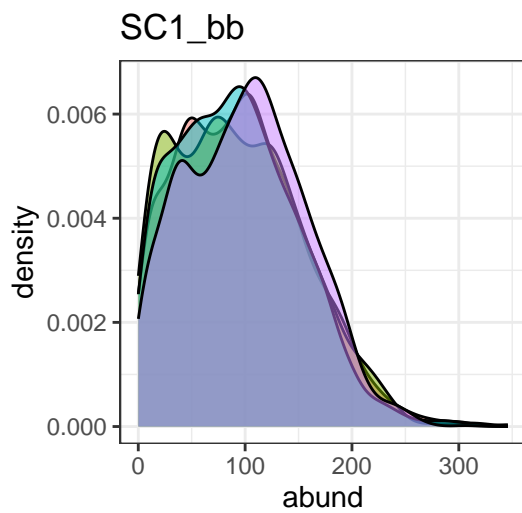
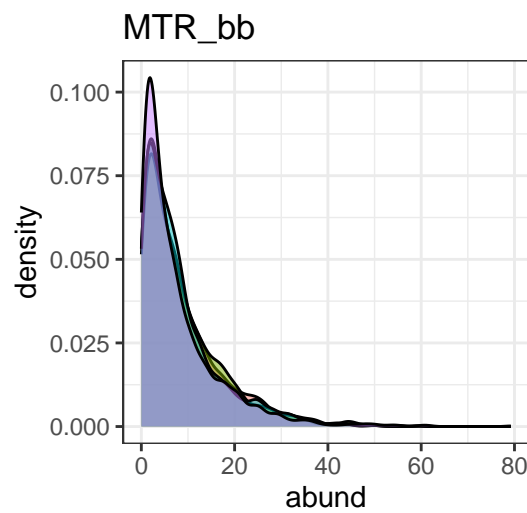
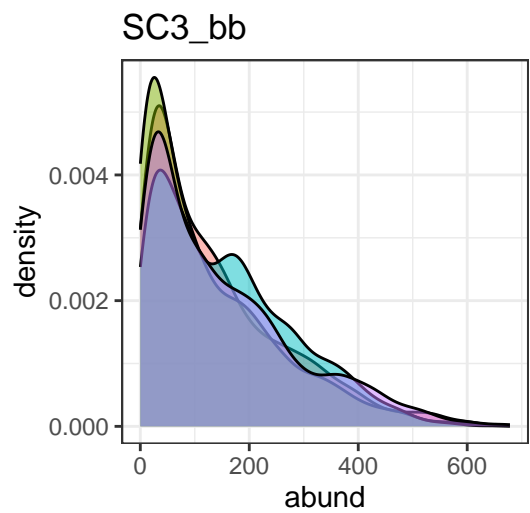
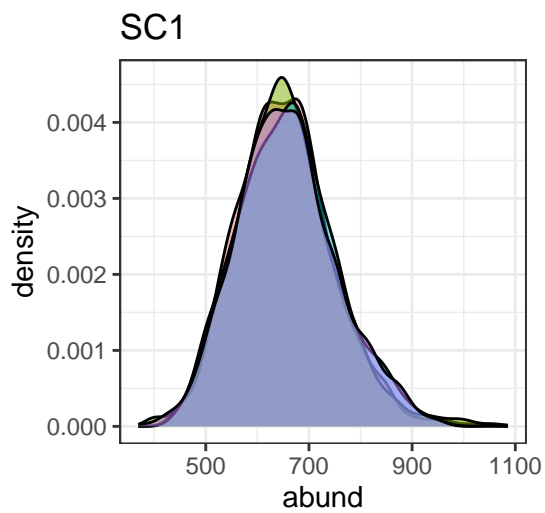
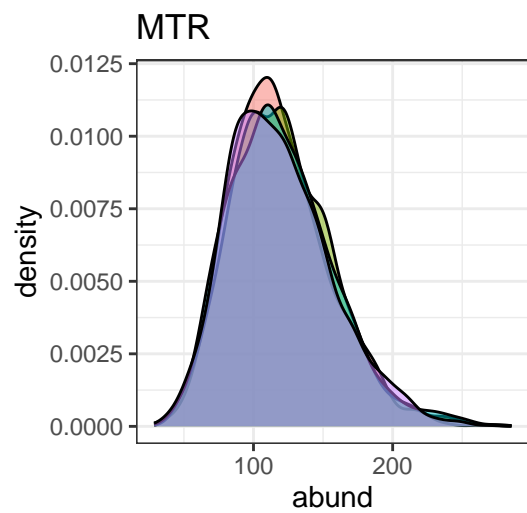
ESS_bb



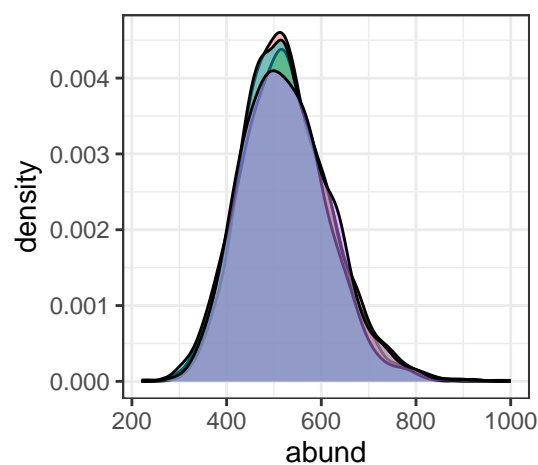




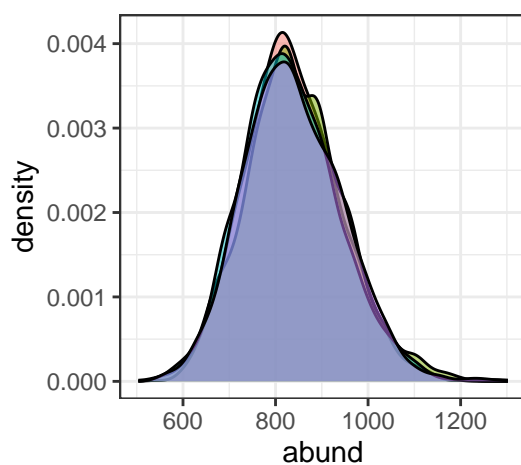




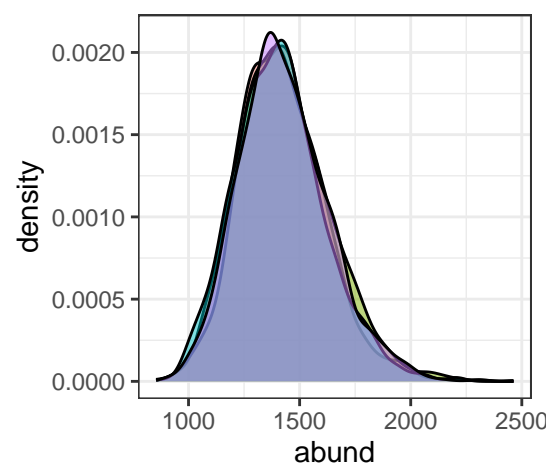
STL



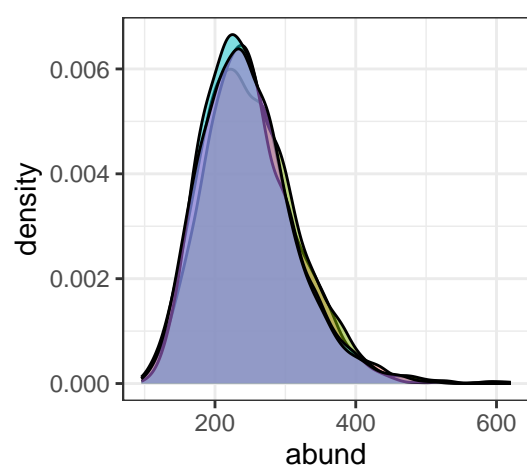
UGR



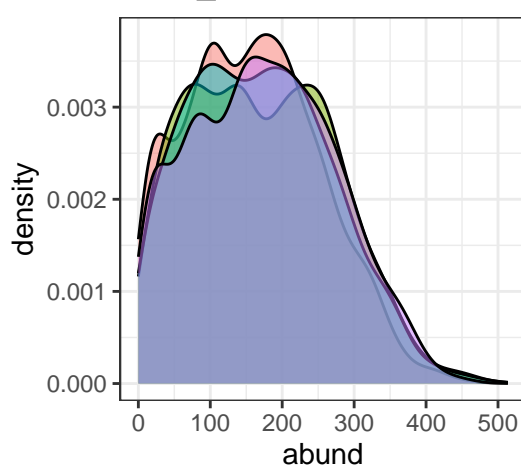
USE_bb



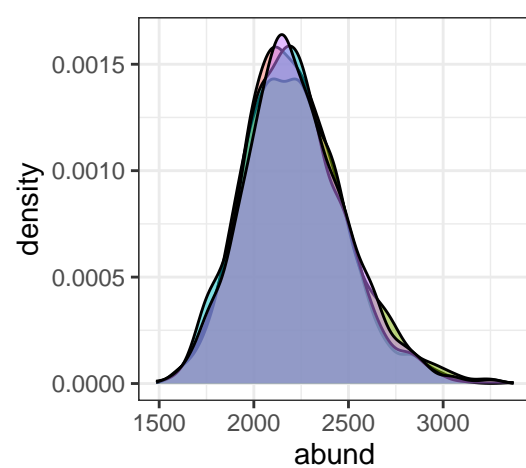
STR



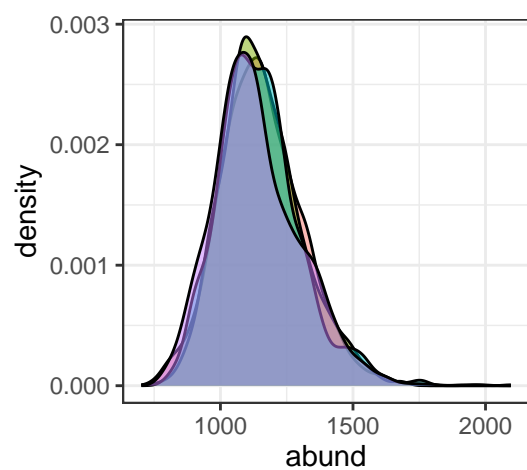
UGR_bb



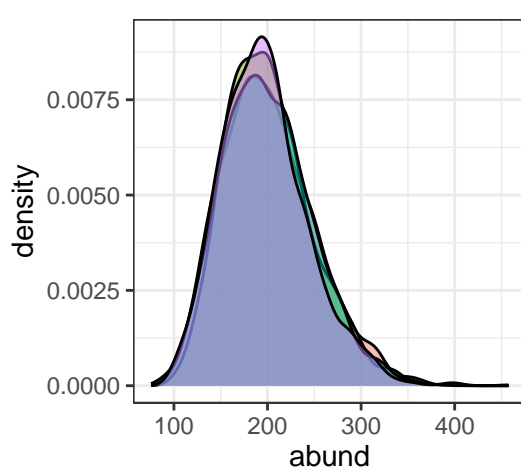
USI



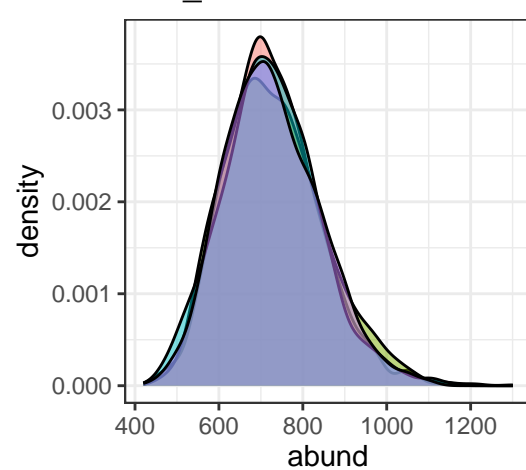
TAY



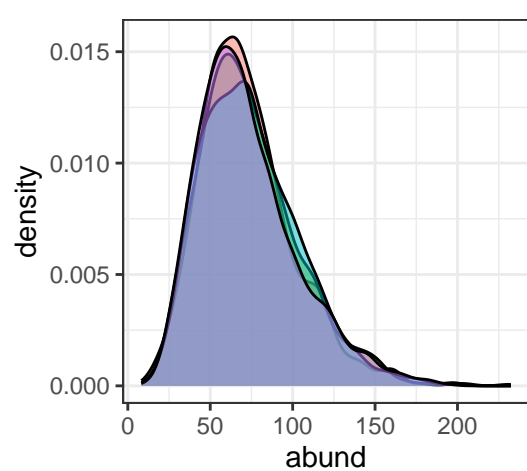
UGS



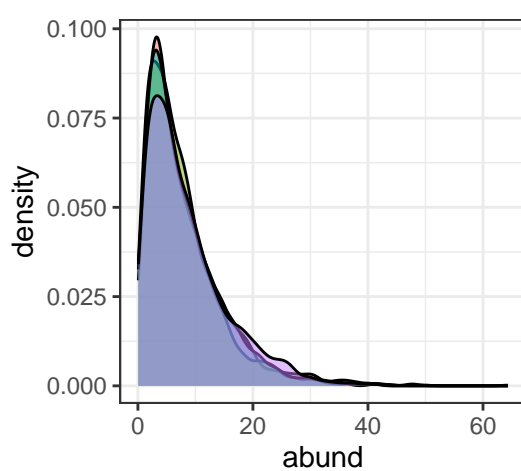
USI_bb



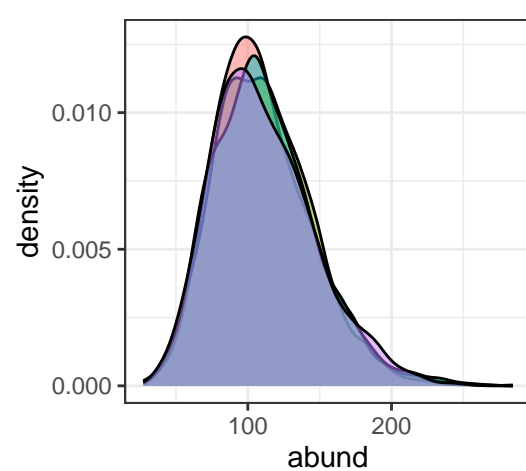
TFH



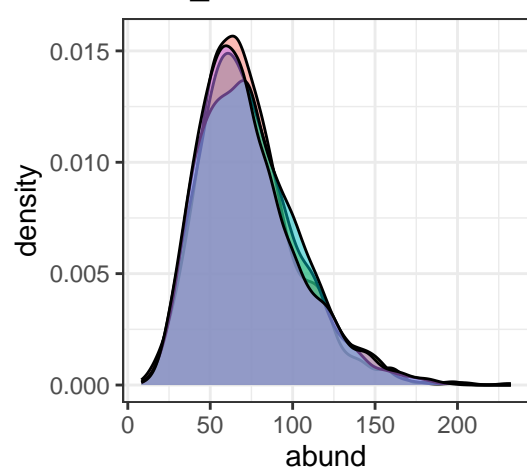
UMW



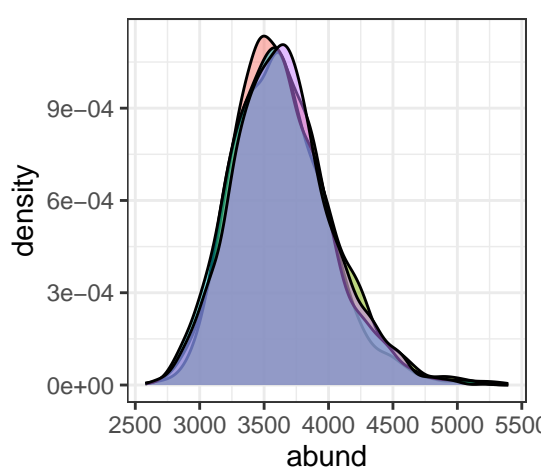
UTR



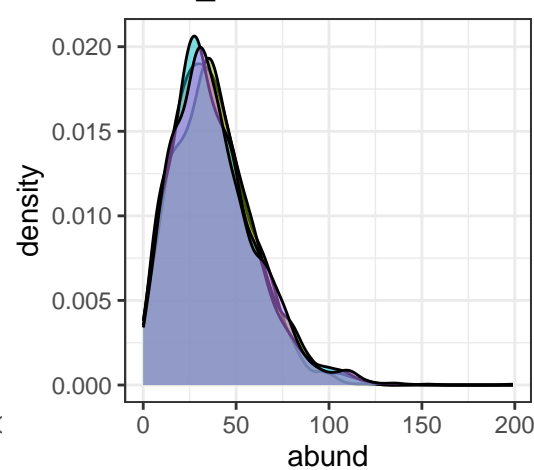
TFH_bb



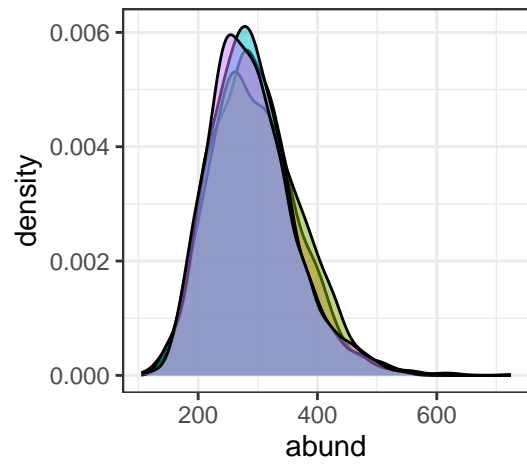
USE



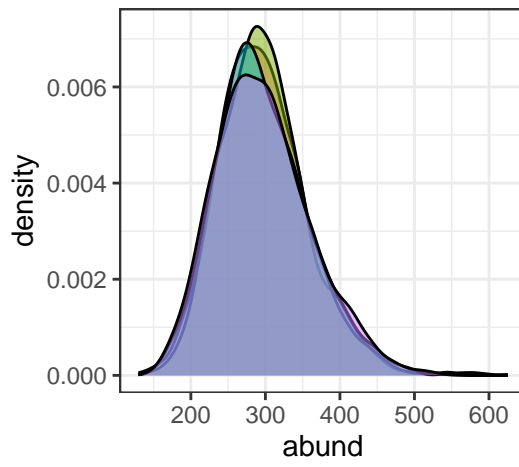
UTR_bb



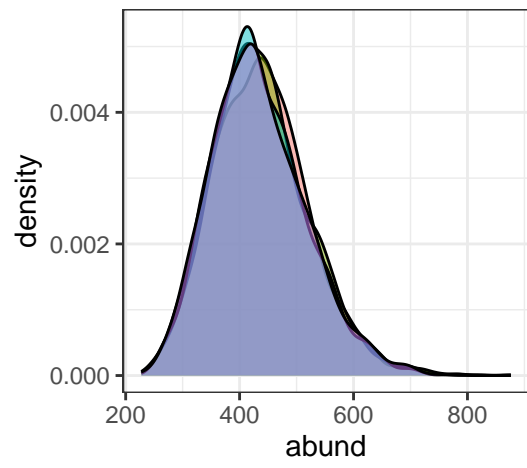
VC1



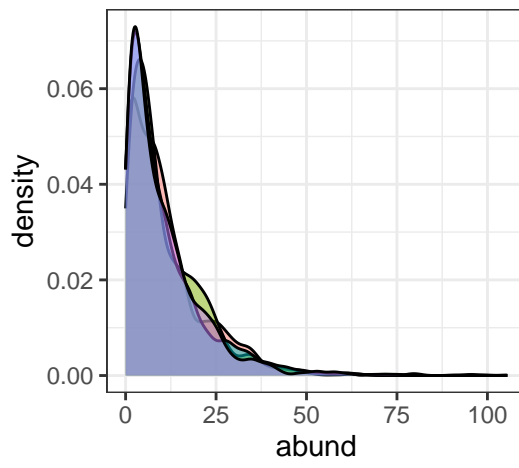
WR2



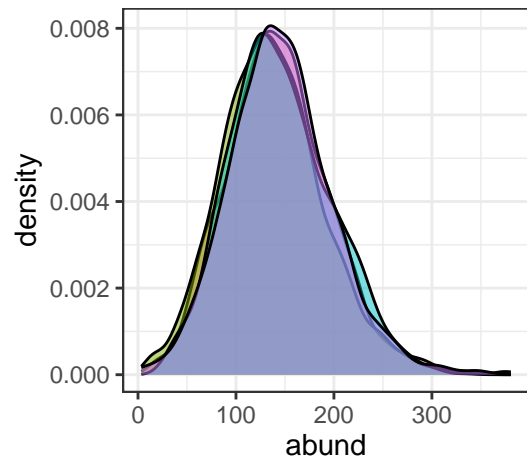
VC2



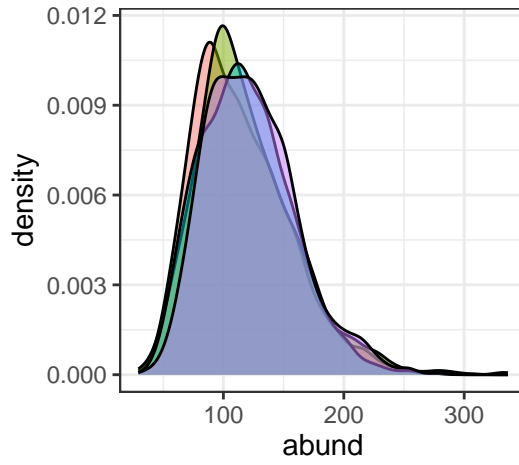
WR2_bb



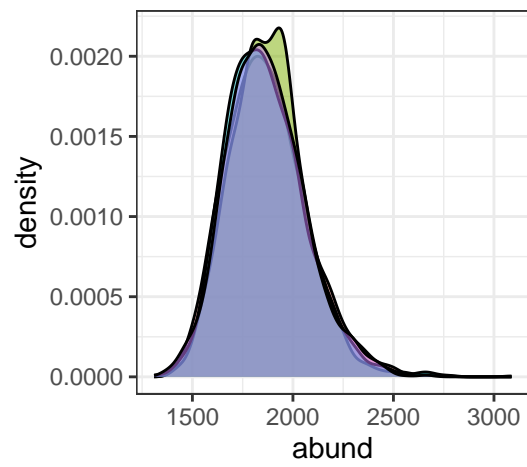
VC2_bb



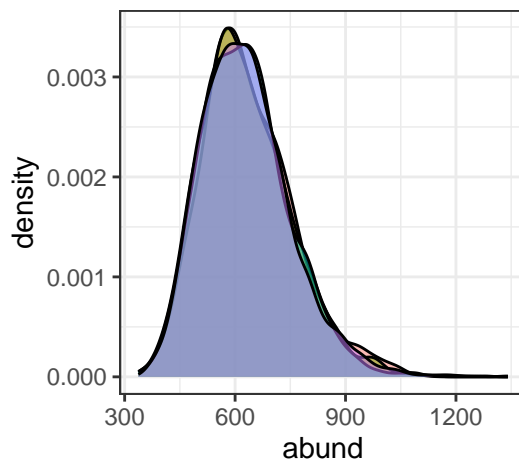
YFK



WR1



ZEN



WR1_bb

