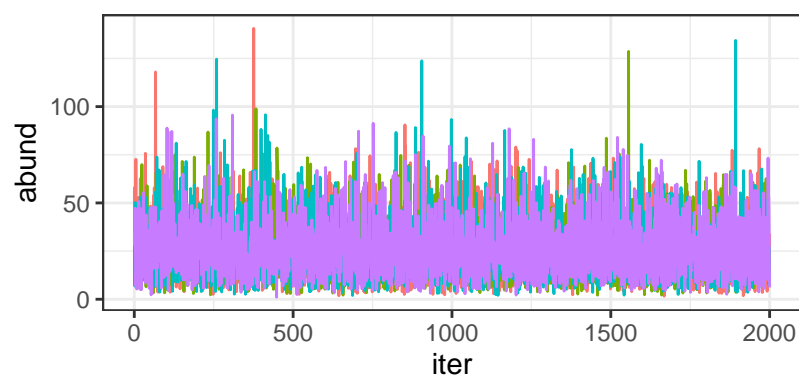
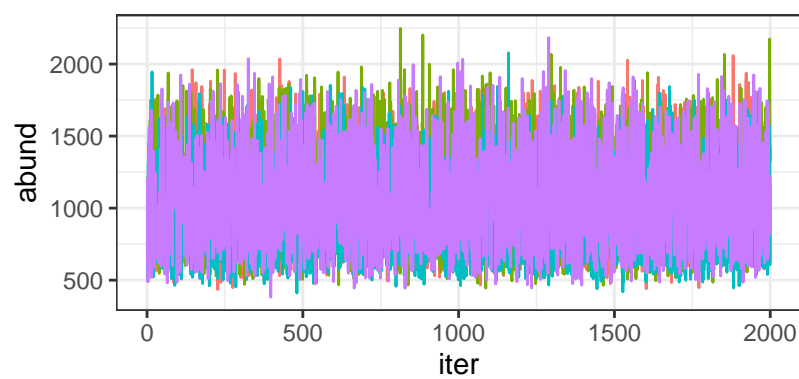


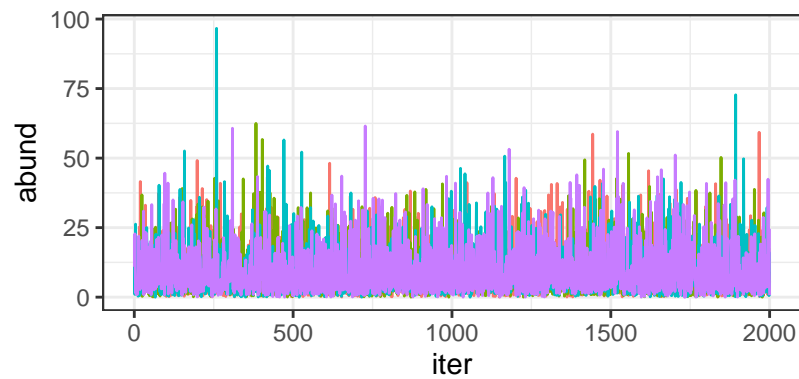
ACB



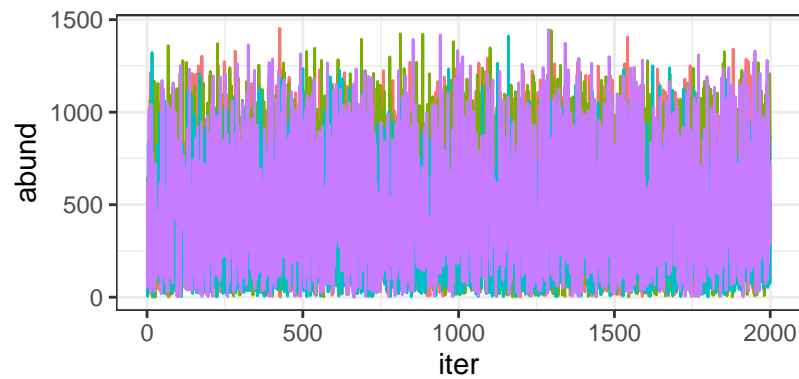
CCU



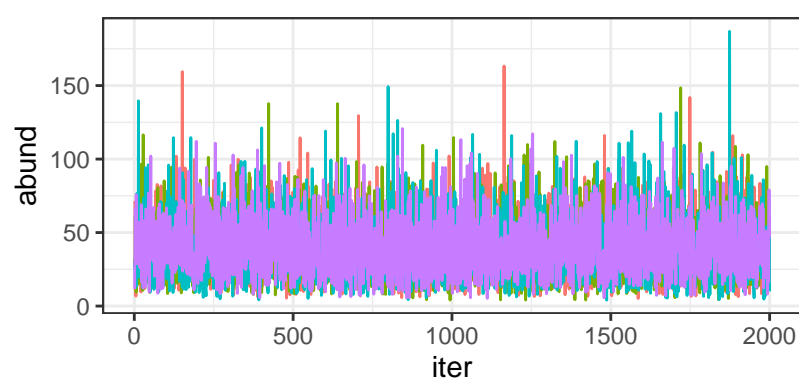
ACB_bb



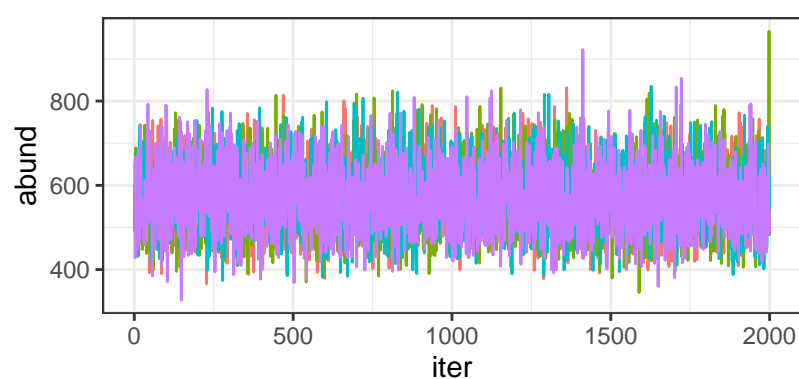
CCU_bb



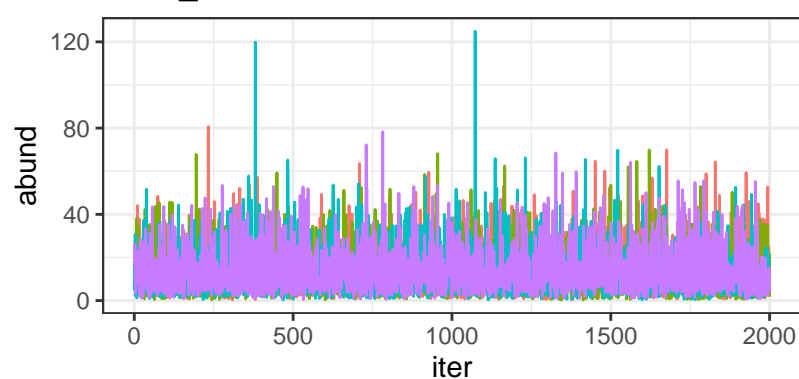
ACM



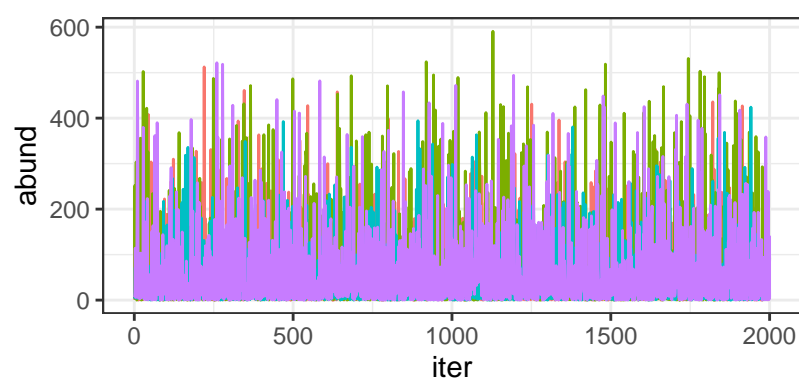
CCW



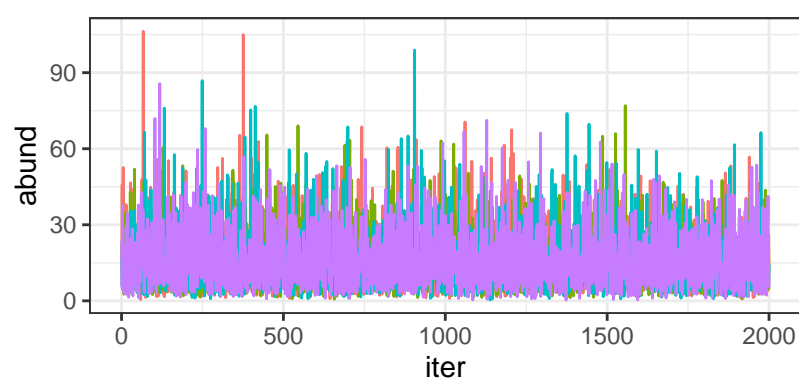
ACM_bb



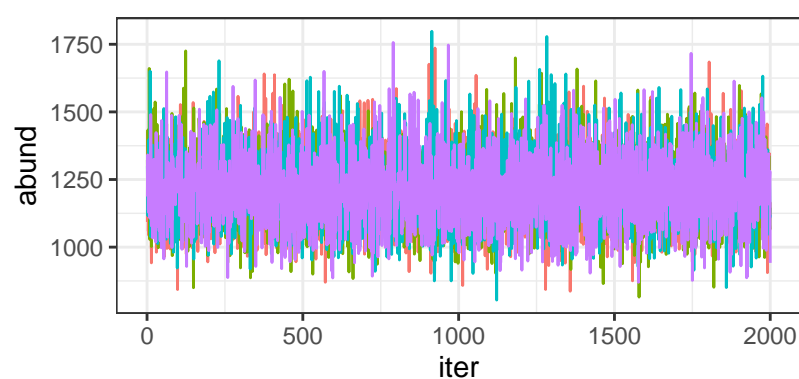
CRA



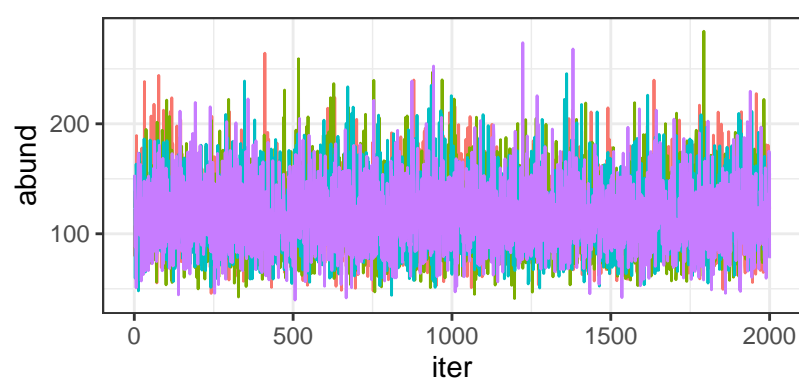
AFC



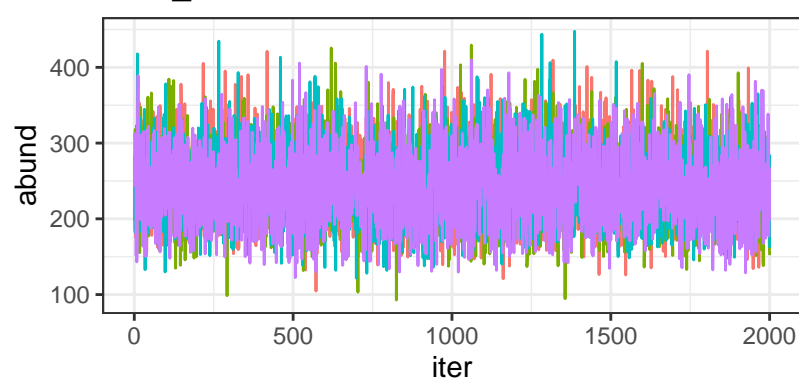
ESS



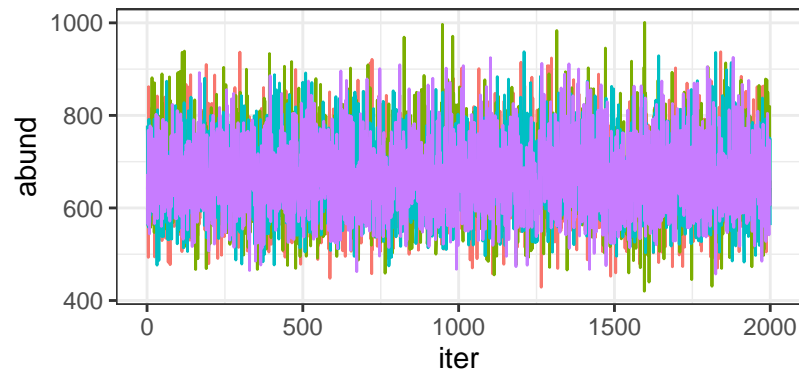
BSC



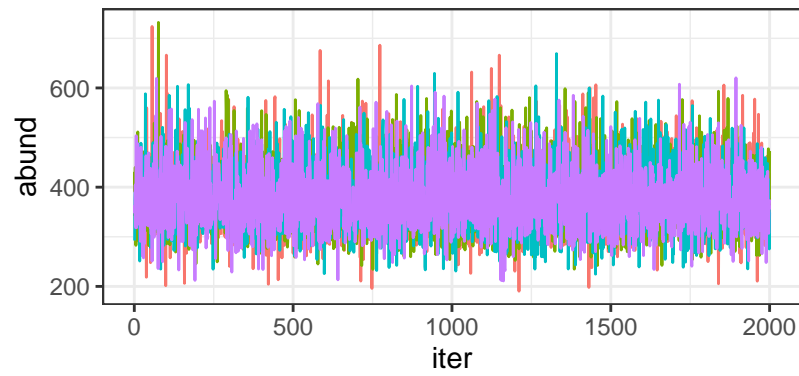
ESS_bb



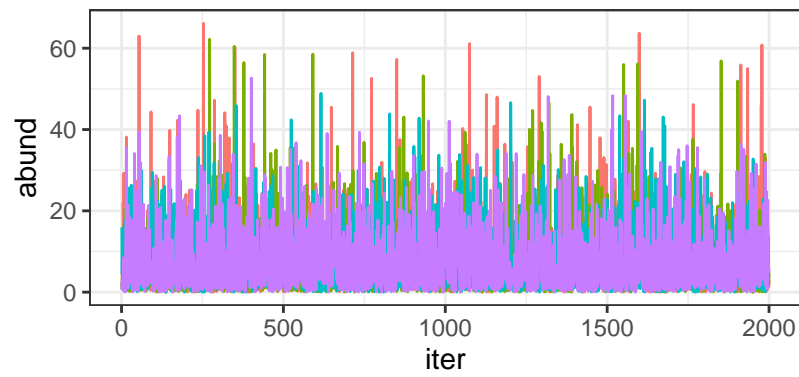
EVL



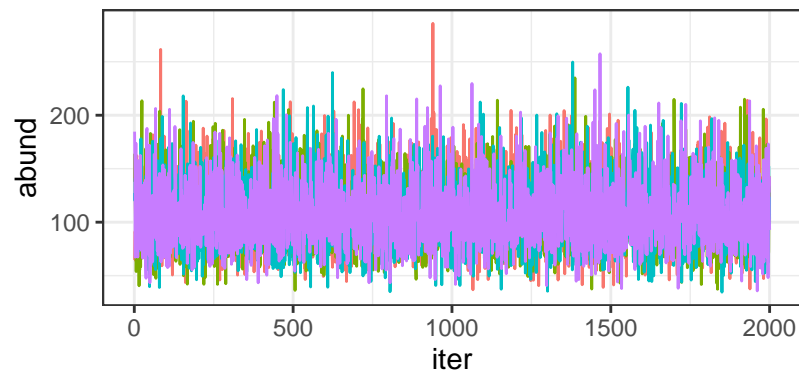
GRS



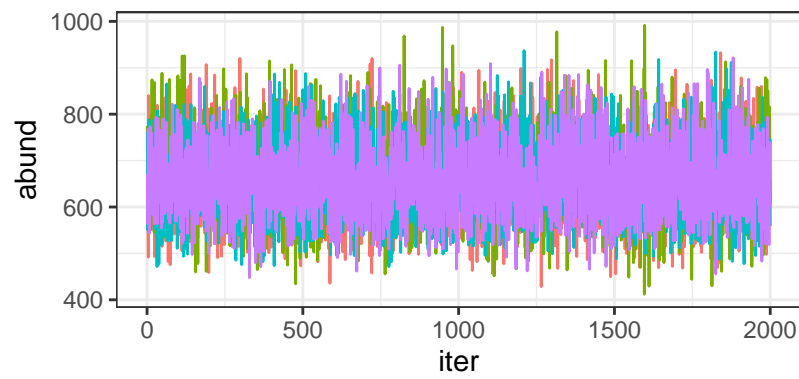
EVL_bb



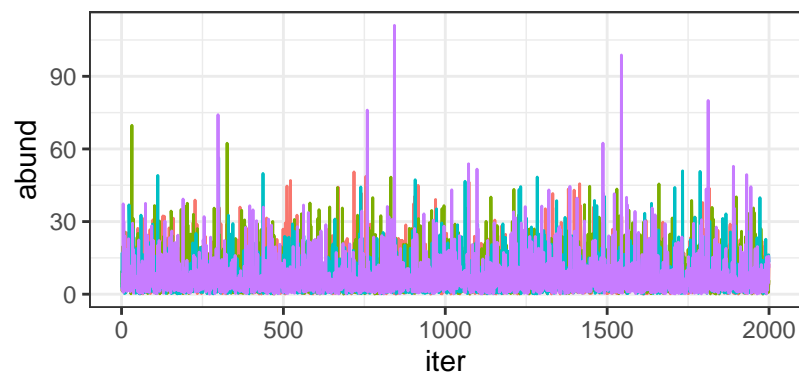
GRS_bb



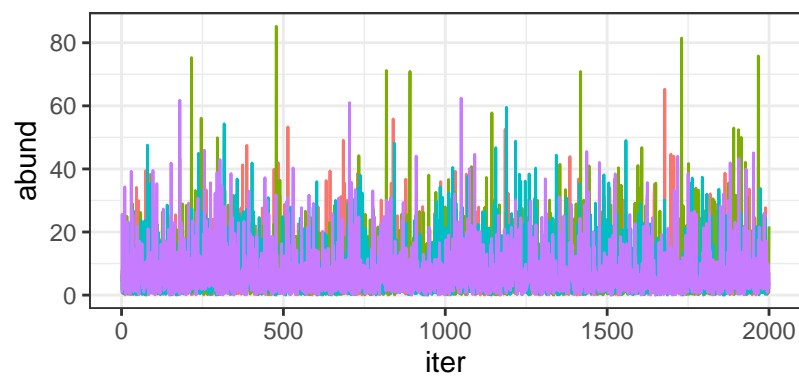
EVL



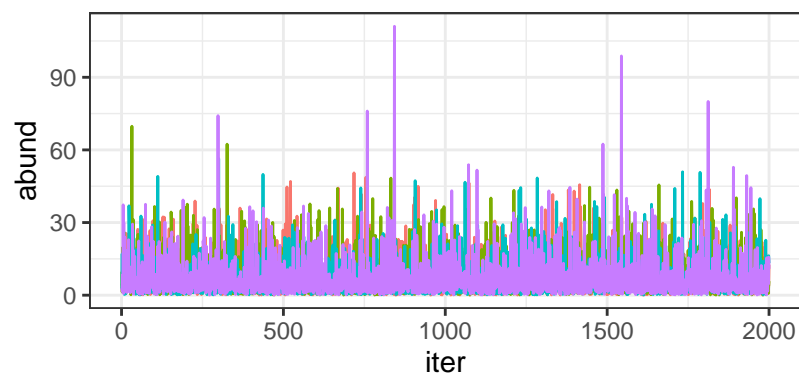
HLM



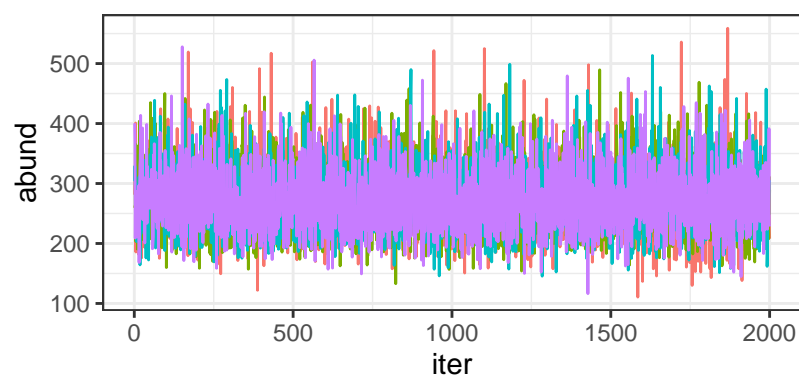
EVL_bb



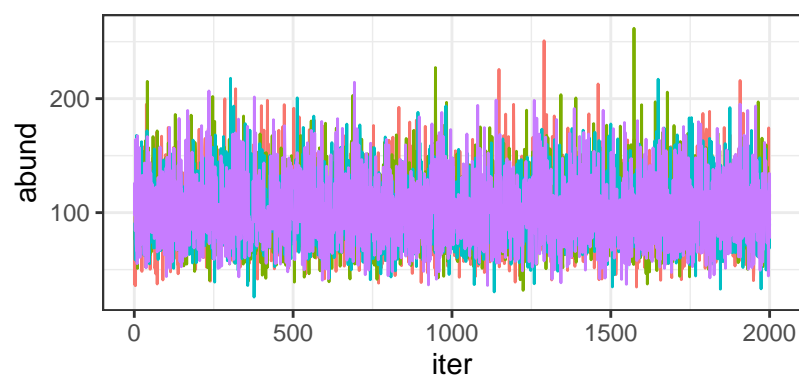
HLM_bb



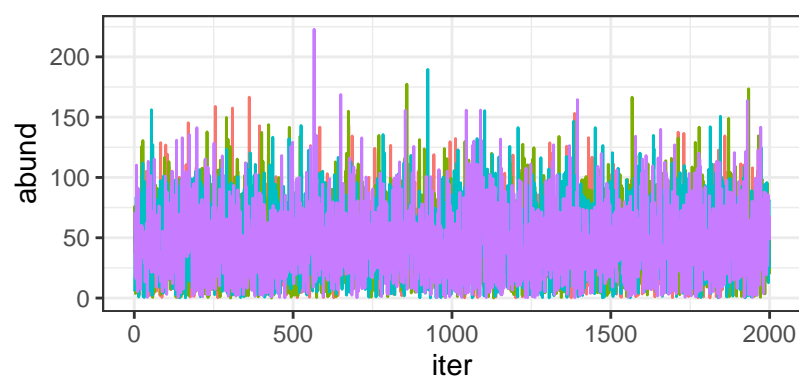
GOA



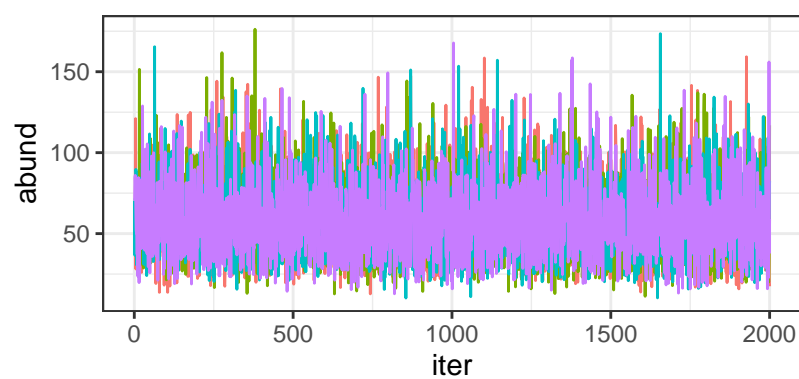
HYC



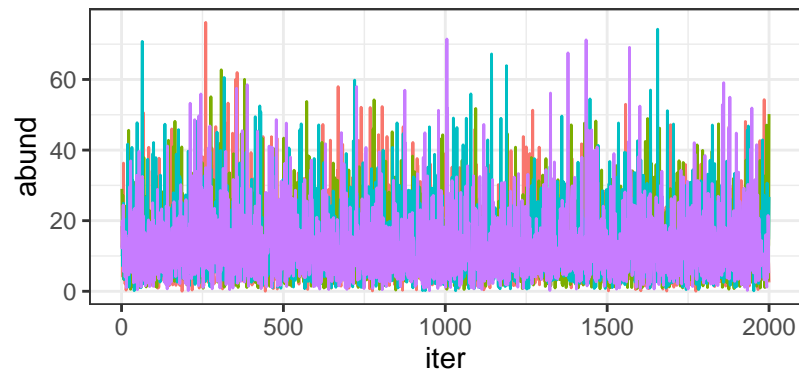
GOA_bb



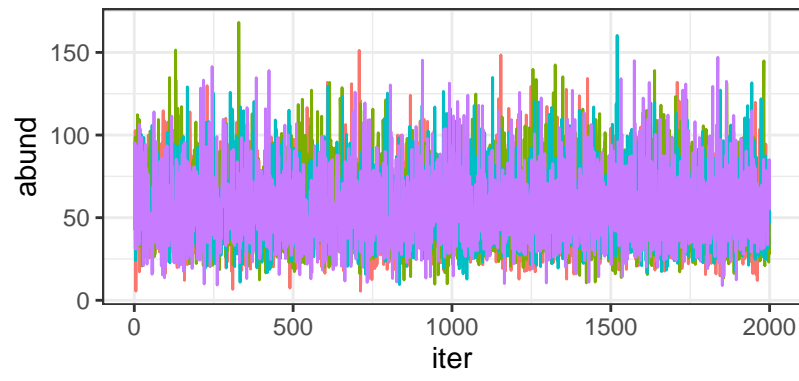
IHR



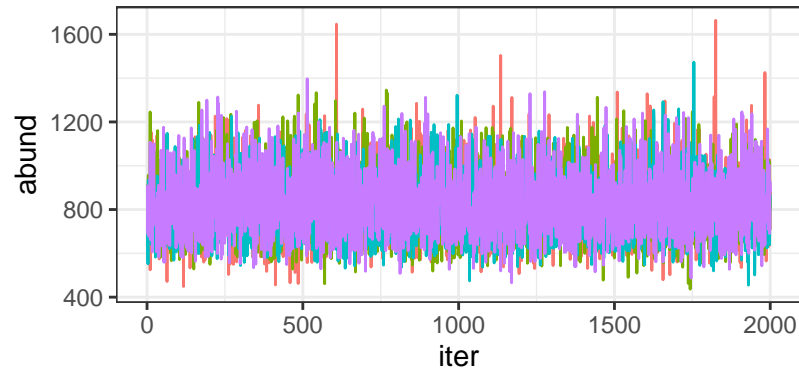
IHR_bb



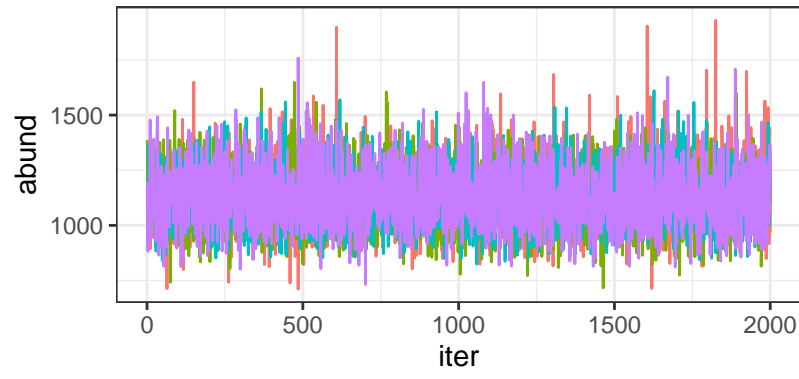
IR2_bb



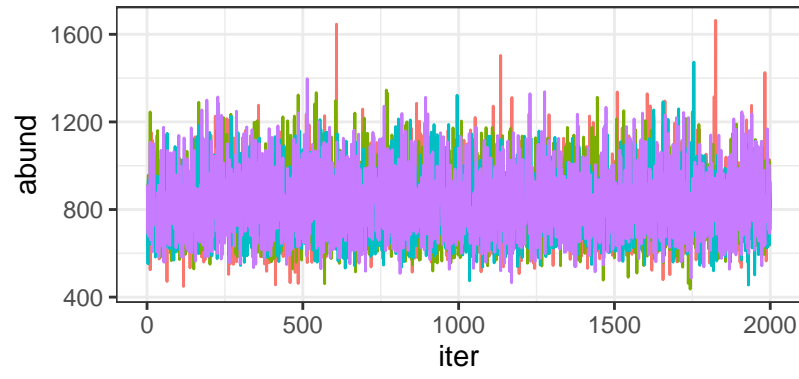
IML



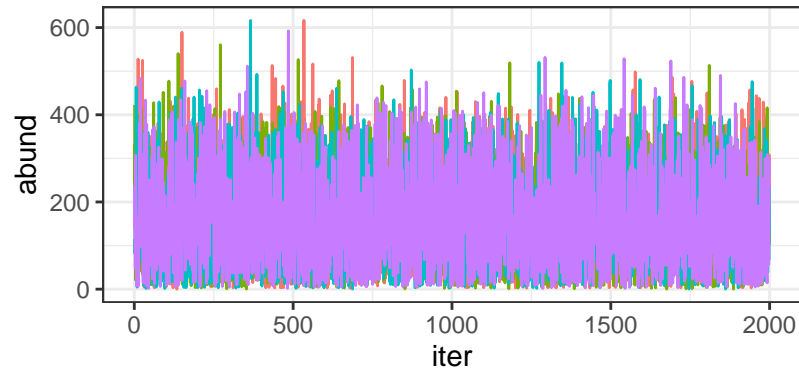
IR3



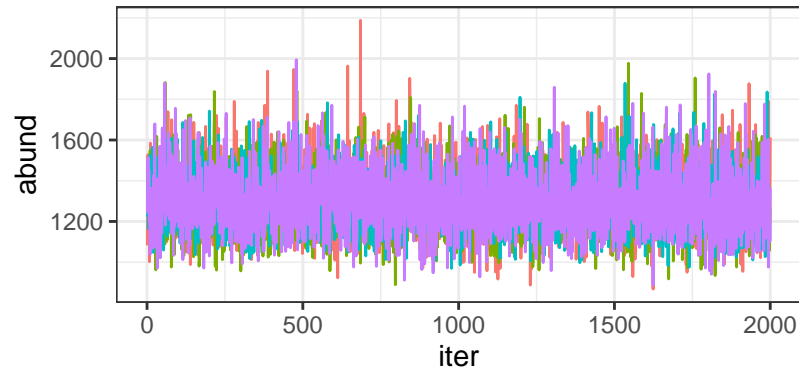
IML_bb



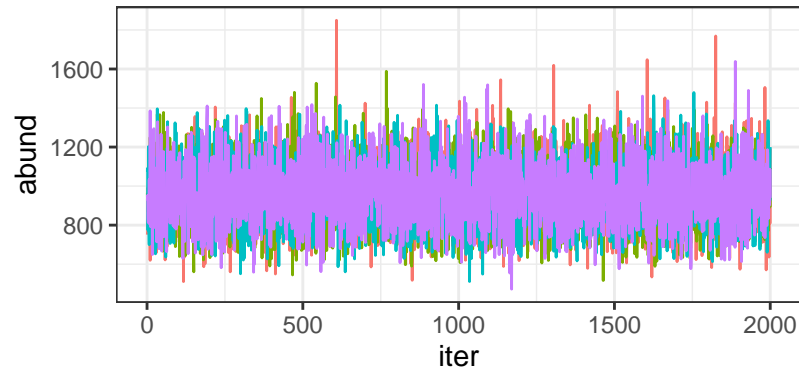
IR3_bb



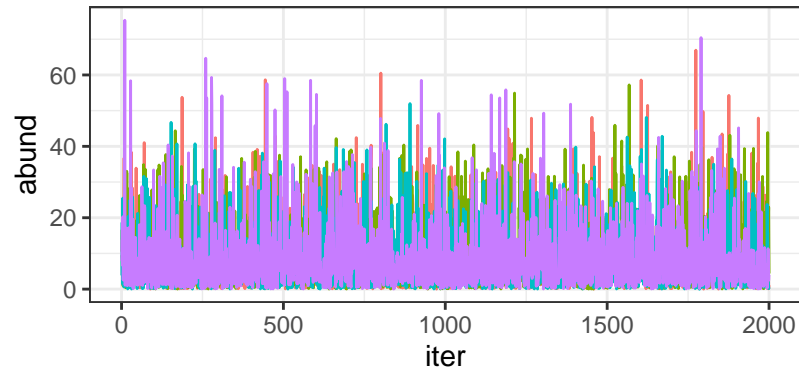
IR1



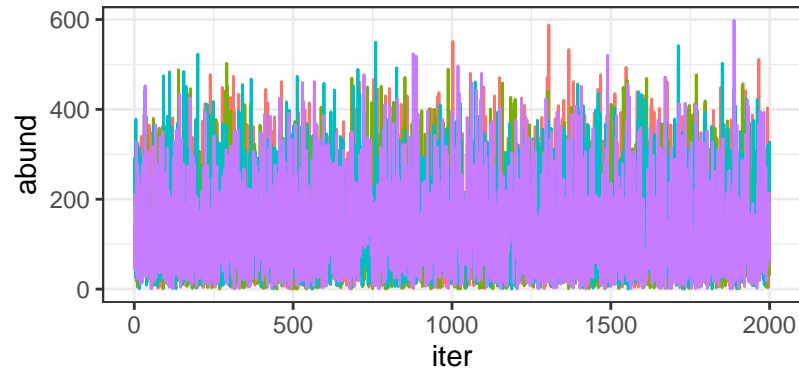
IR4



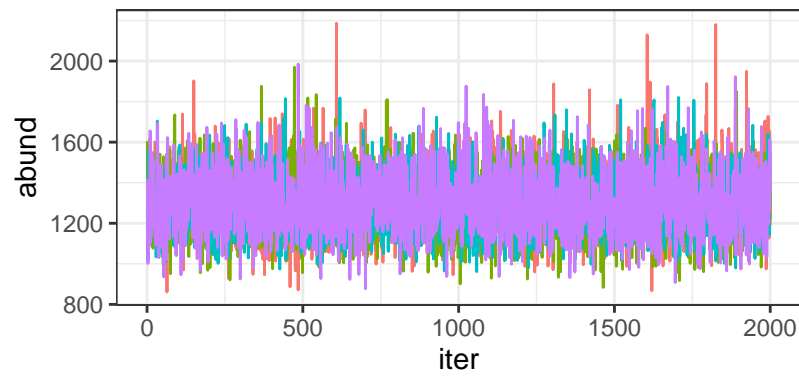
IR1_bb



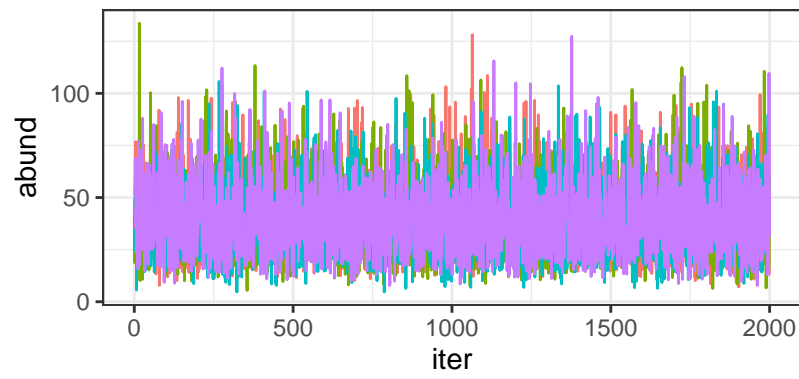
IR4_bb

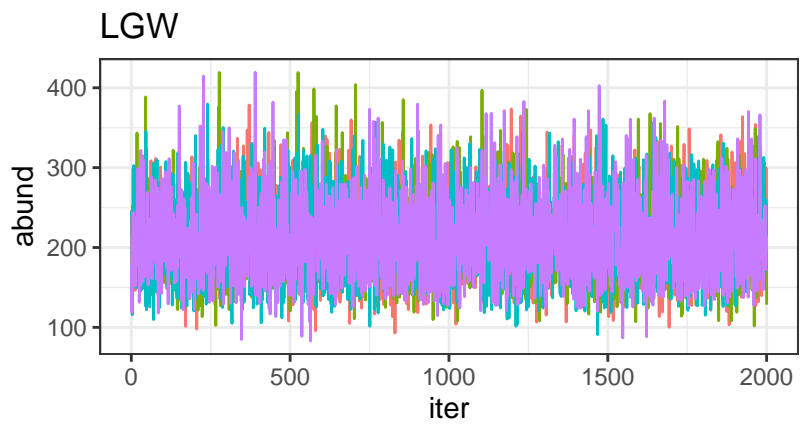
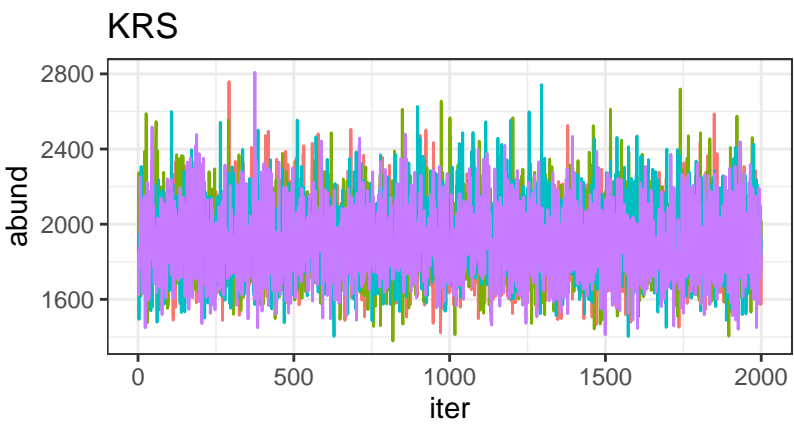
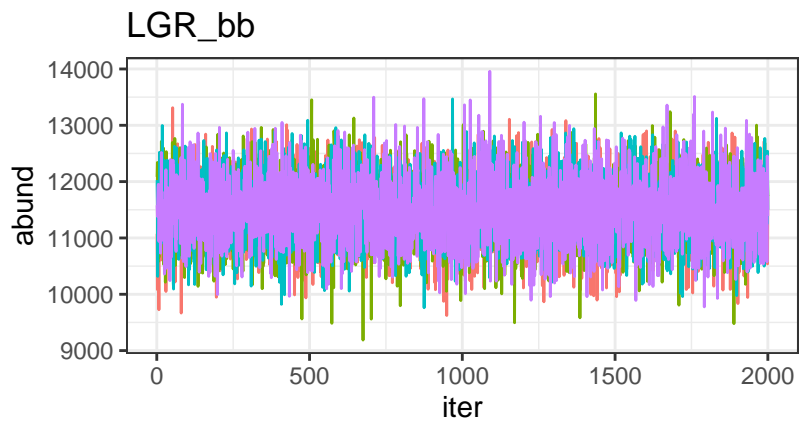
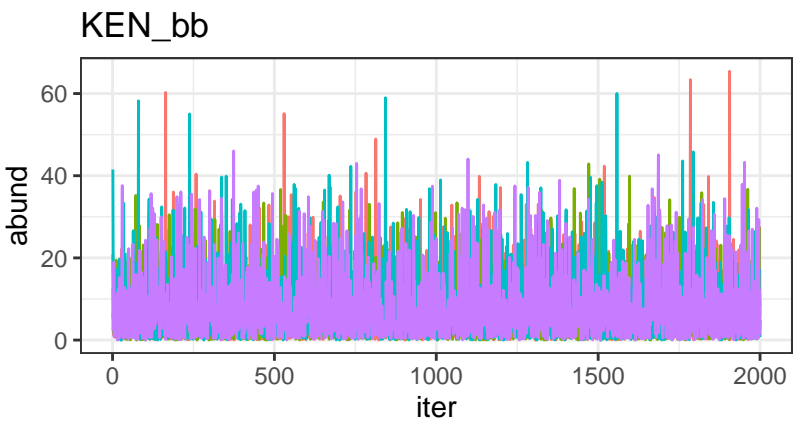
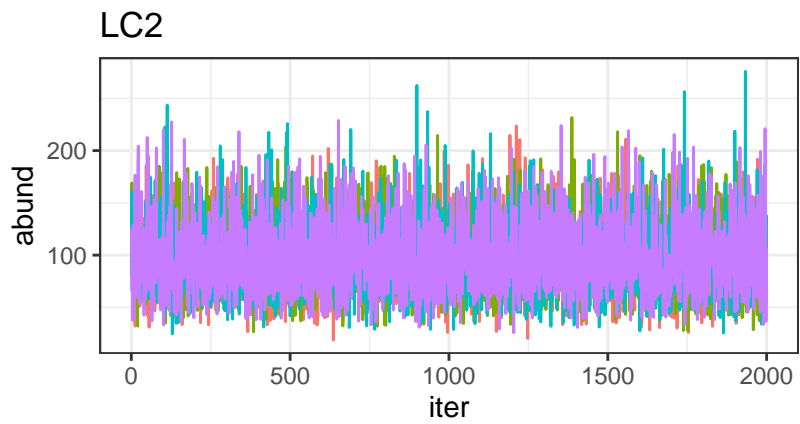
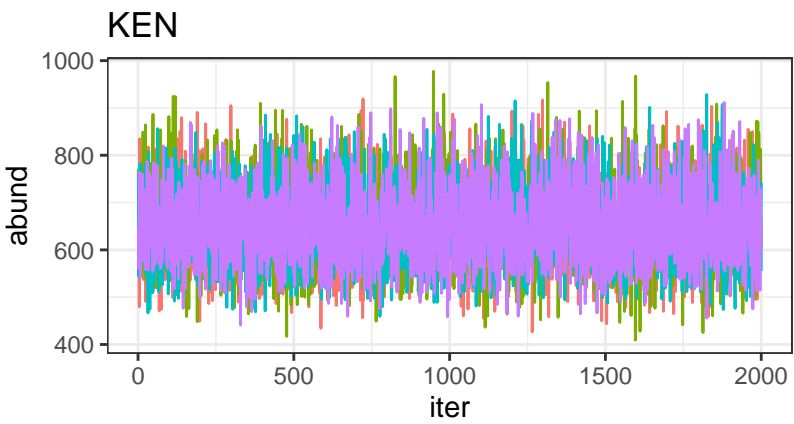
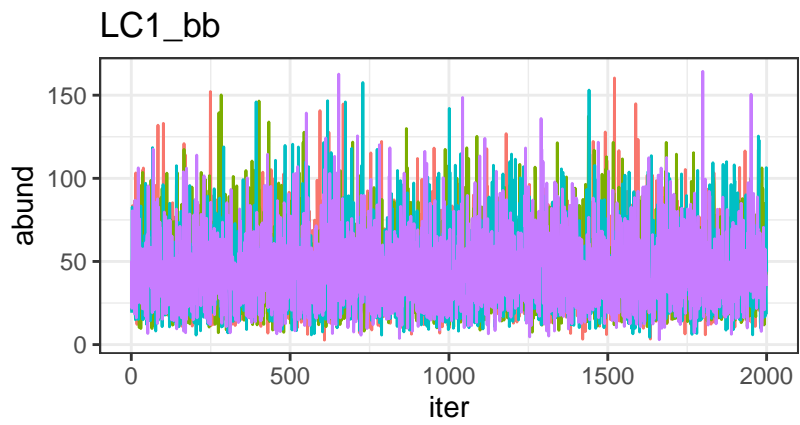
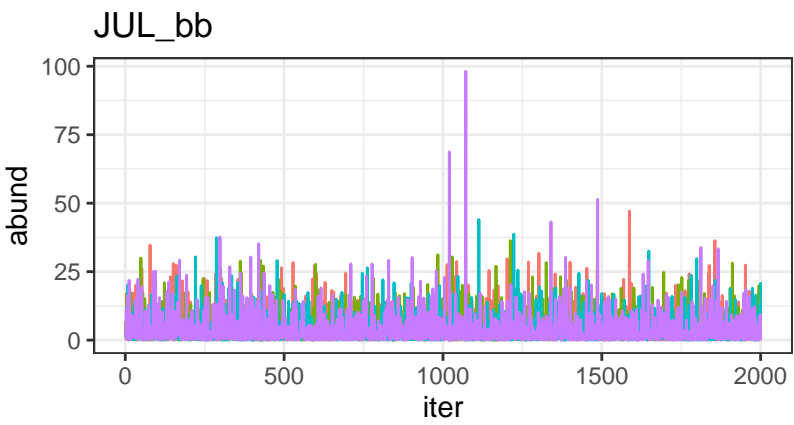
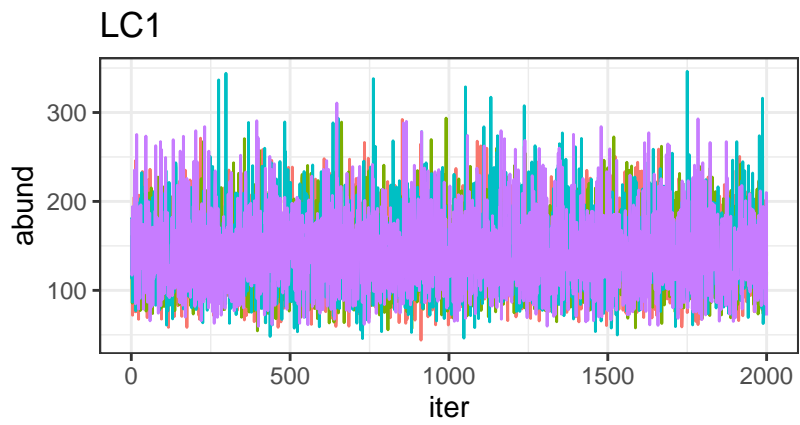
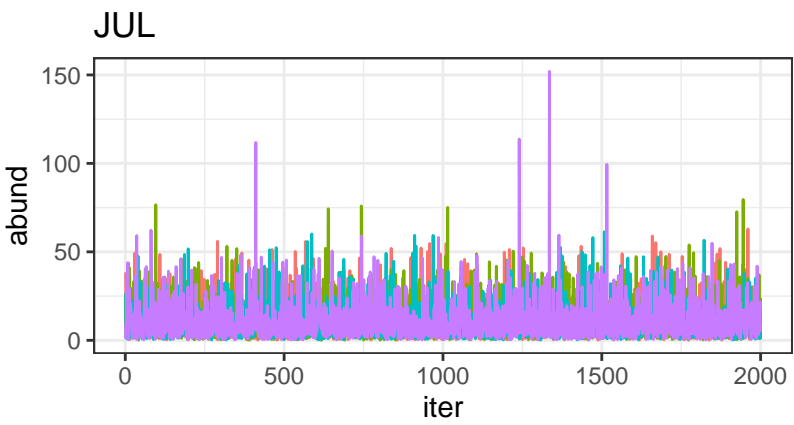
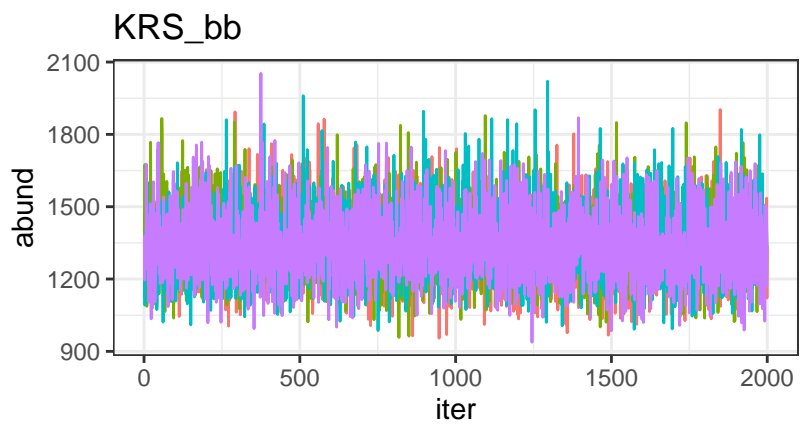
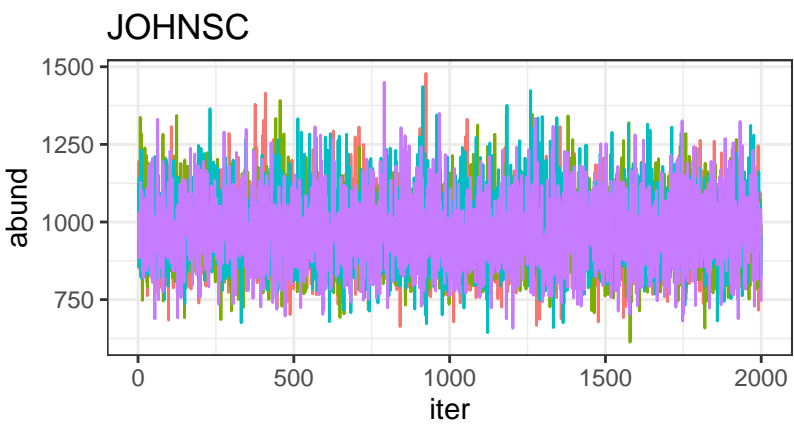


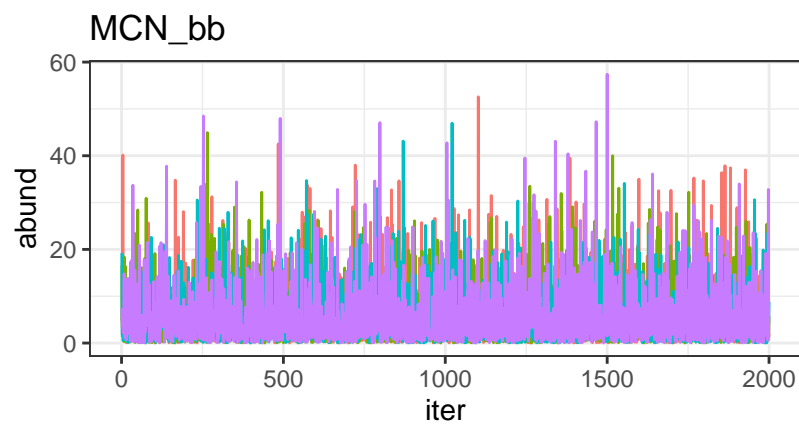
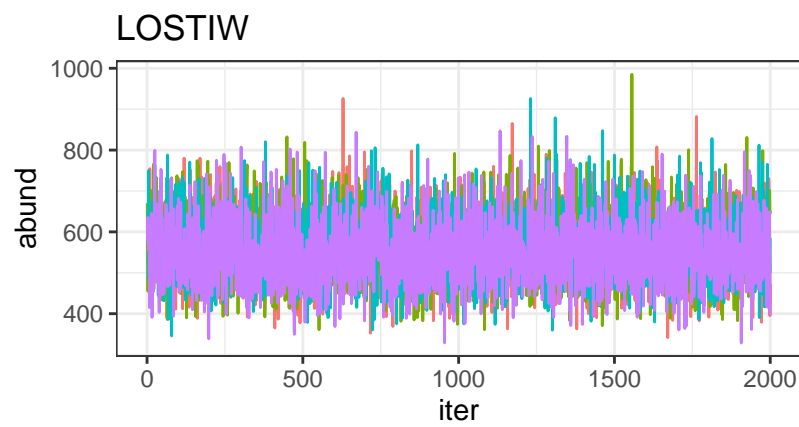
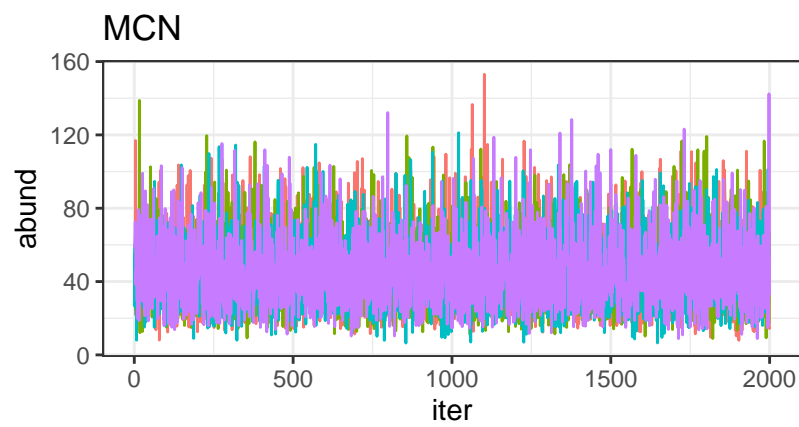
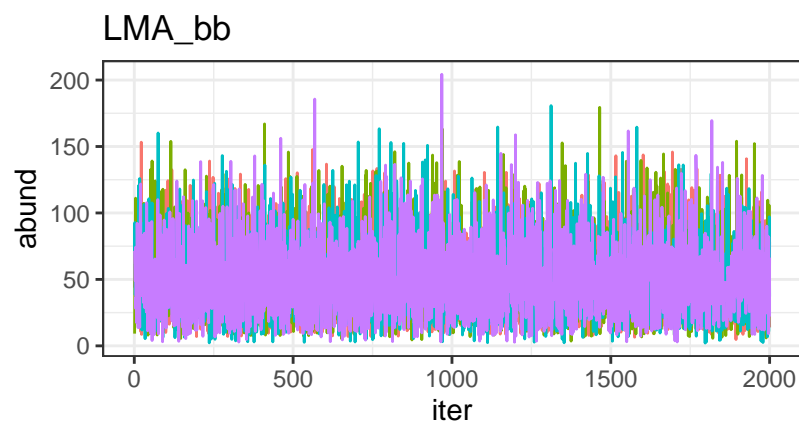
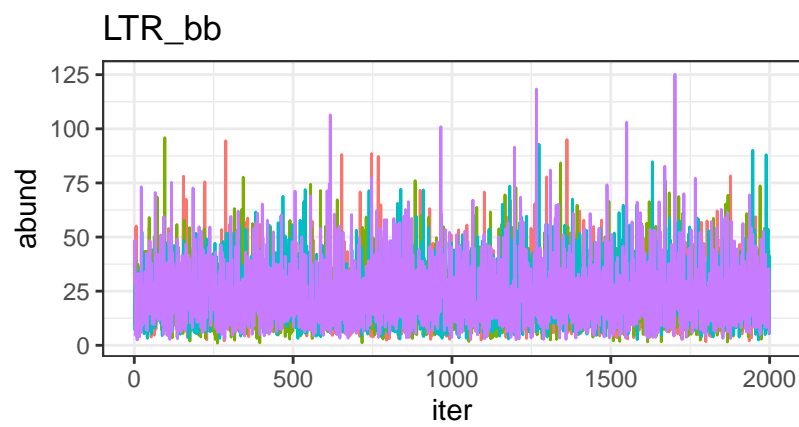
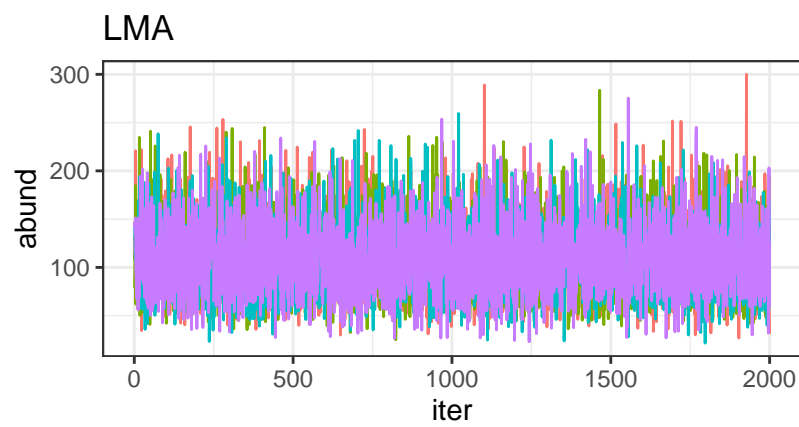
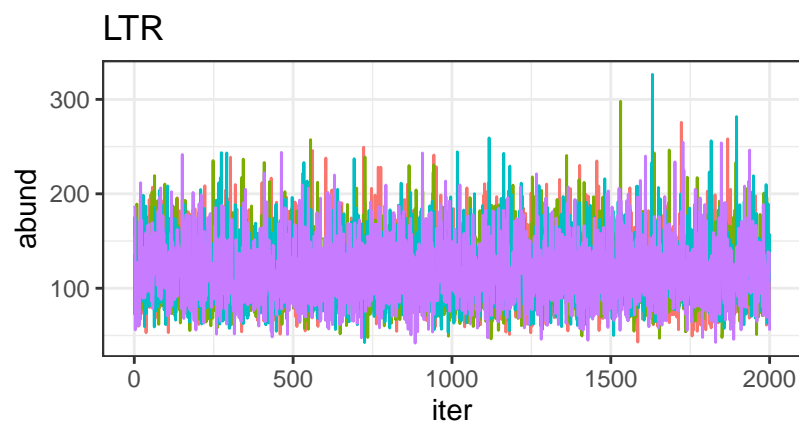
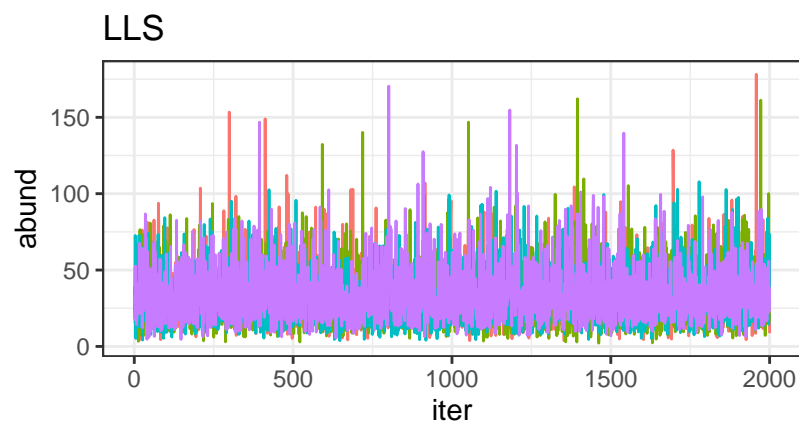
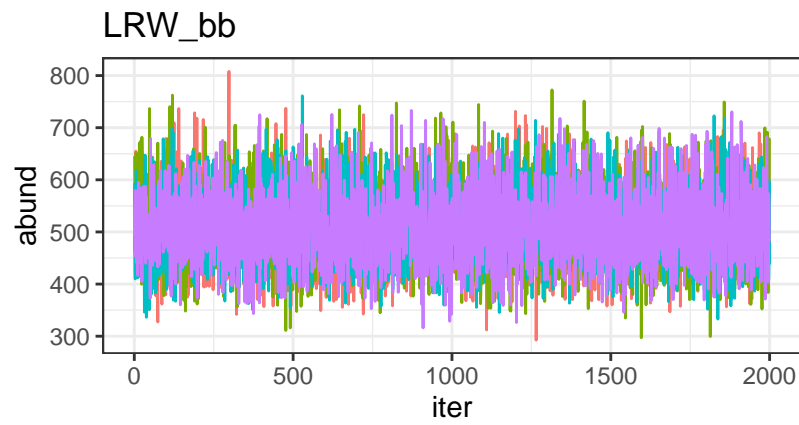
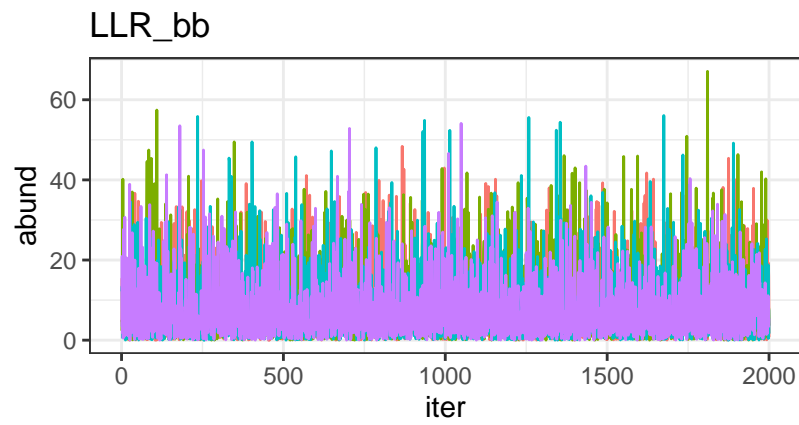
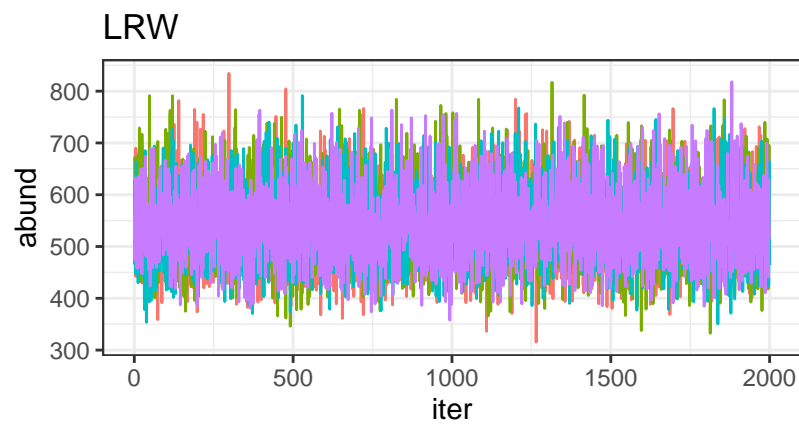
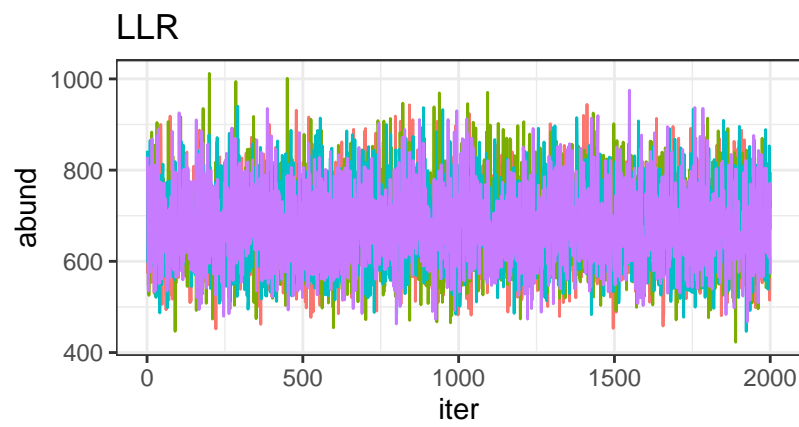
IR2



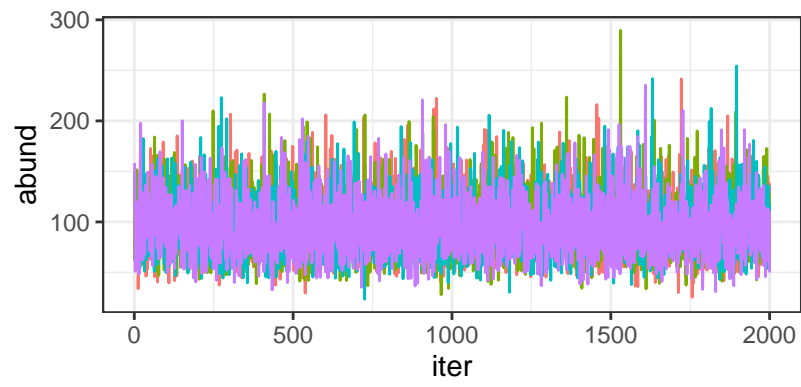
JD1



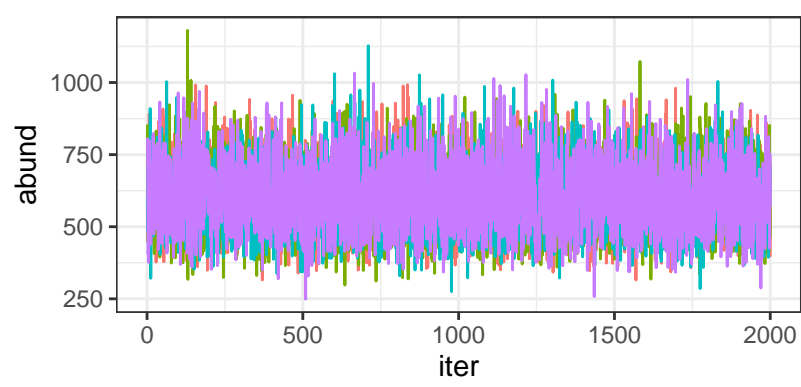




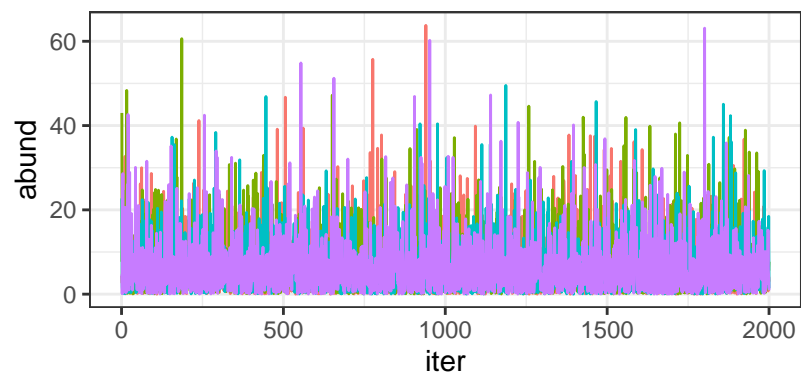
MTR



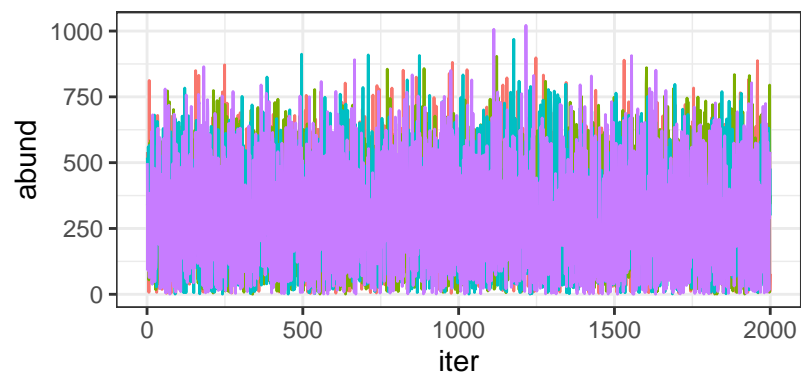
SC2



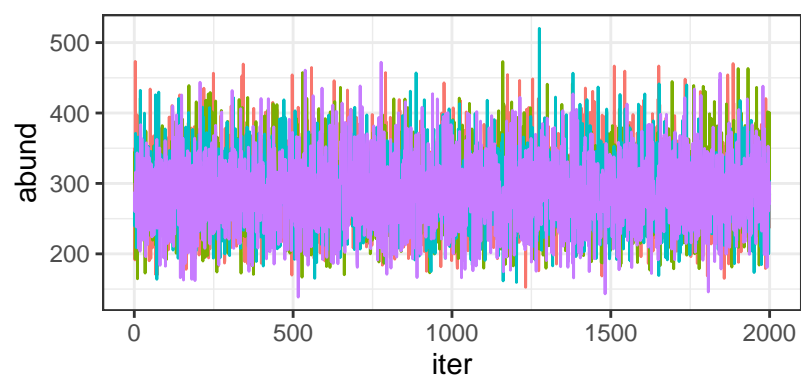
MTR_bb



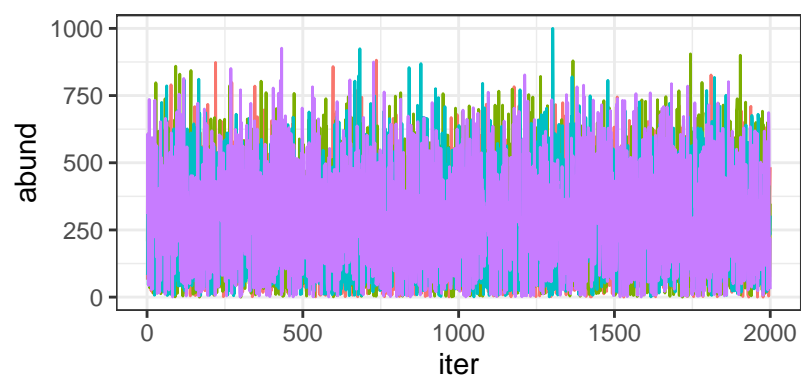
SC2_bb



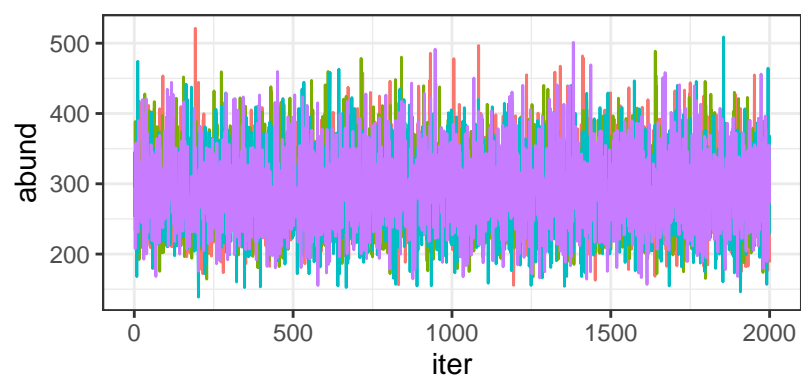
PAHH



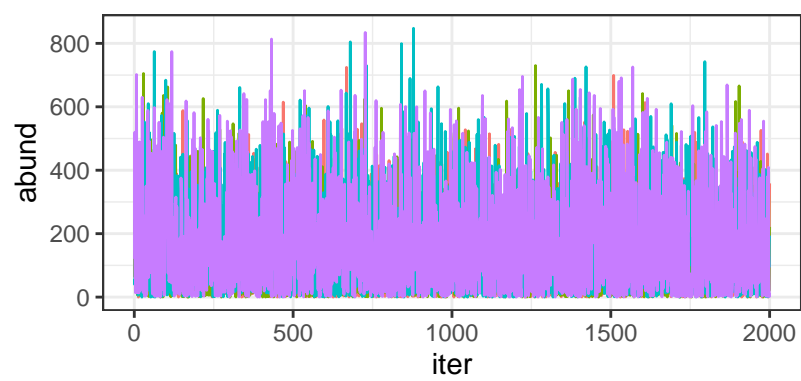
SC3



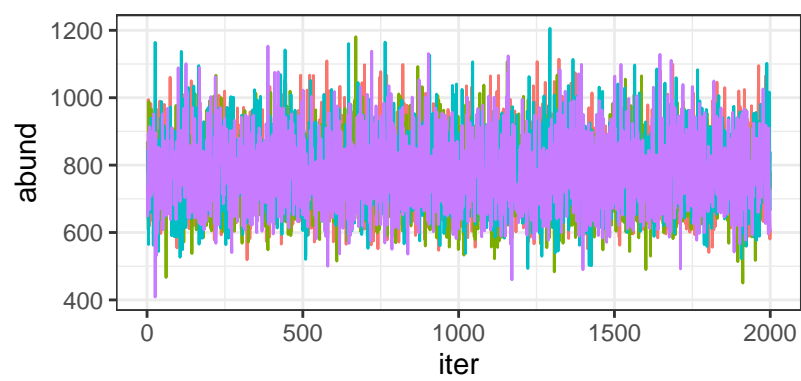
SALEFT



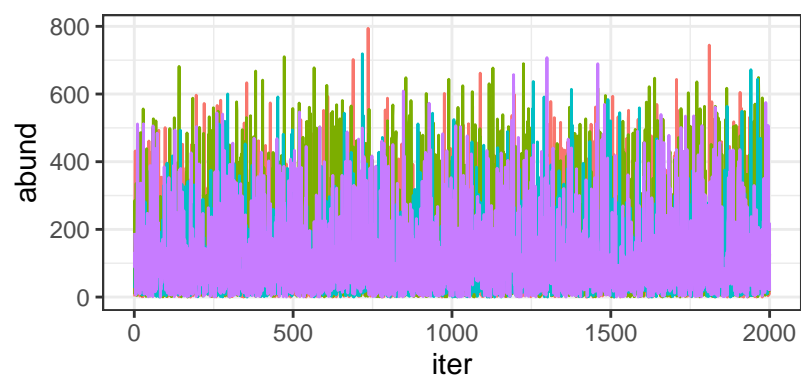
SC3_bb



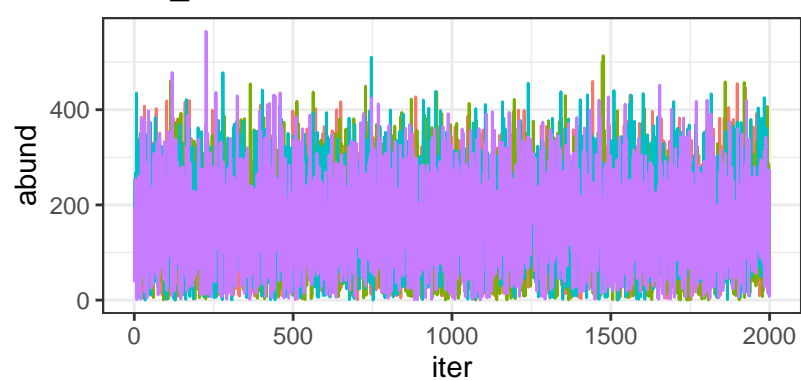
SC1



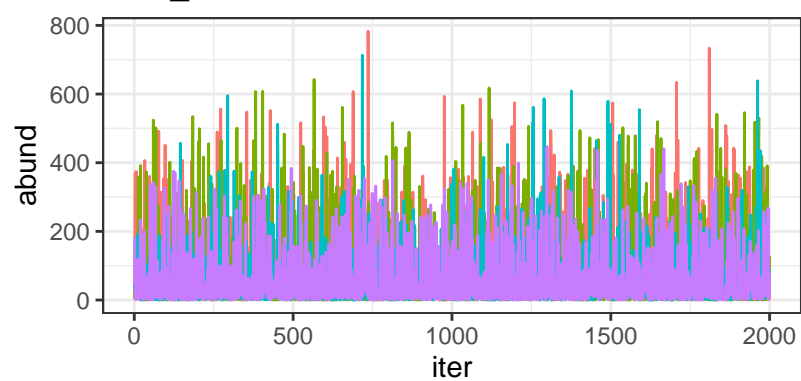
SC4



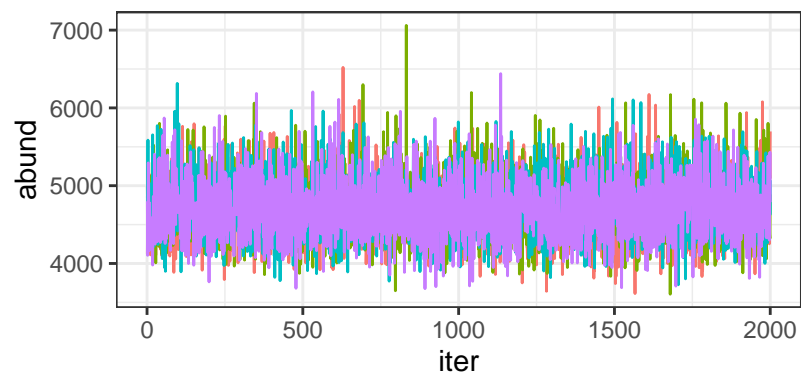
SC1_bb



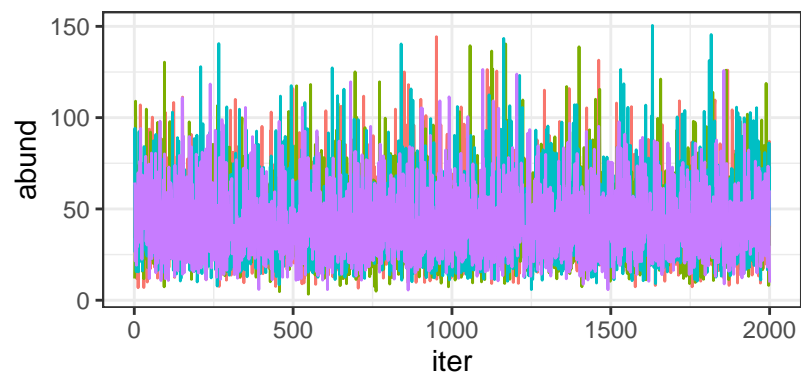
SC4_bb



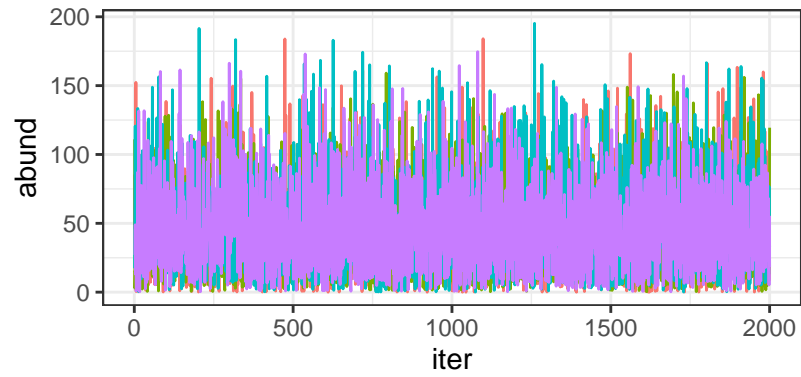
SFG



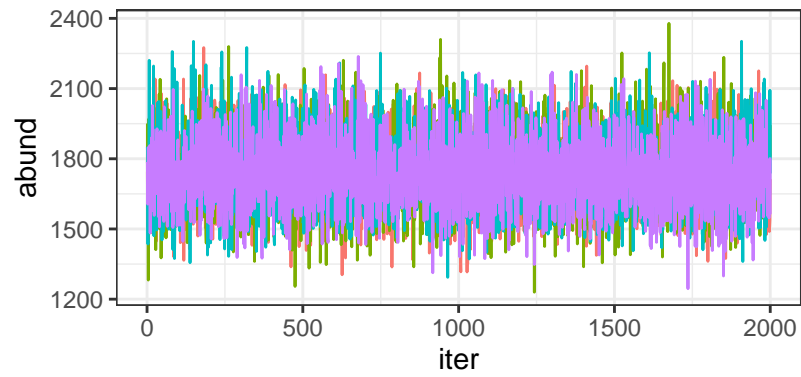
TFH_bb



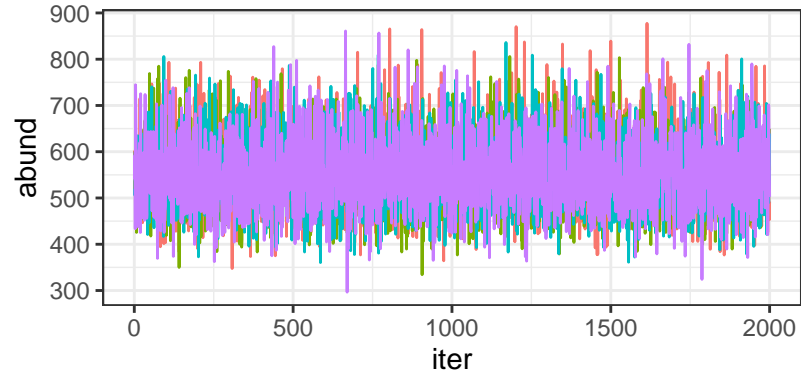
SFG_bb



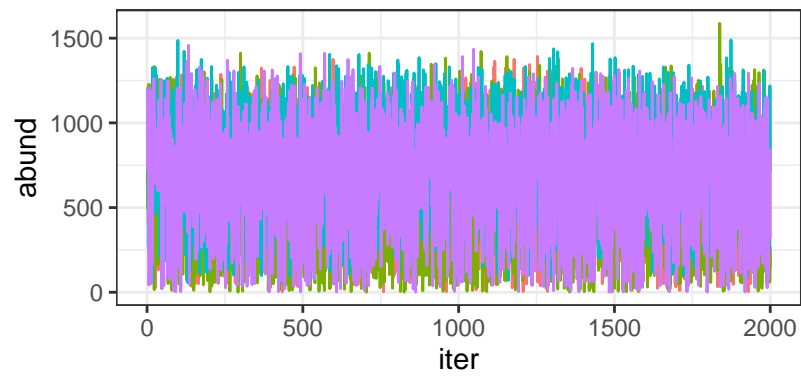
UGR



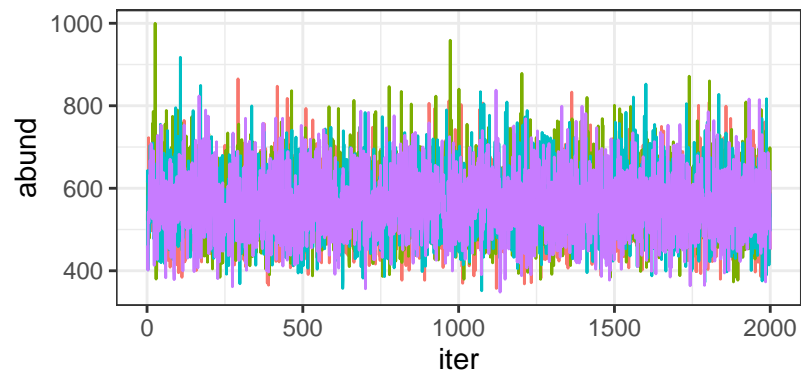
STL



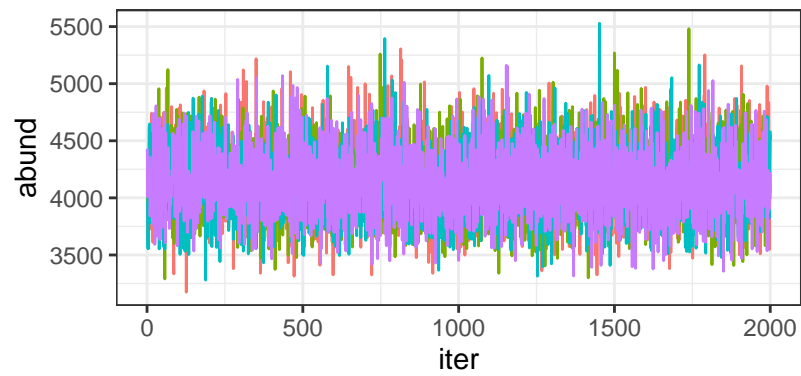
UGR_bb



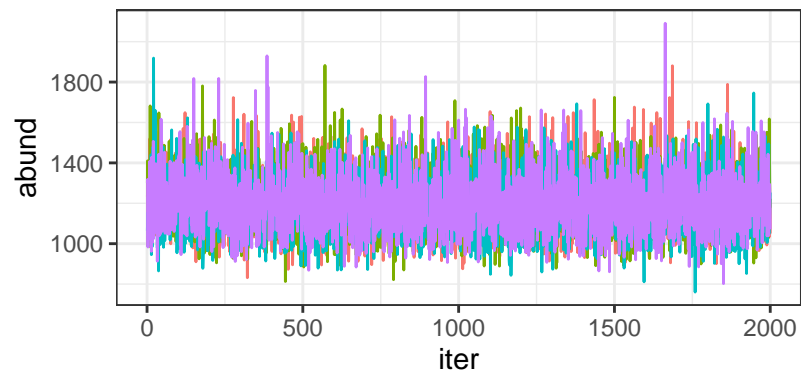
STR



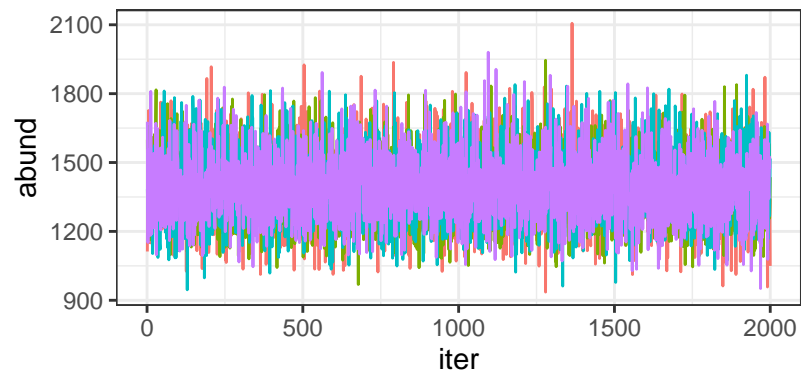
USE



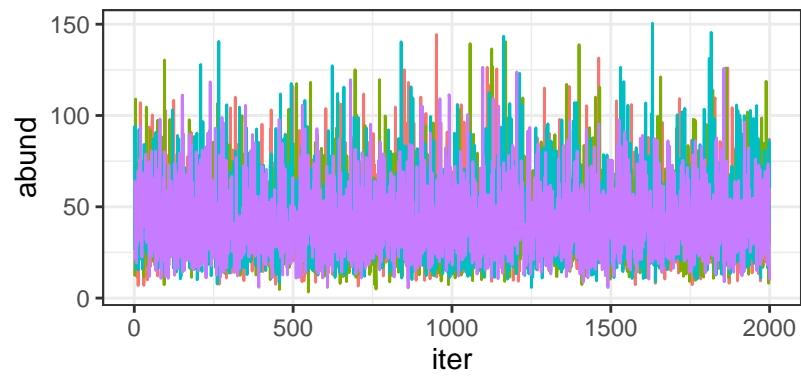
TAY



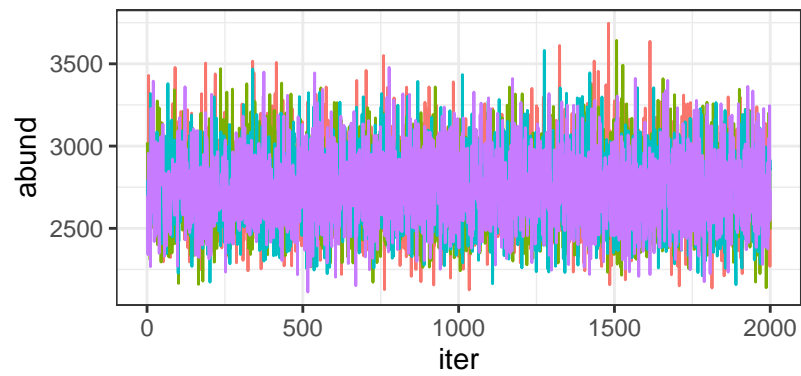
USE_bb



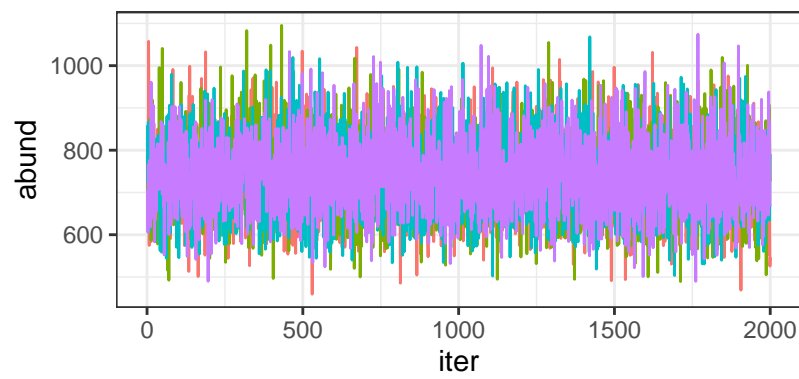
TFH



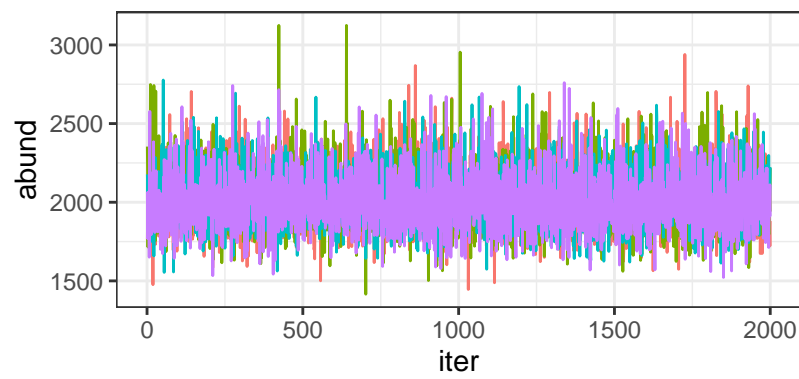
USI



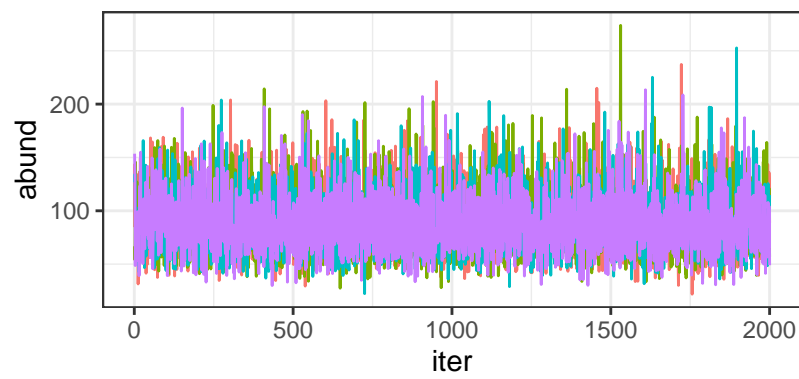
USI_bb



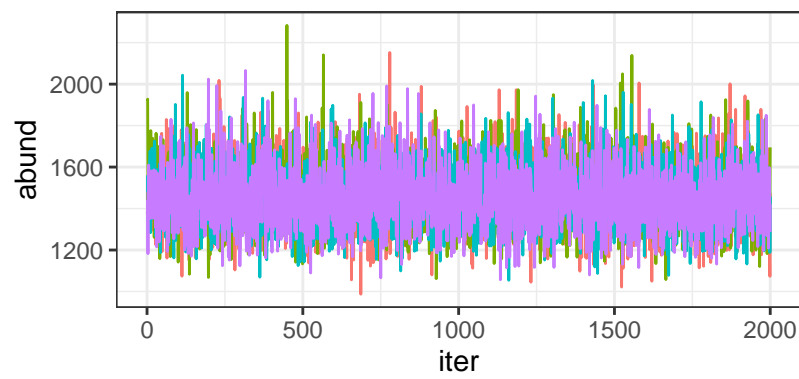
WR1



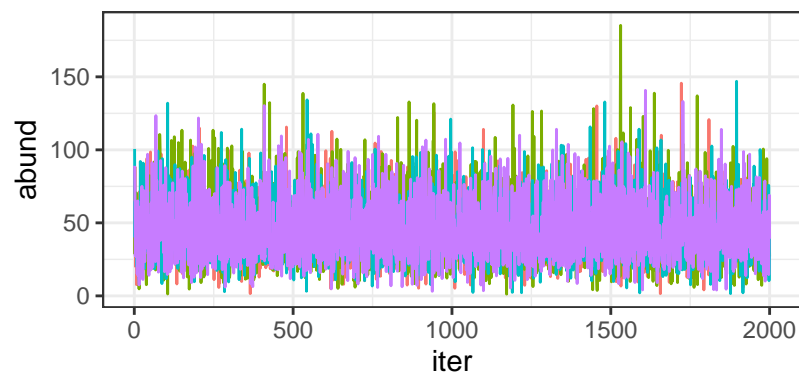
UTR



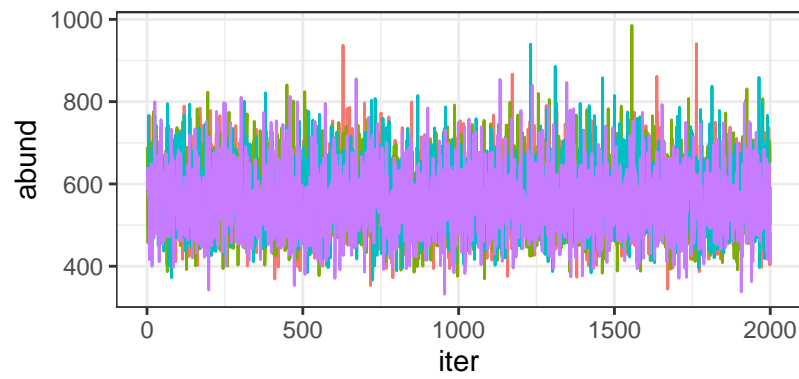
WR1_bb



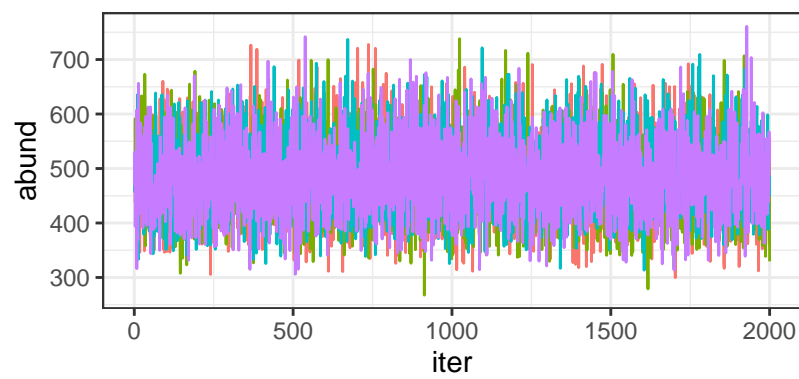
UTR_bb



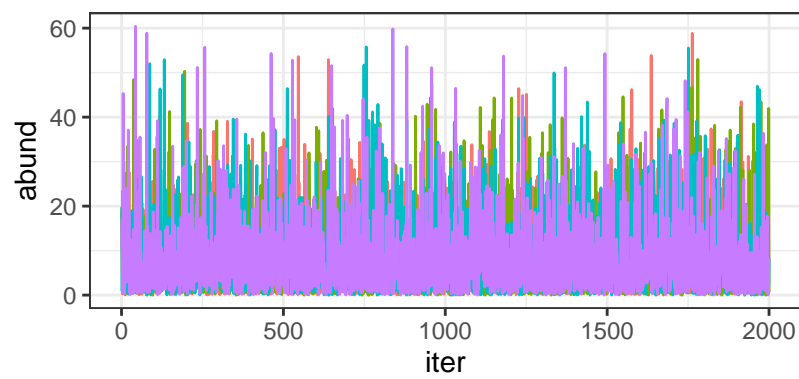
WR2



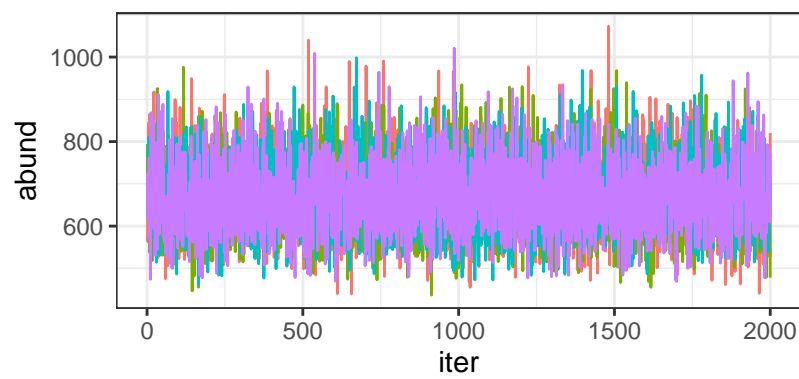
VC1



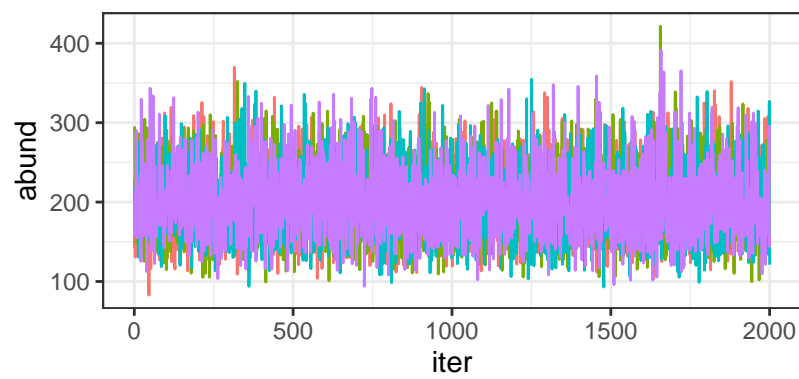
WR2_bb



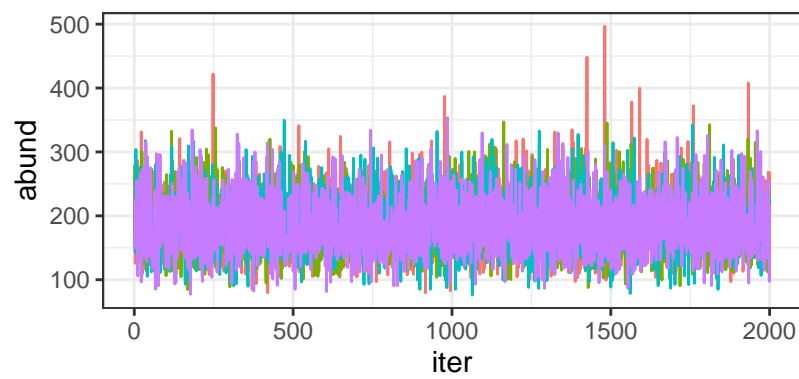
VC2



YFK



VC2_bb



ZEN

