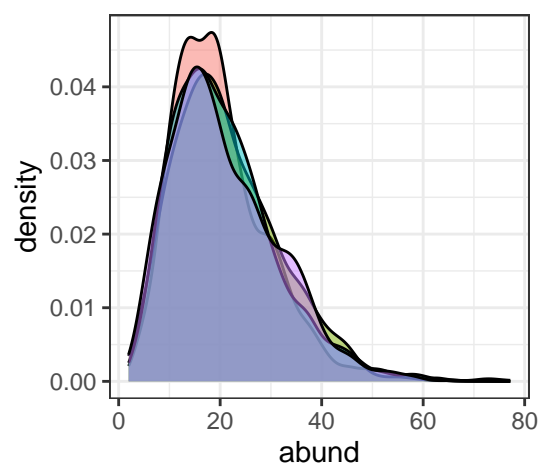
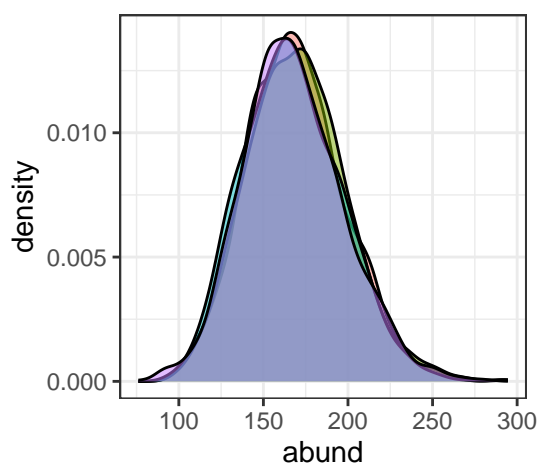


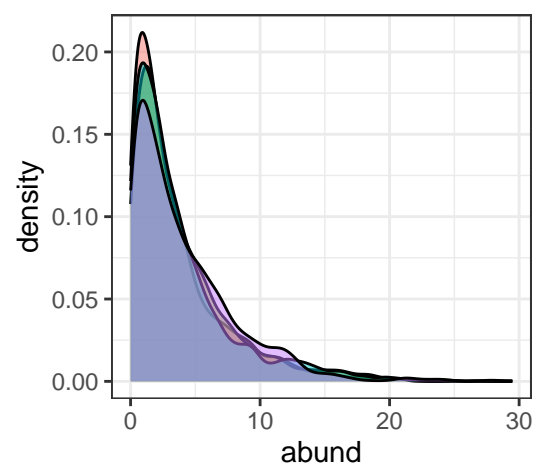
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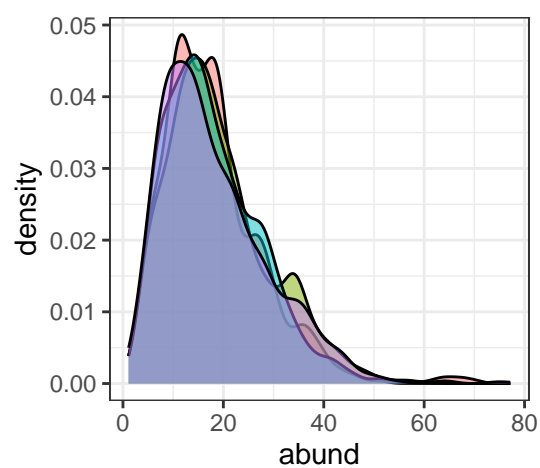
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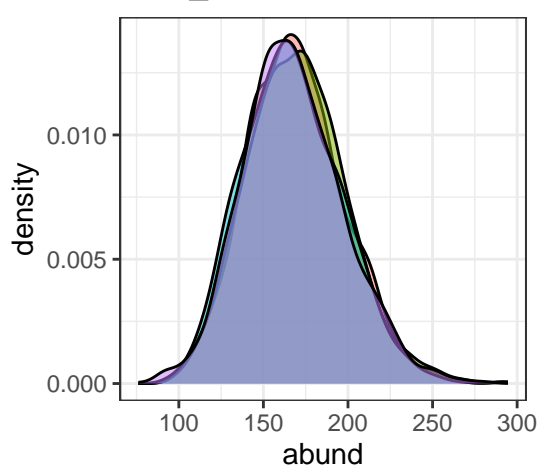
EVU\_bb



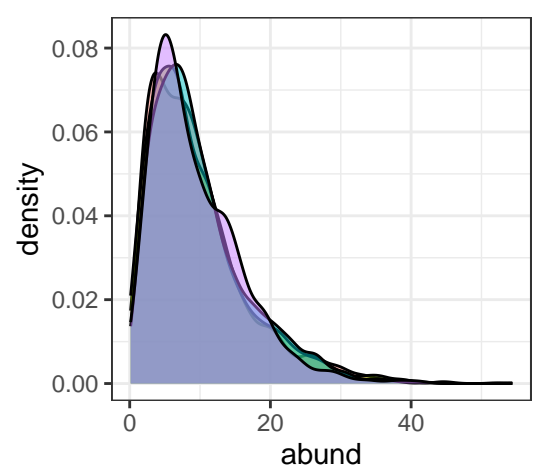
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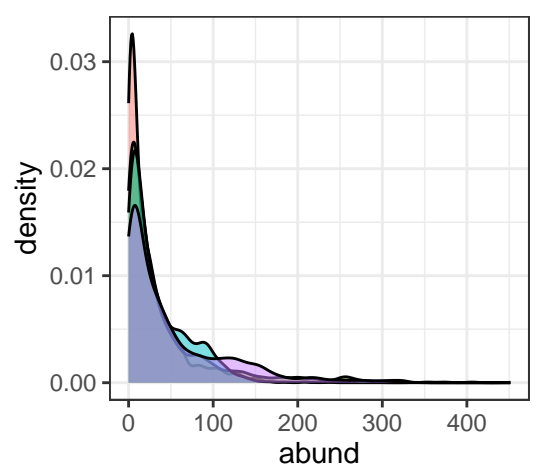
ESS\_bb



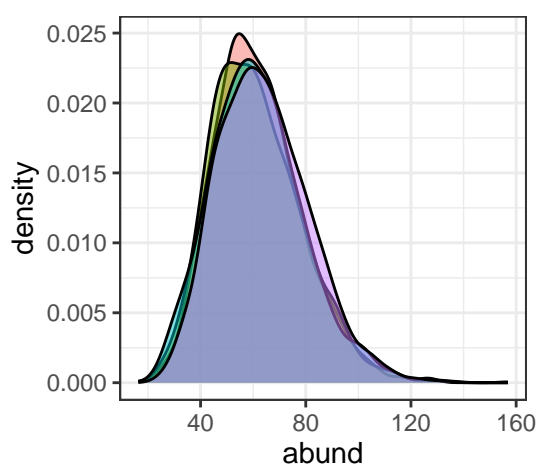
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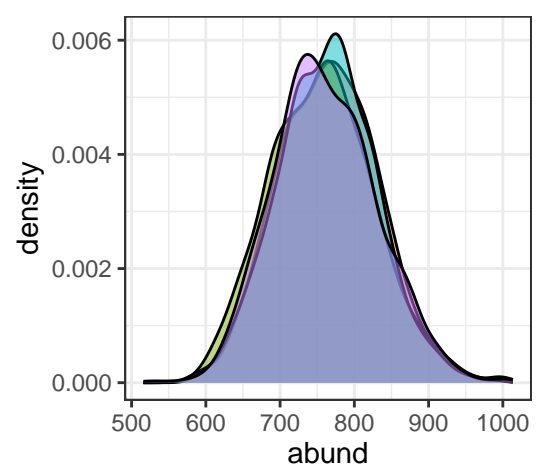
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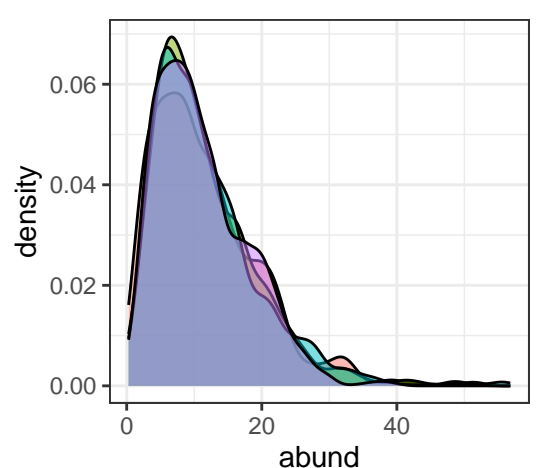
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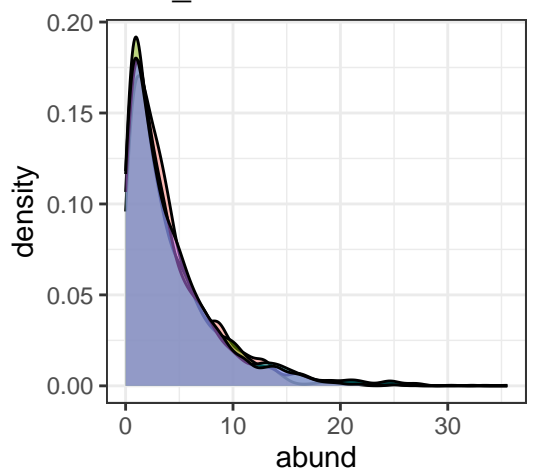
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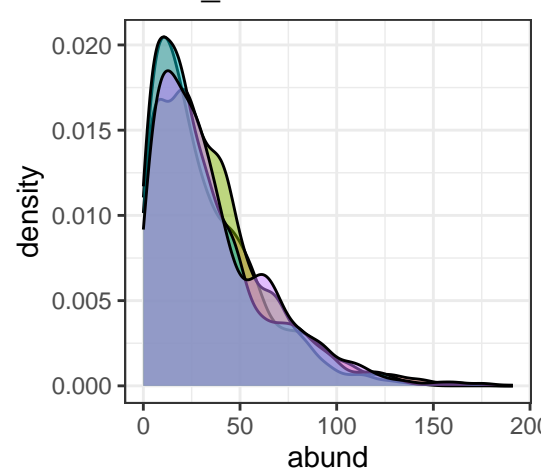
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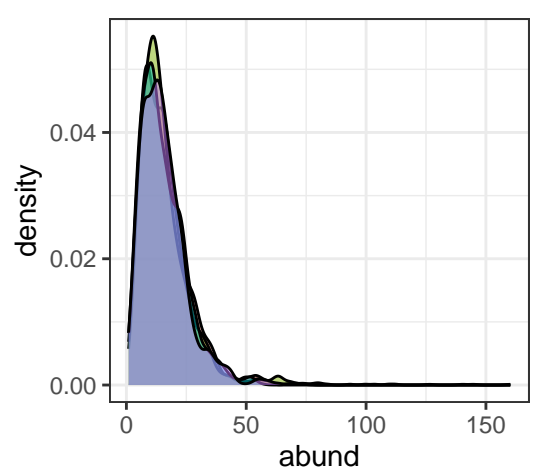
EVL\_bb



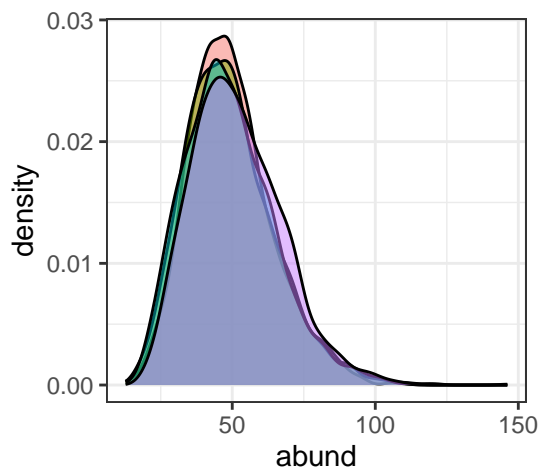
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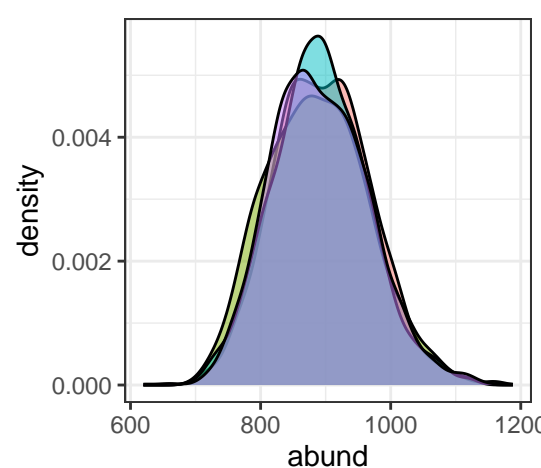
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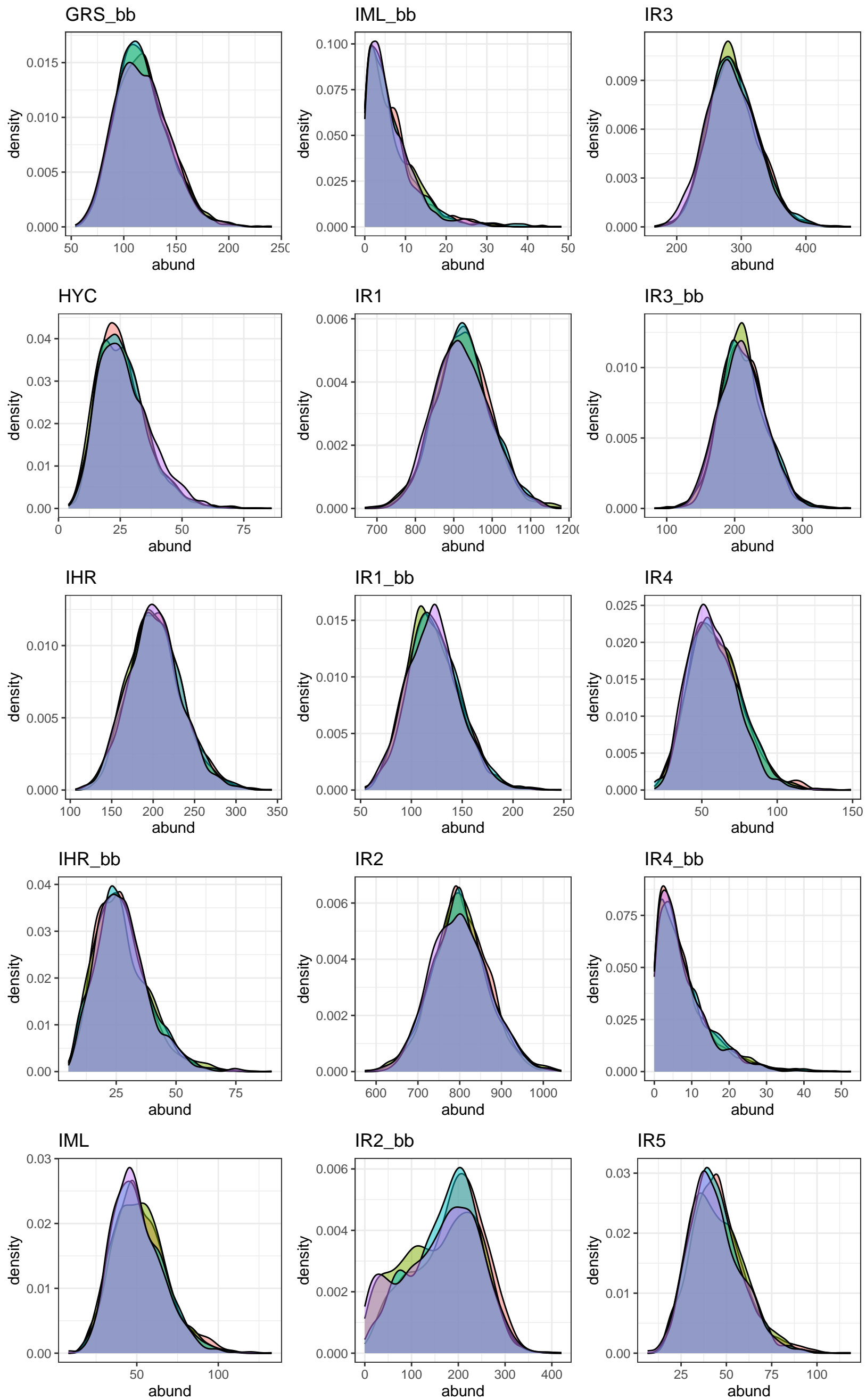


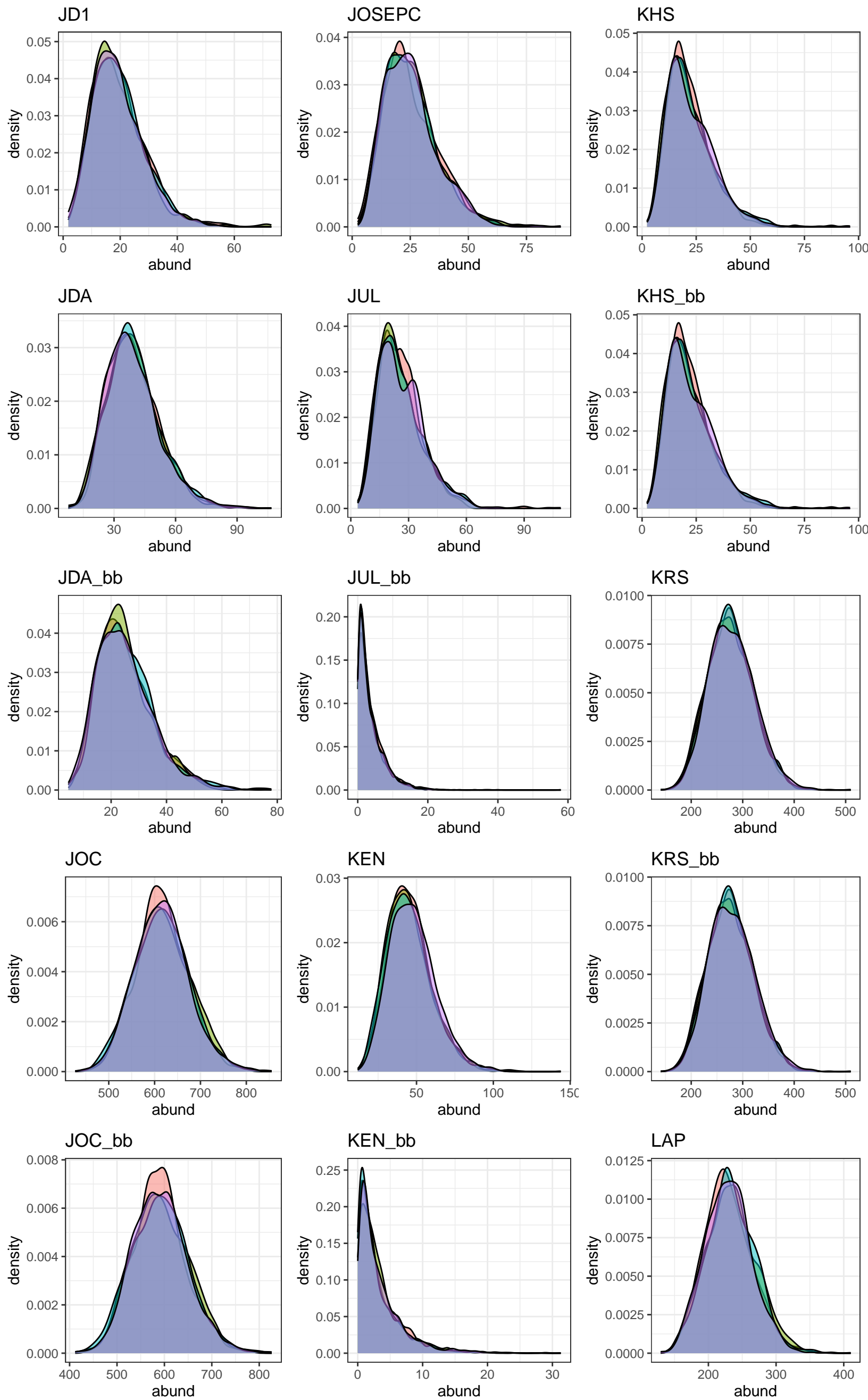
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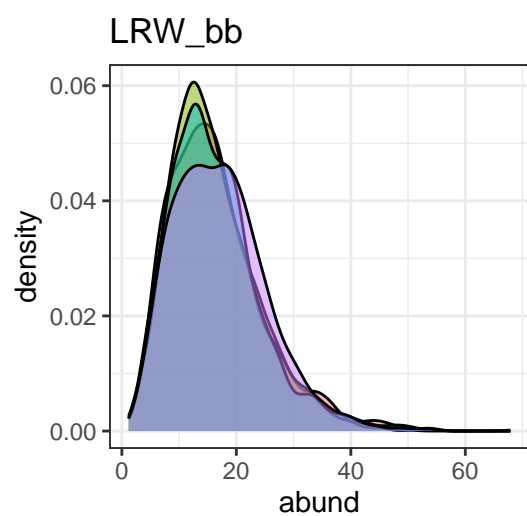
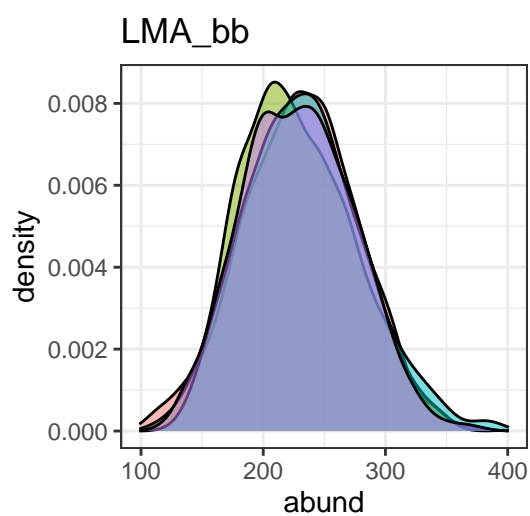
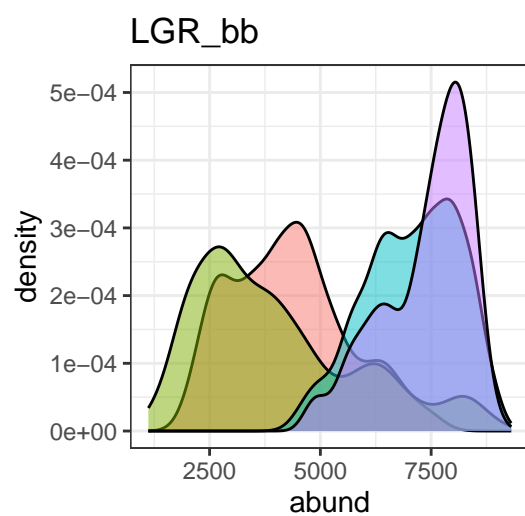
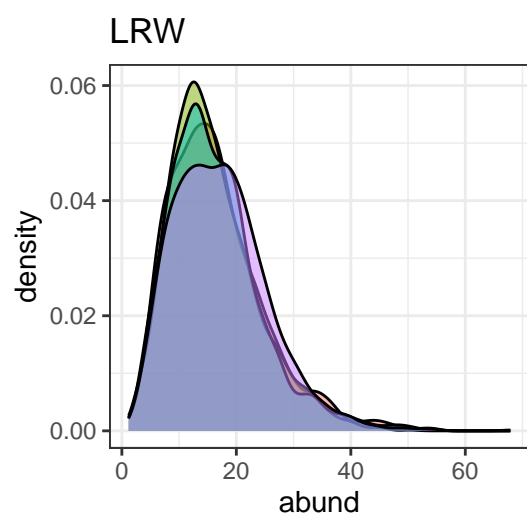
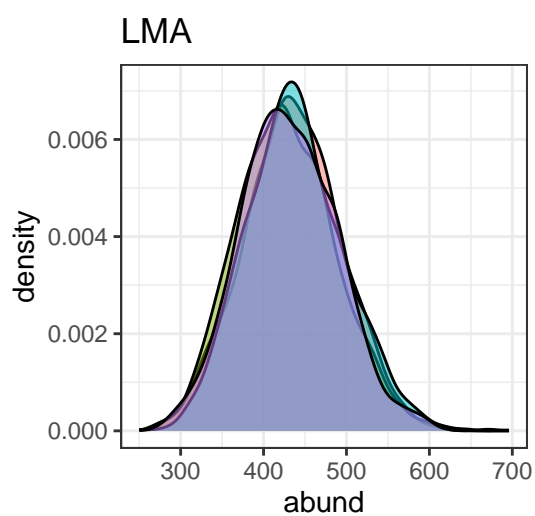
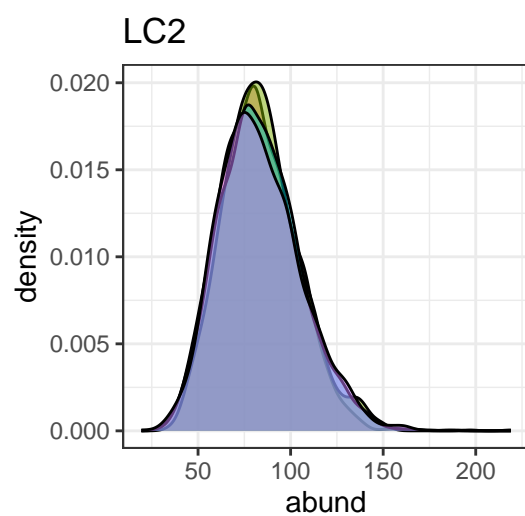
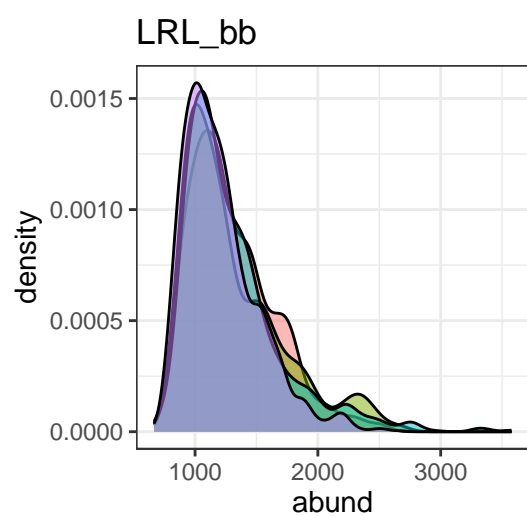
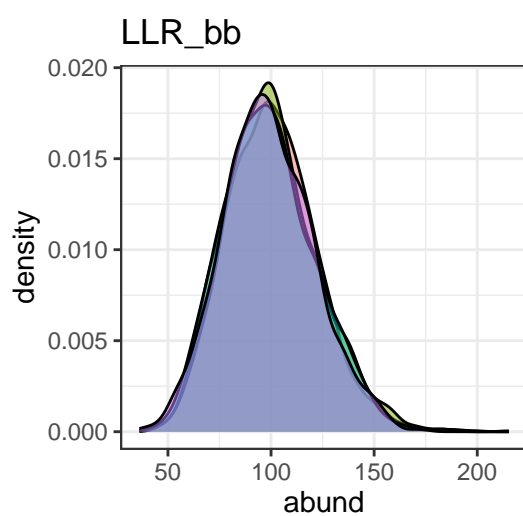
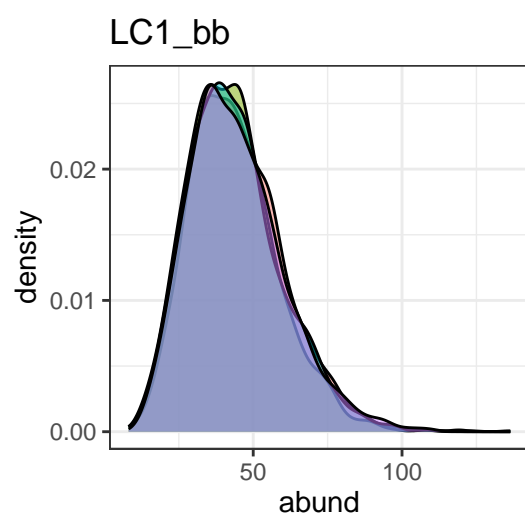
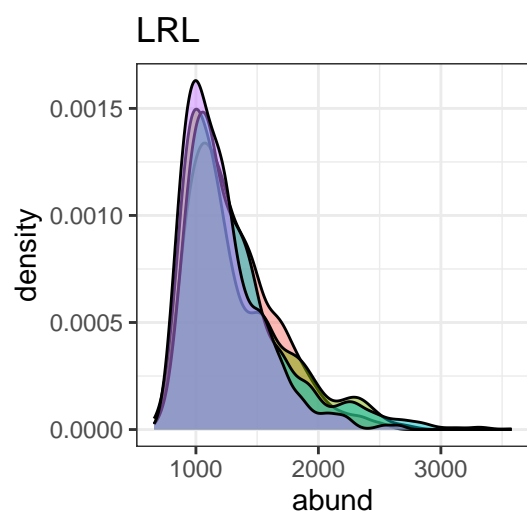
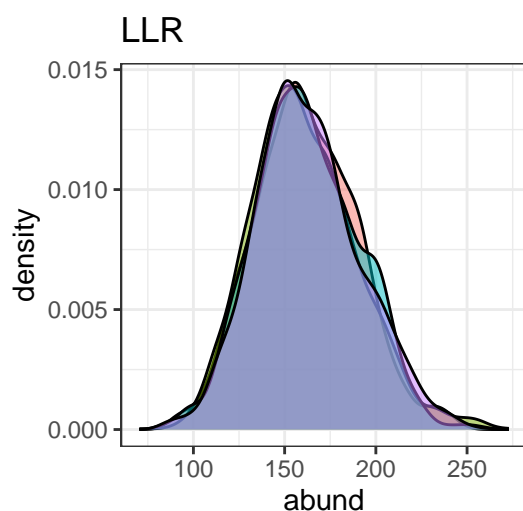
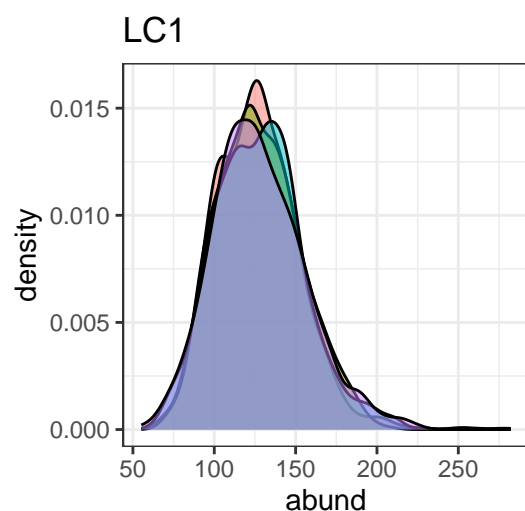
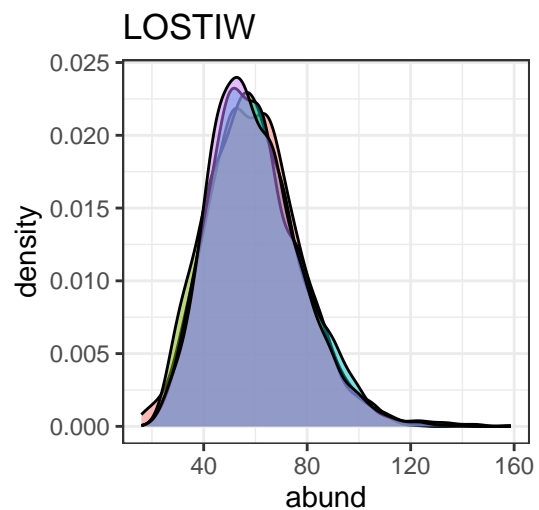
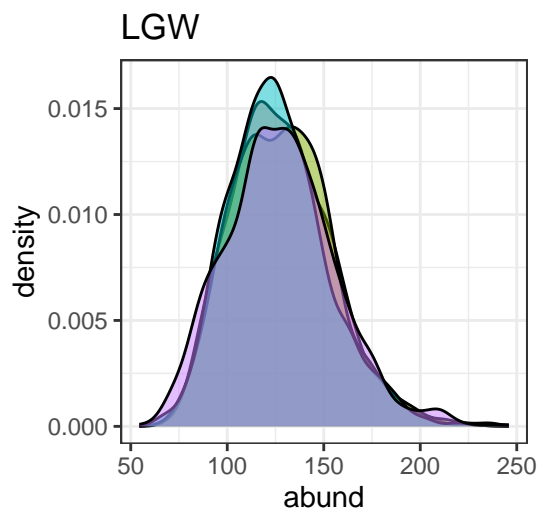
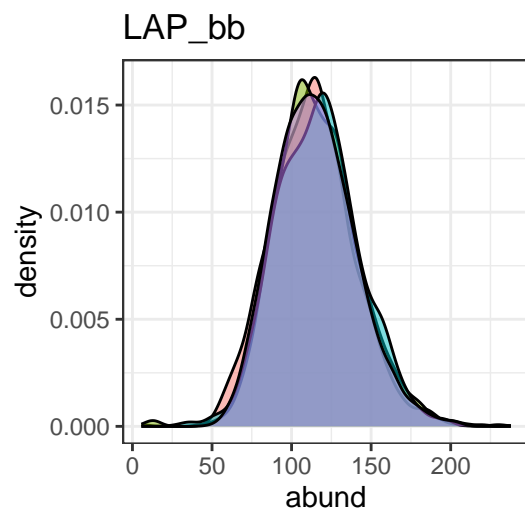


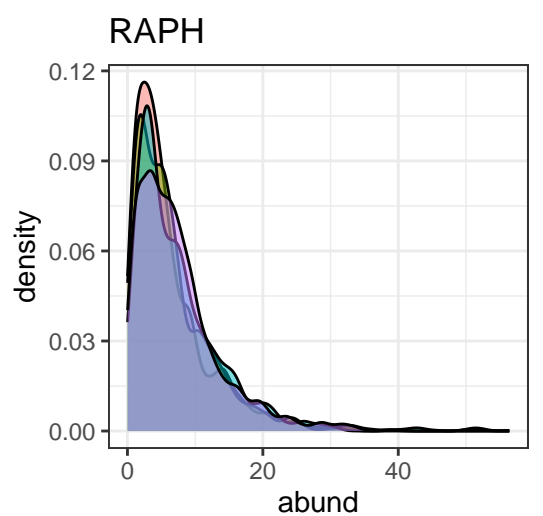
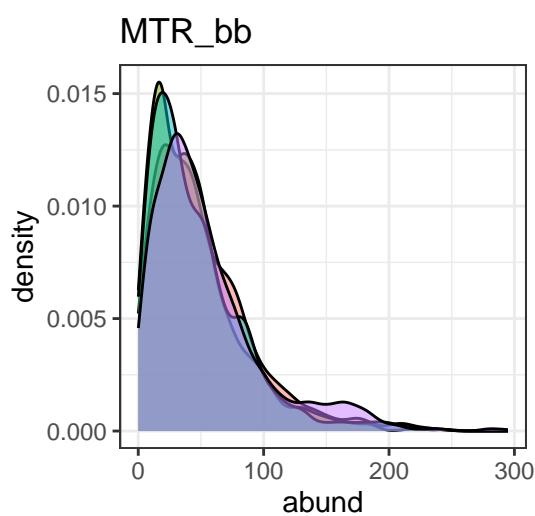
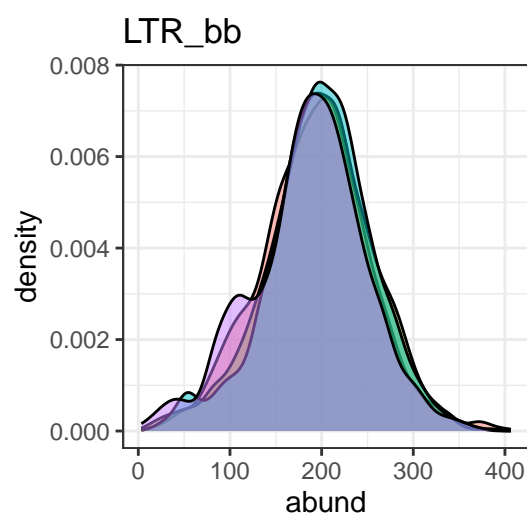
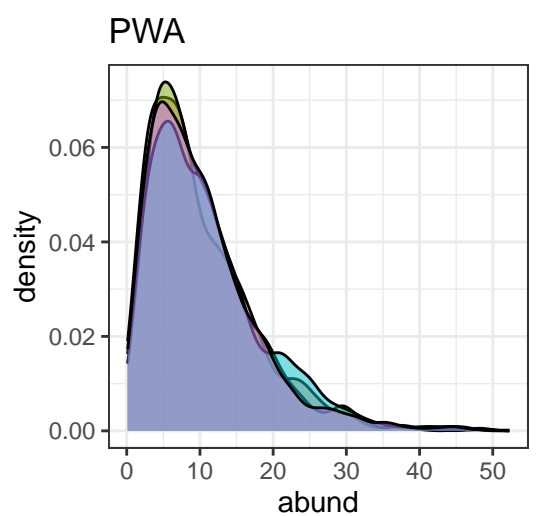
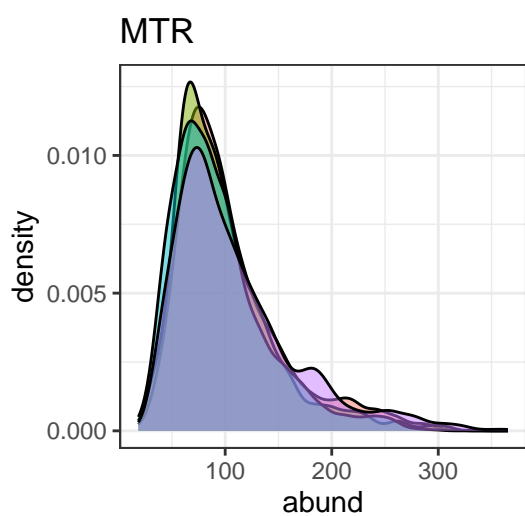
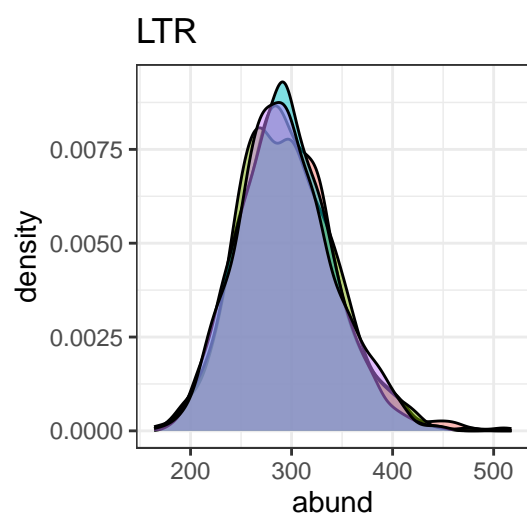
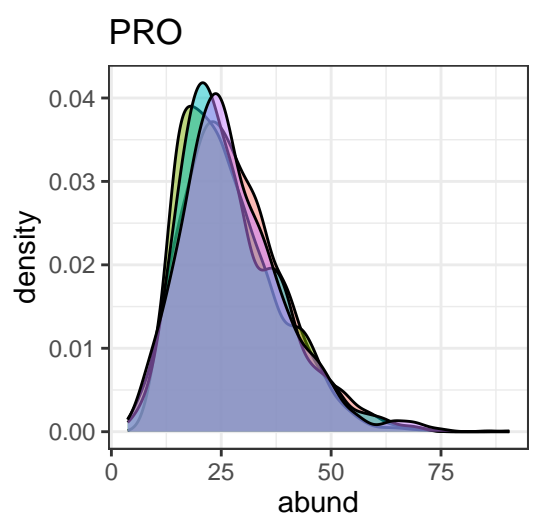
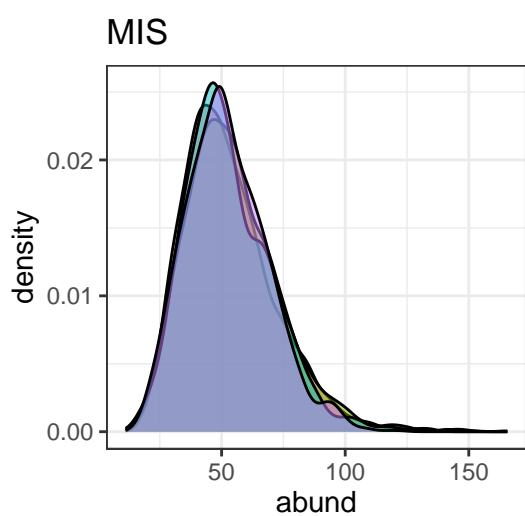
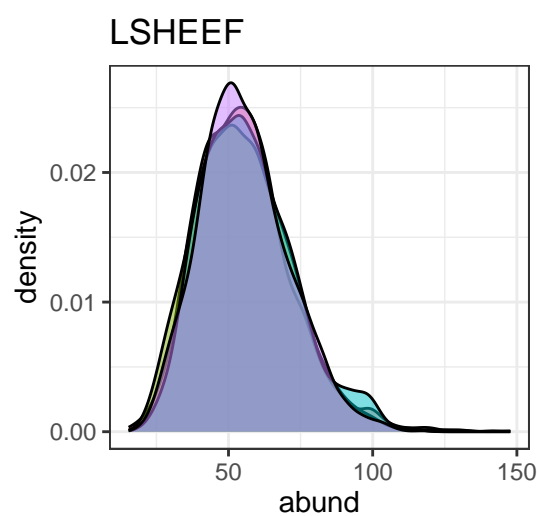
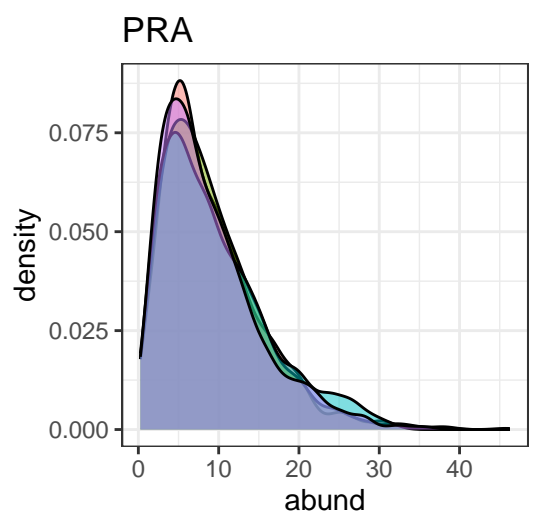
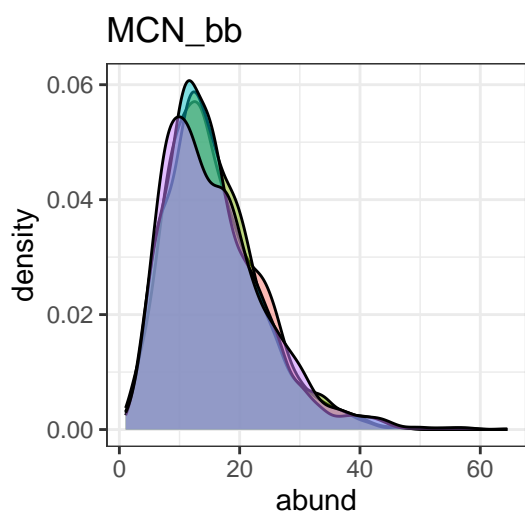
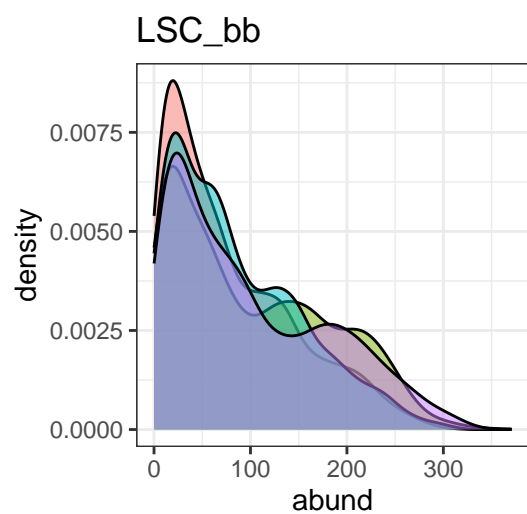
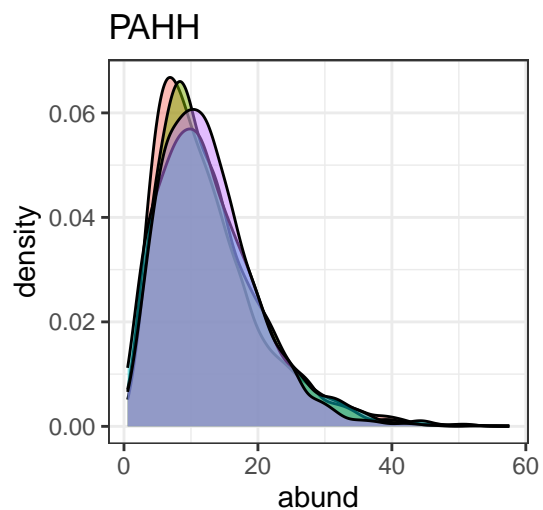
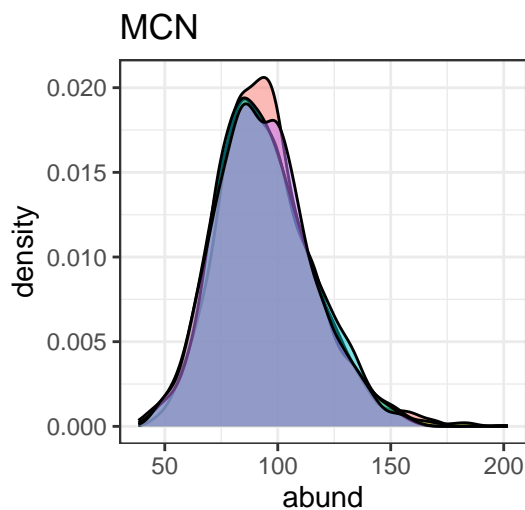
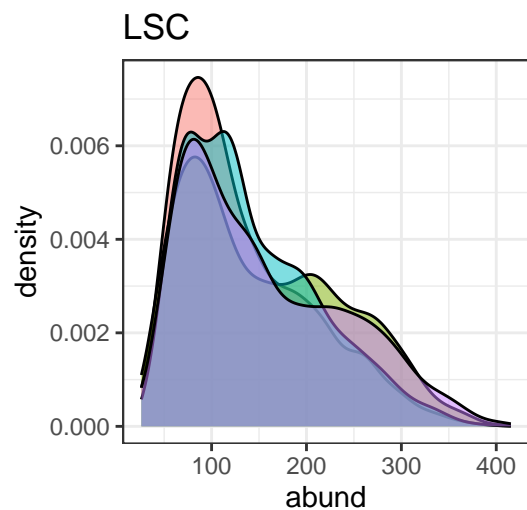
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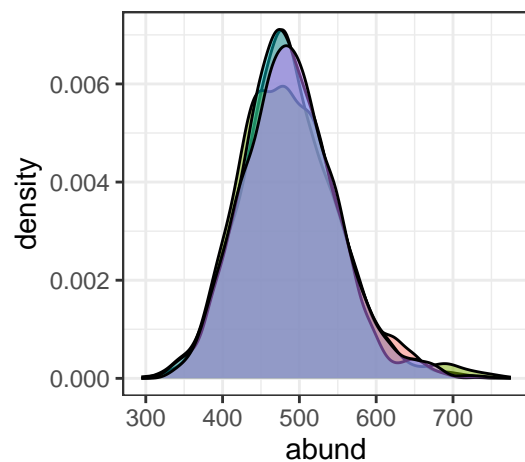




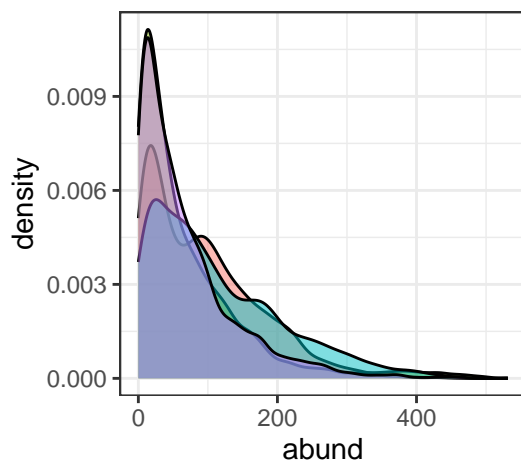




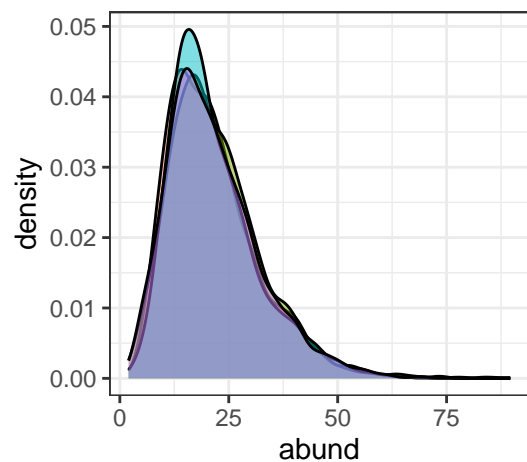
SC1



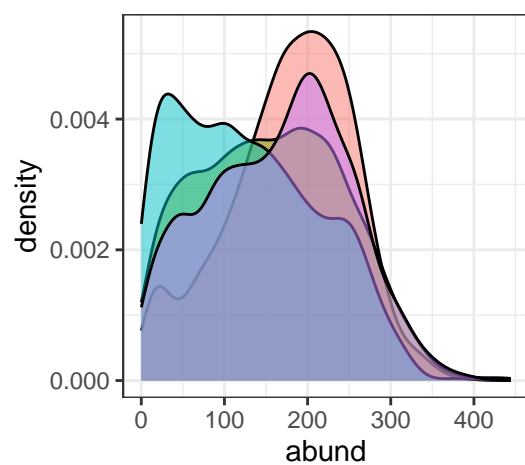
SC3\_bb



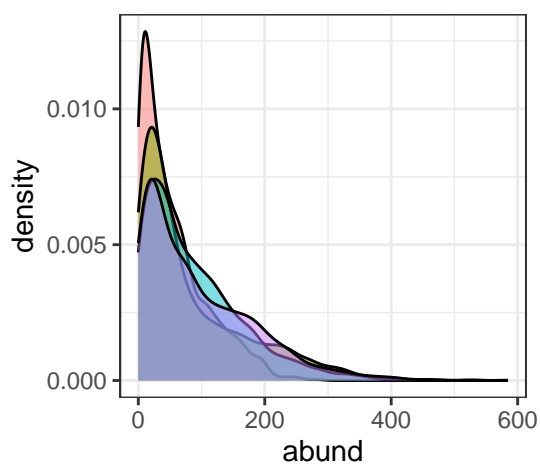
STL



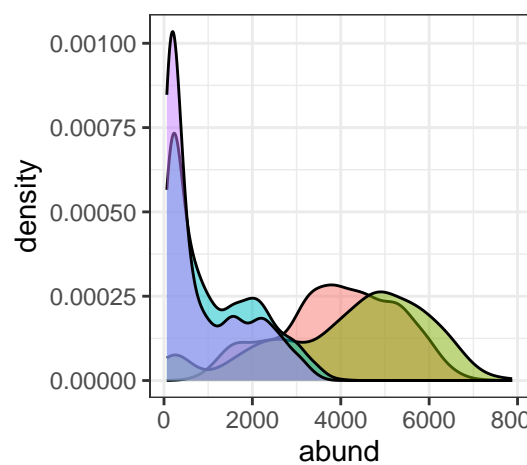
SC1\_bb



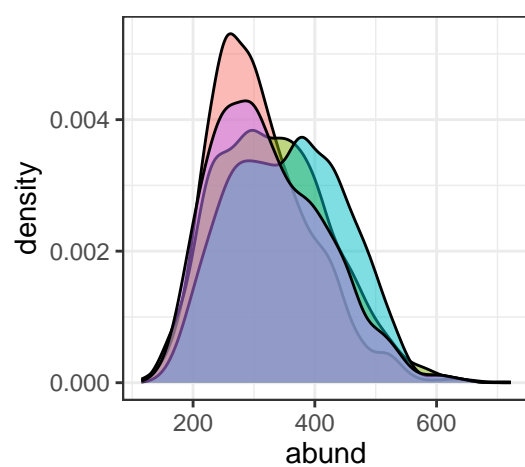
SC4



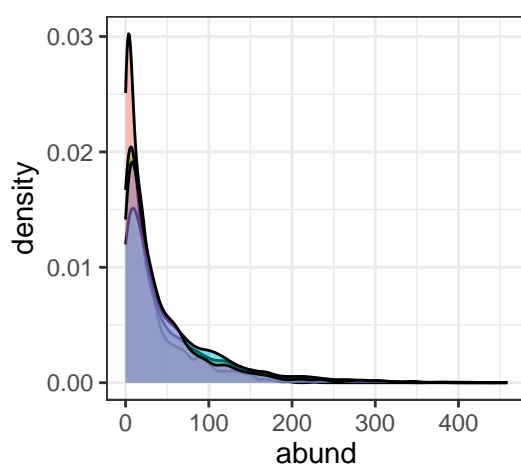
SW1



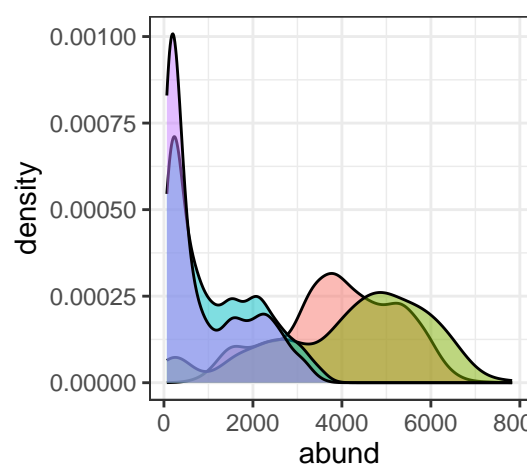
SC2



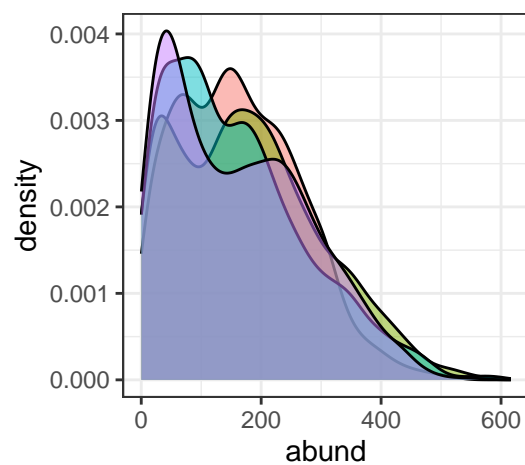
SC4\_bb



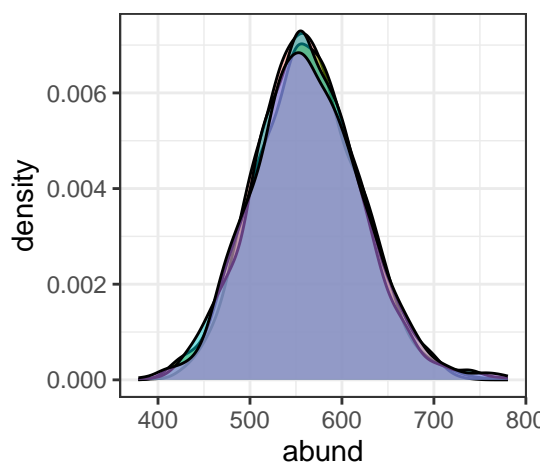
SW1\_bb



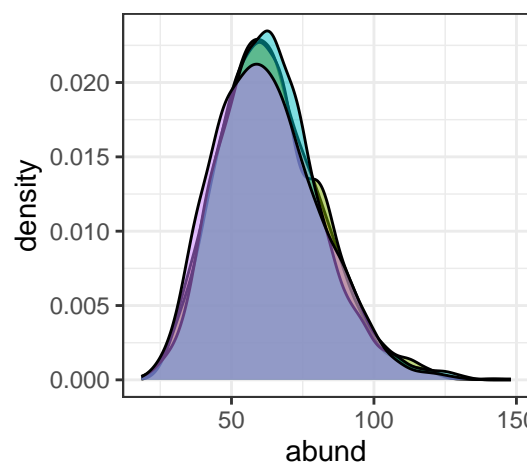
SC2\_bb



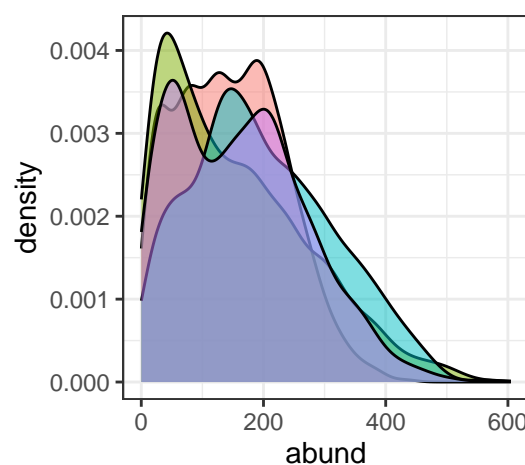
SFG



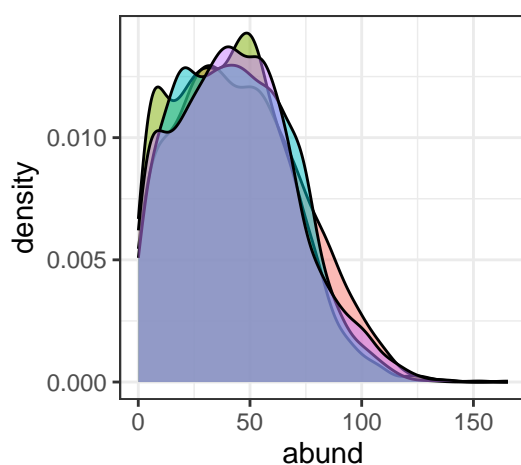
SWT



SC3



SFG\_bb



SWT\_bb

