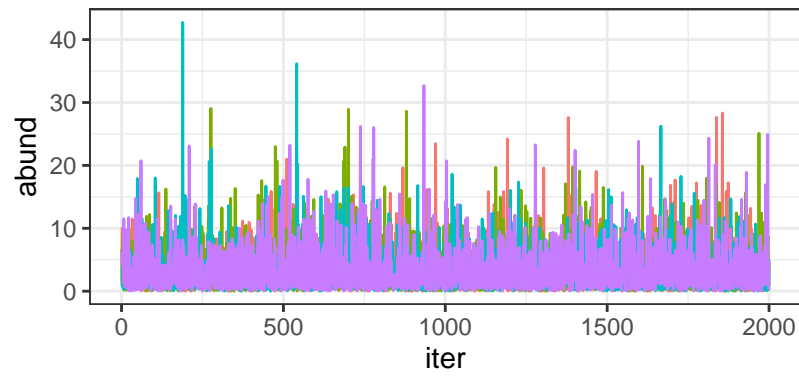
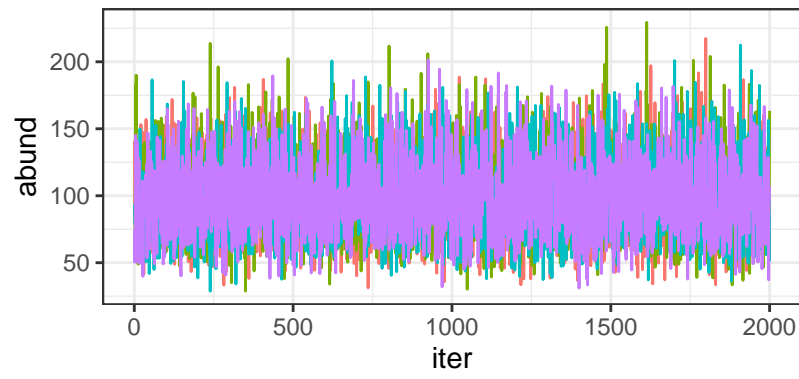


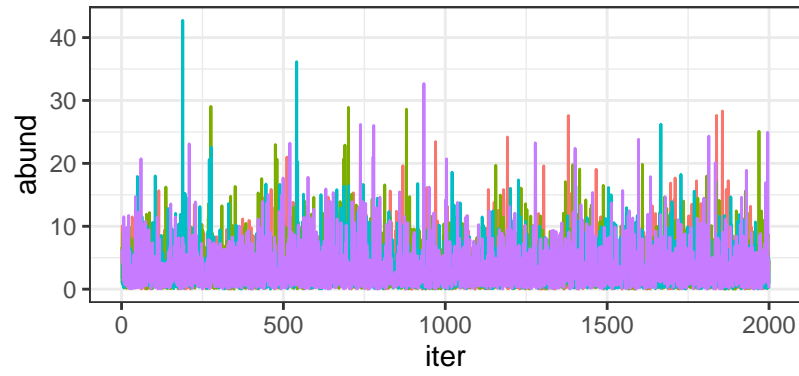
ACB



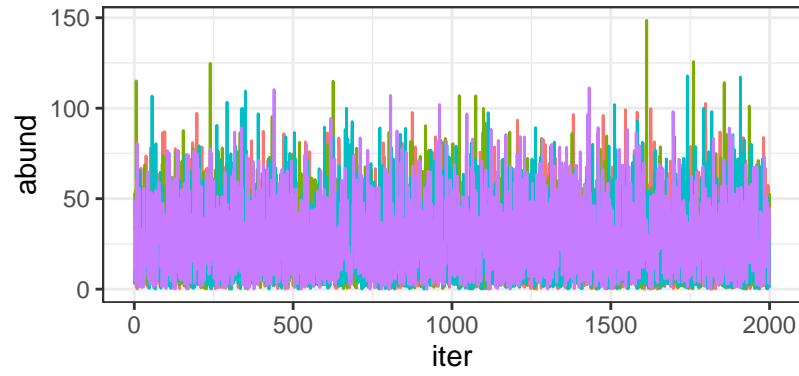
CCU



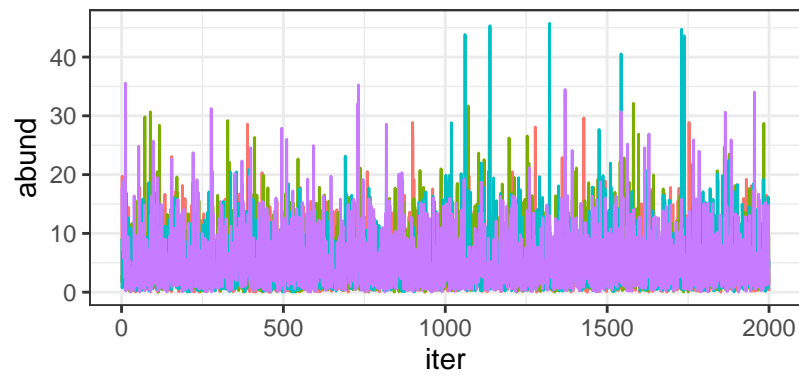
ACB_bb



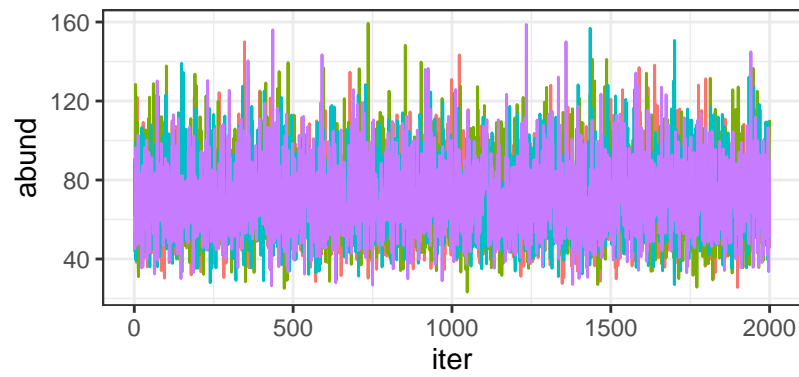
CCU_bb



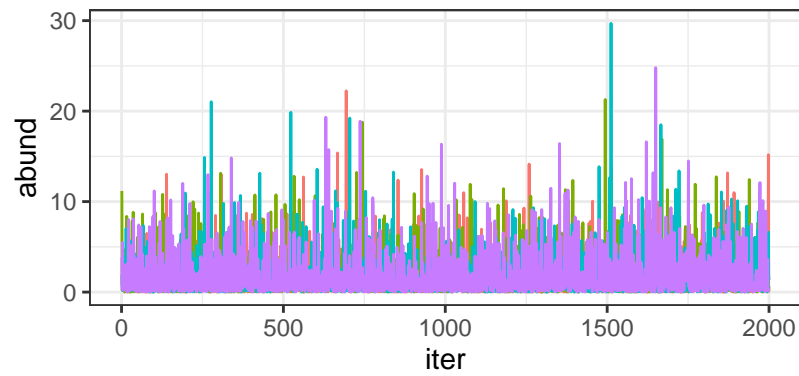
ACM



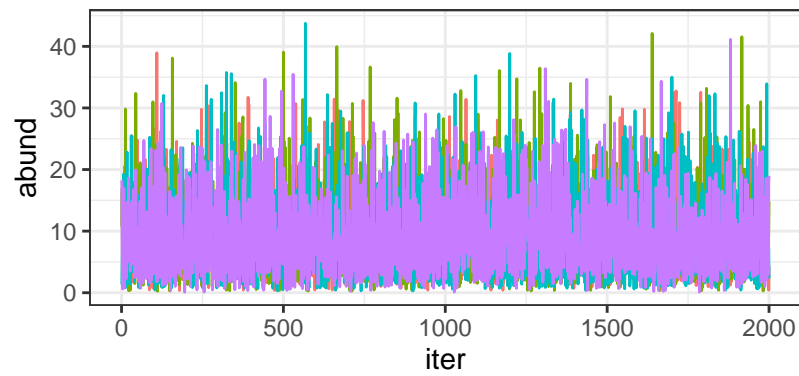
CCW



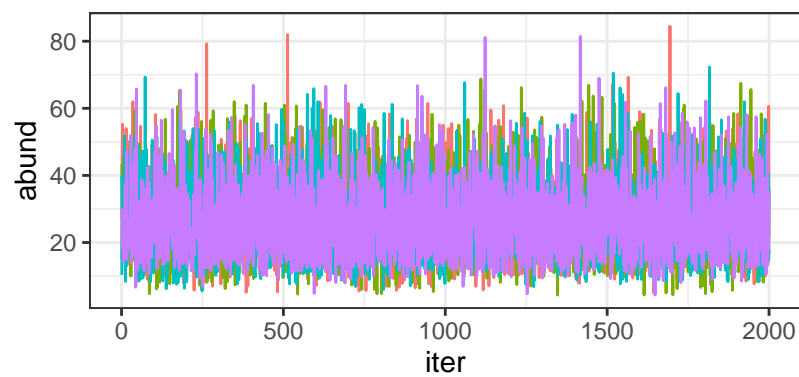
ACM_bb



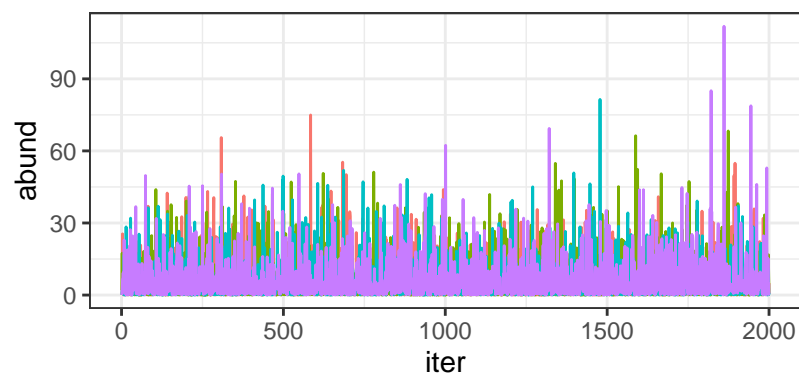
CLC



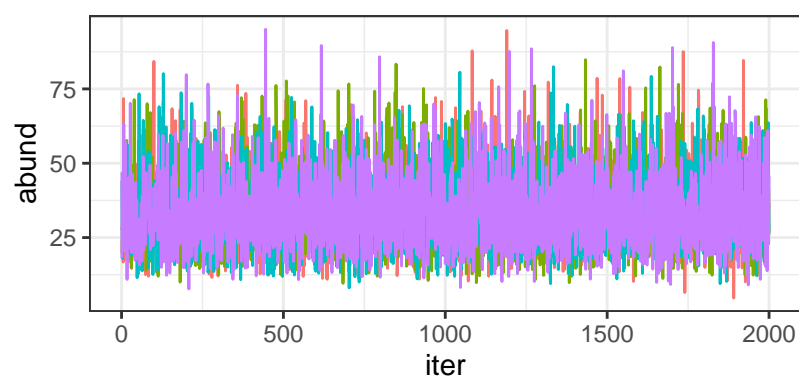
BRC



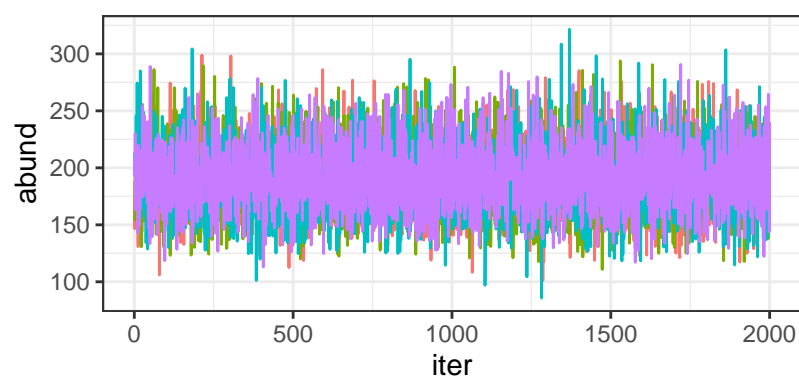
CRA



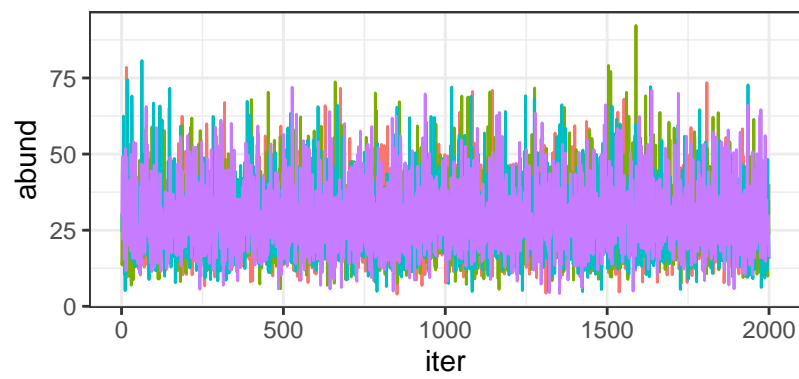
BSC



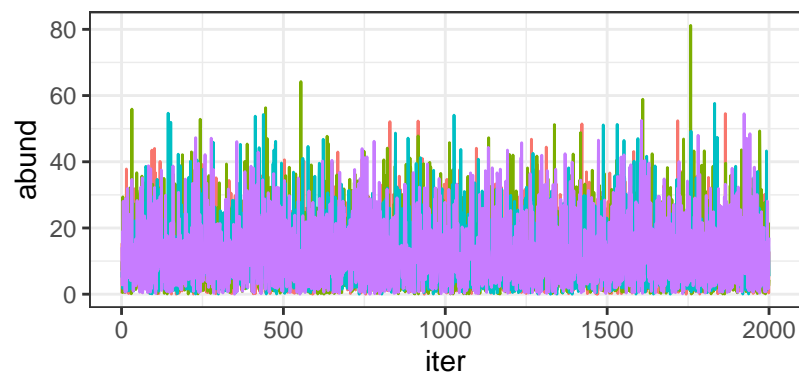
ESS



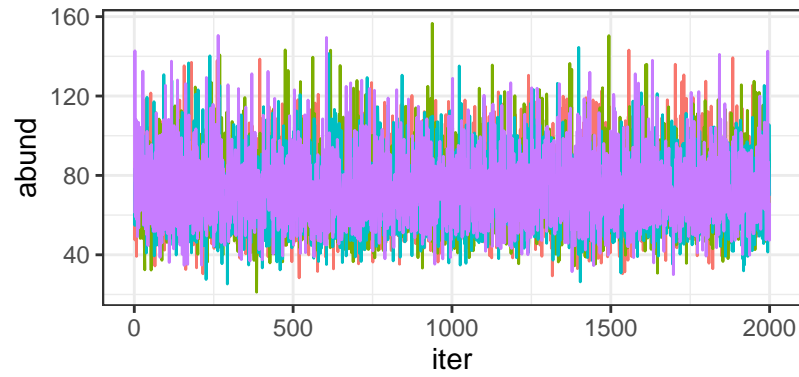
ESS_bb



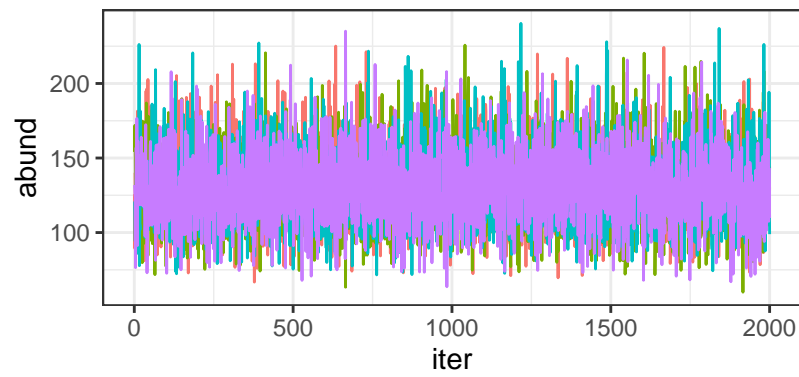
GOA_bb



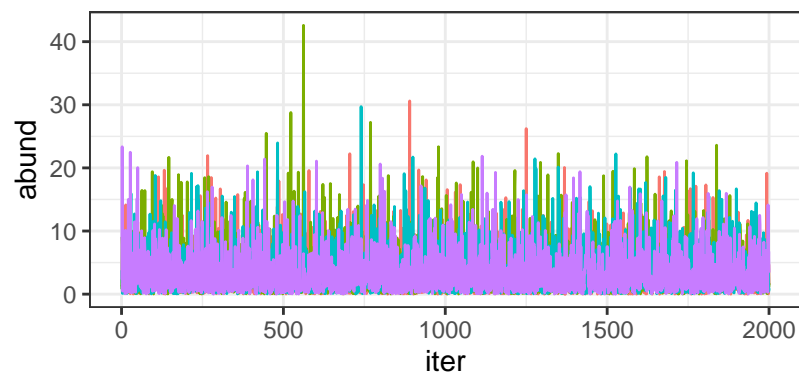
EVL



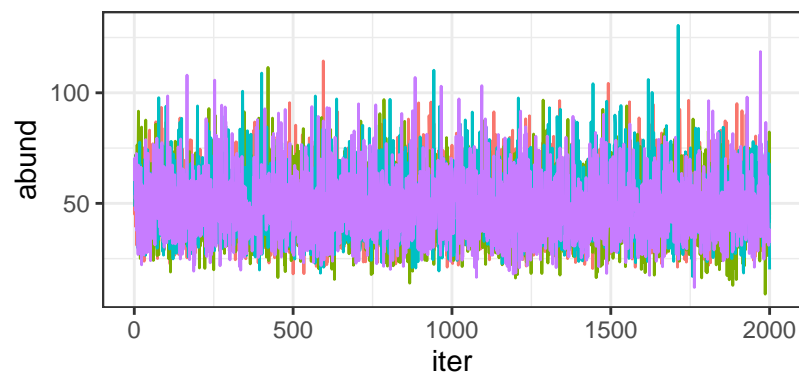
GRS



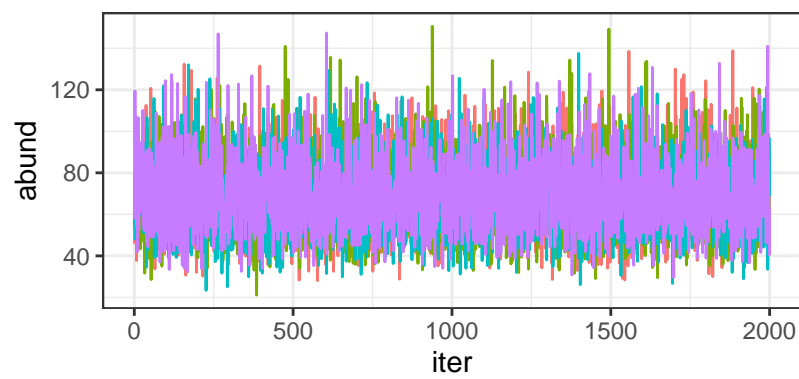
EVL_bb



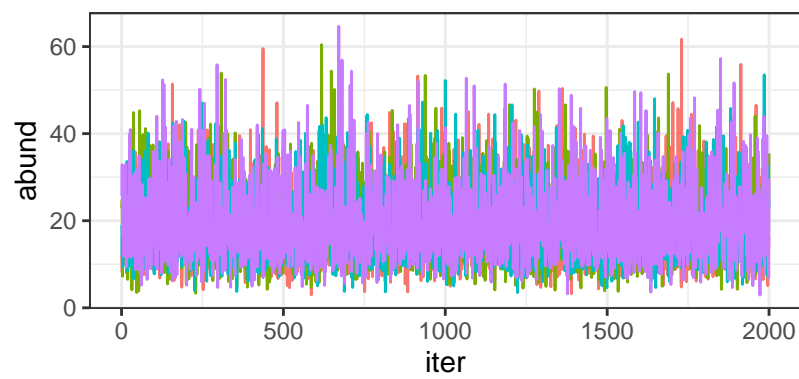
GRS_bb



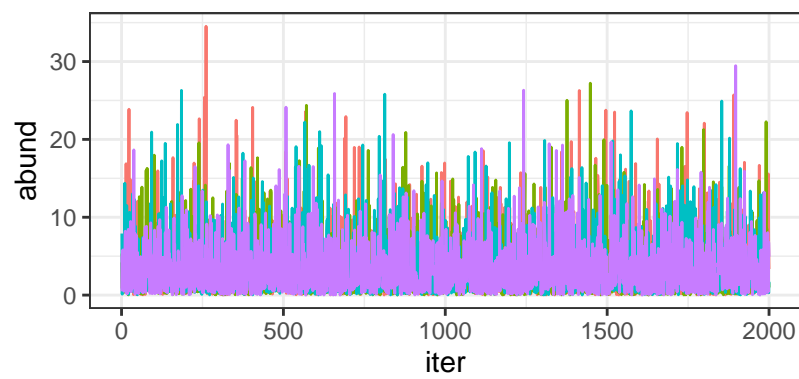
EVU



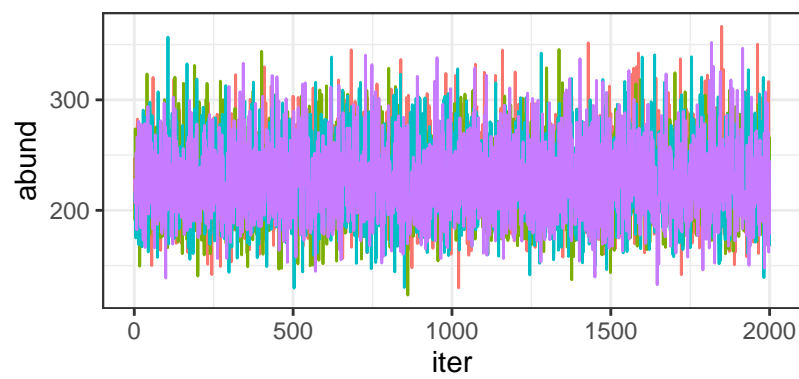
HYC



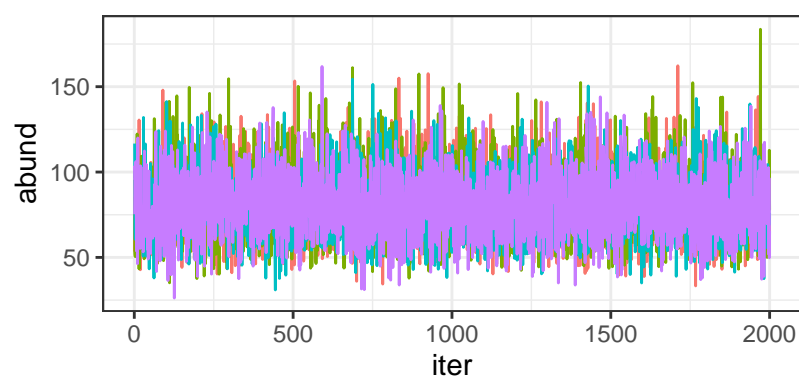
EVU_bb



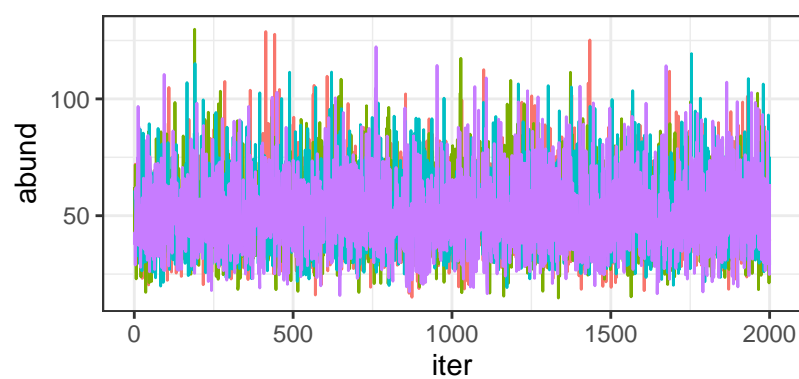
IML



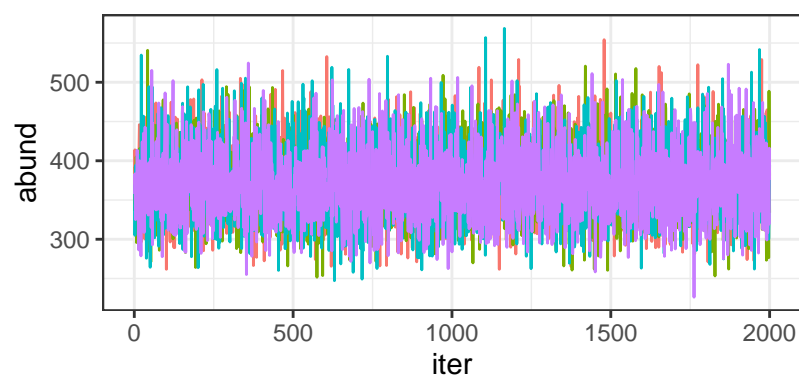
GOA



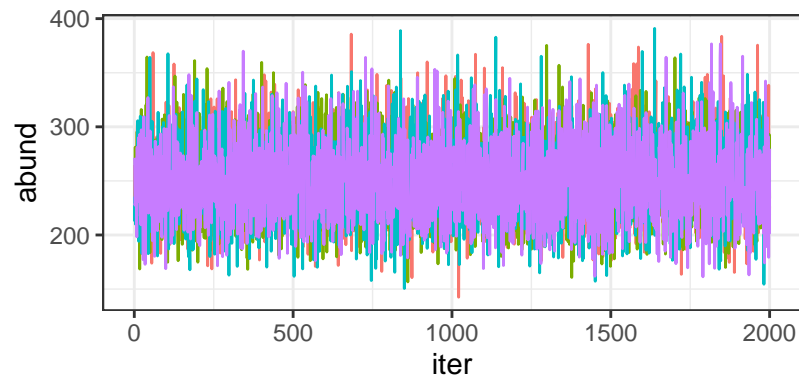
IML_bb



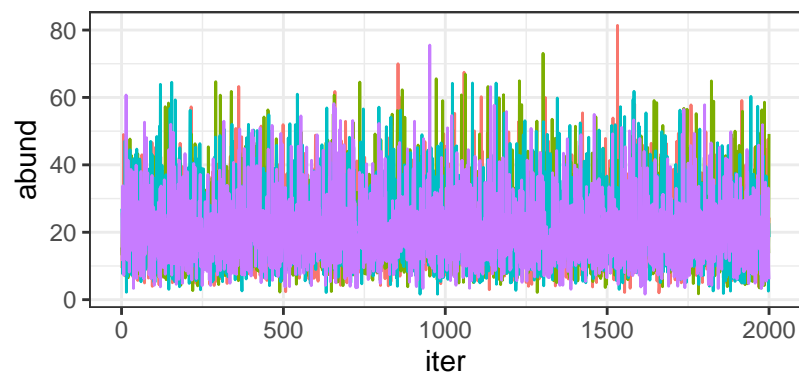
IR1



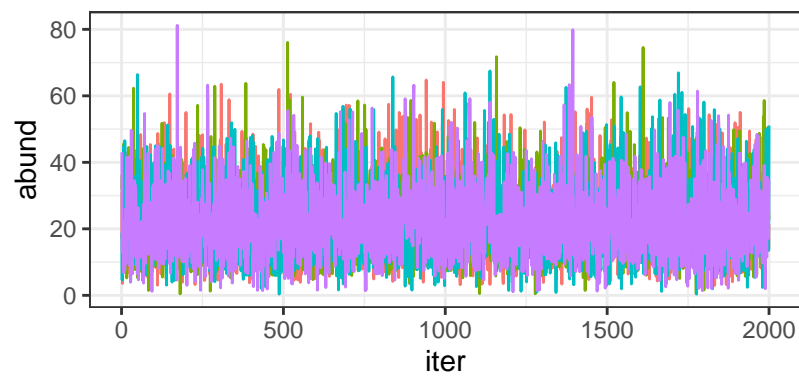
IR4



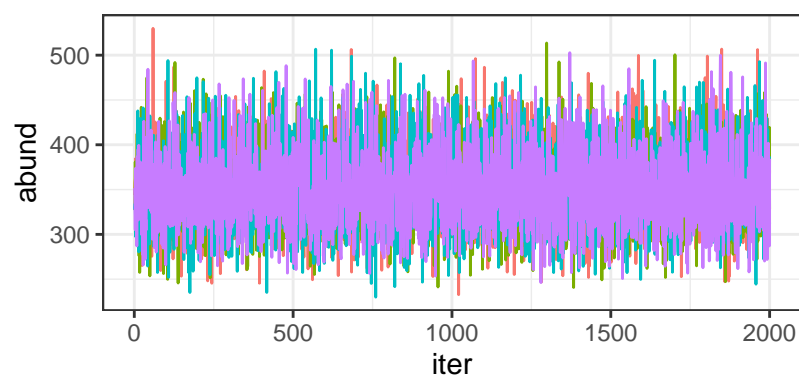
IR1_bb



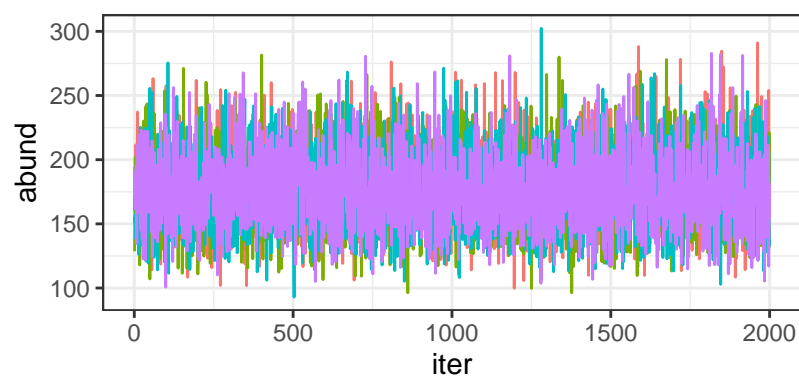
IR4_bb



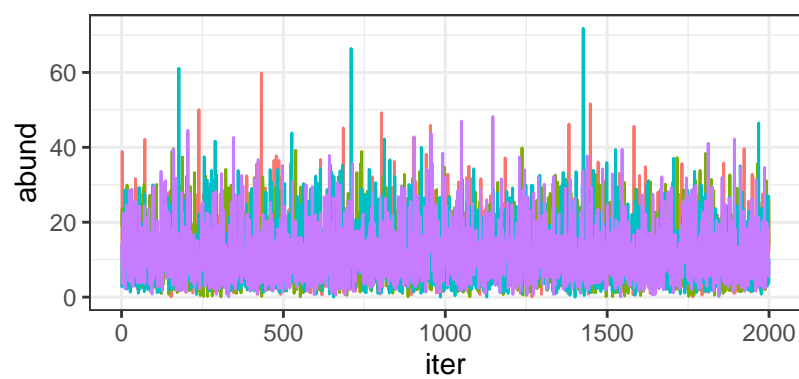
IR2



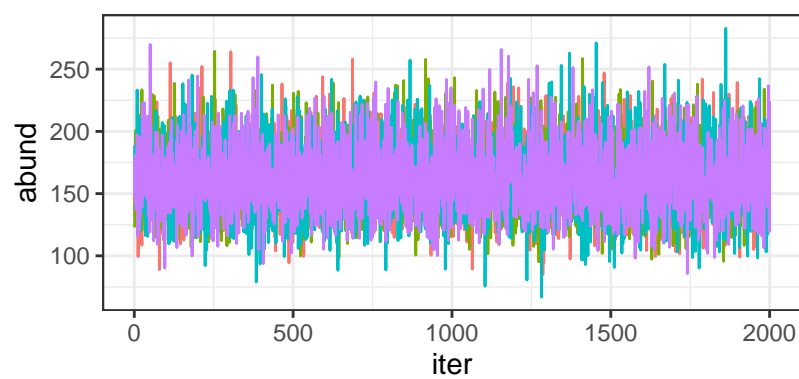
IR5



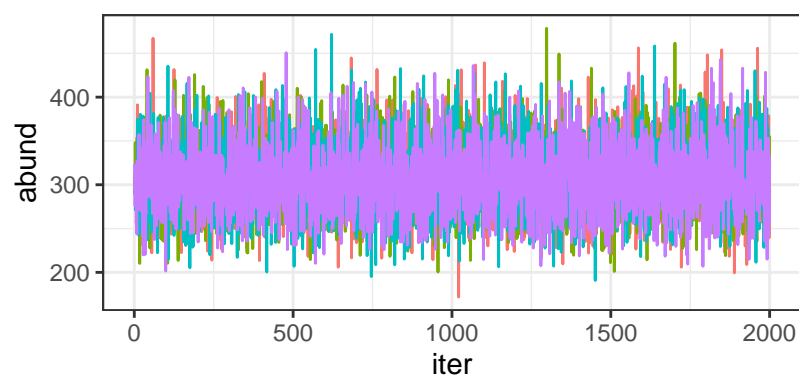
IR2_bb



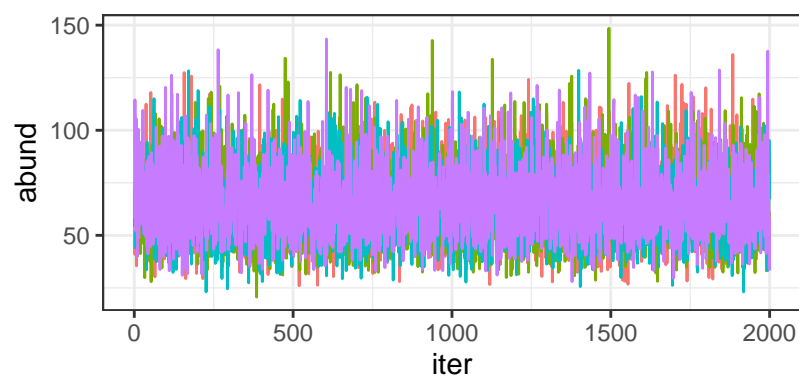
JOHNSC



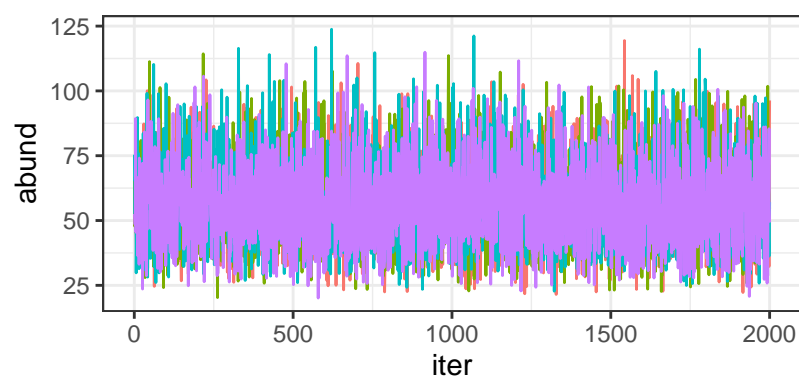
IR3



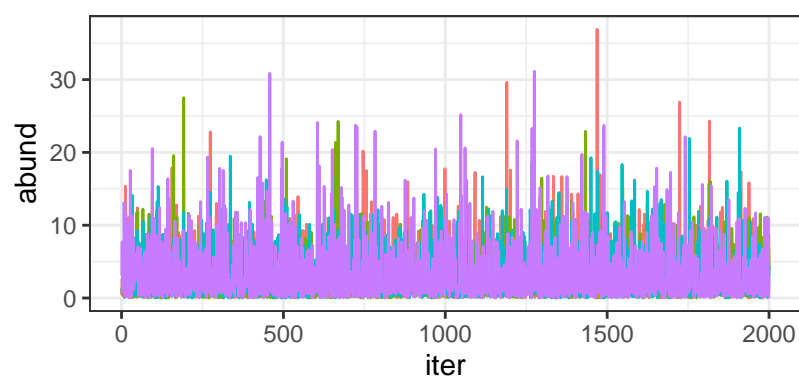
KEN



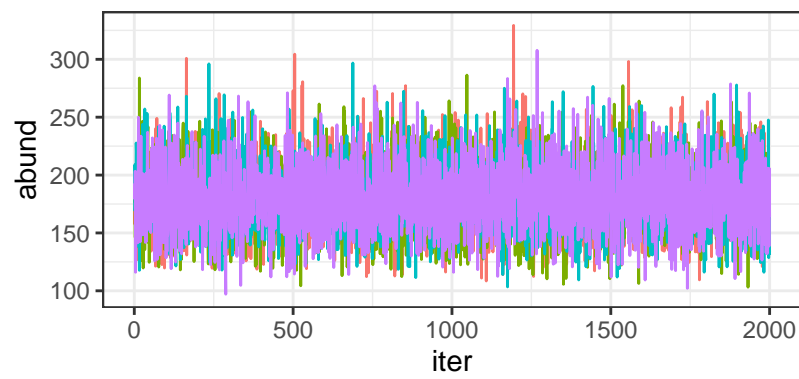
IR3_bb



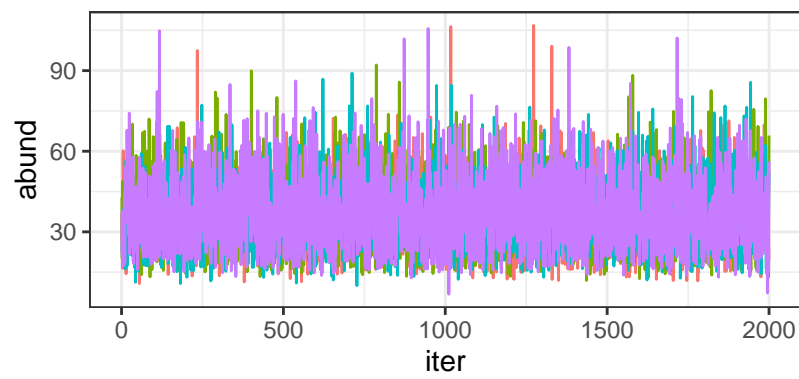
KEN_bb



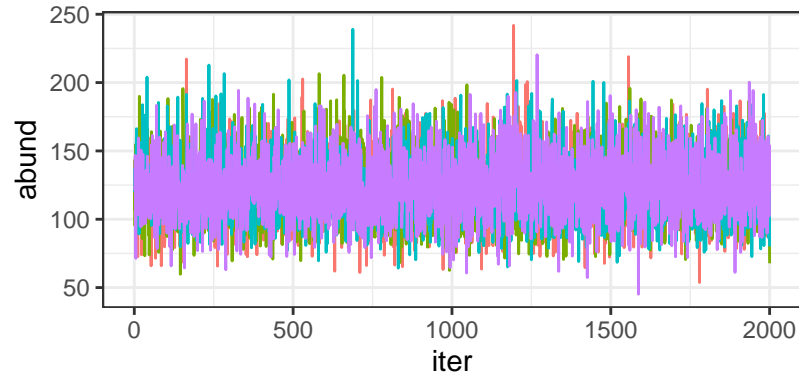
KRS



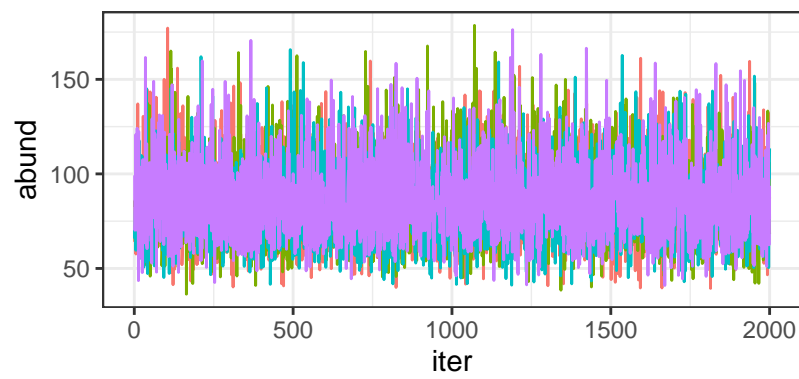
LGW



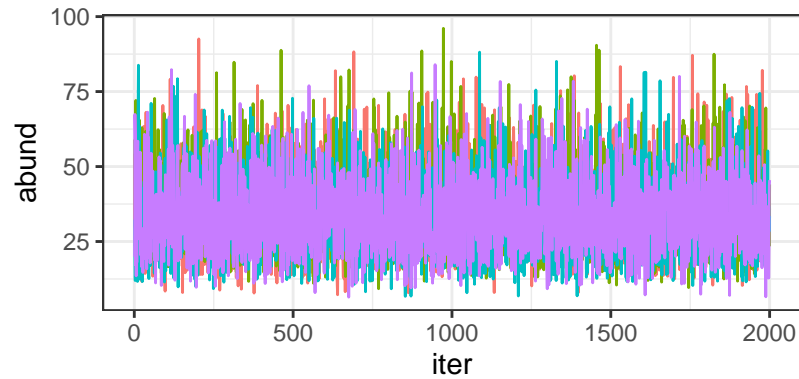
KRS_bb



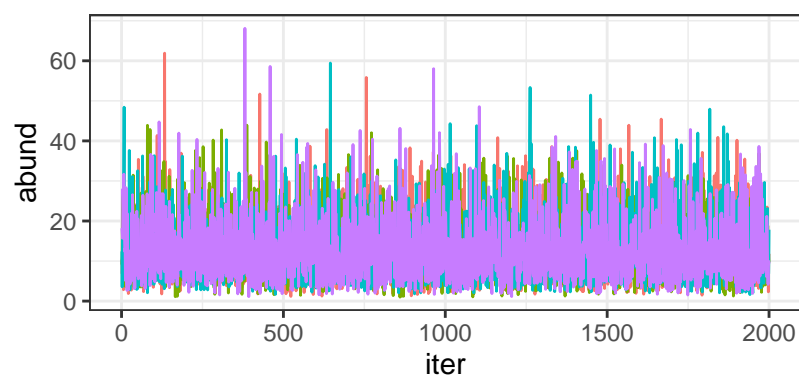
LLR



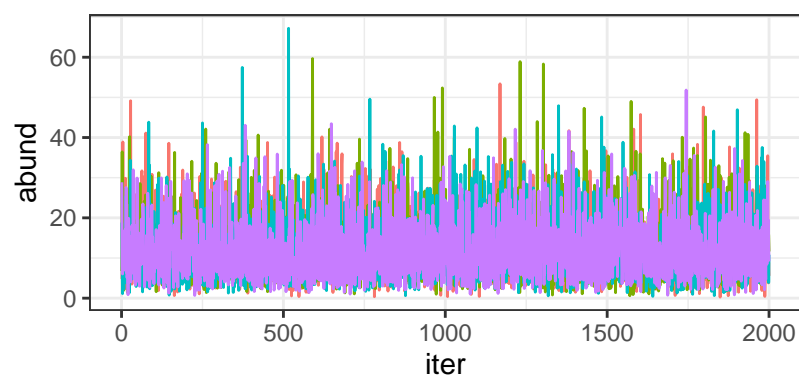
LC1



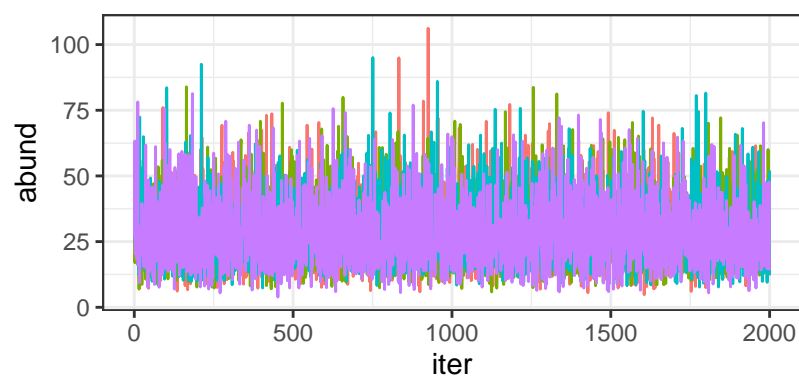
LLR_bb



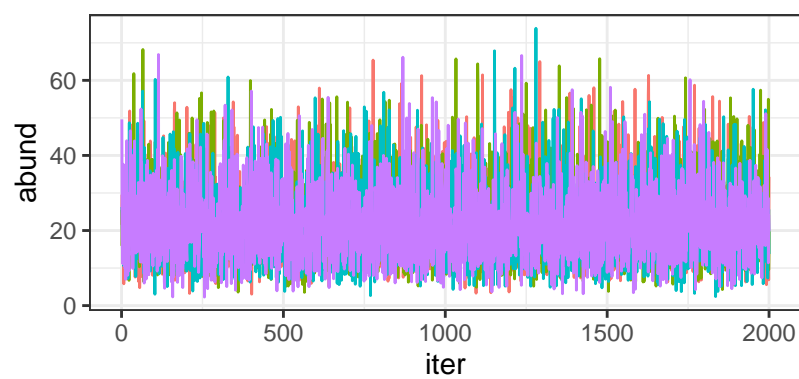
LC1_bb



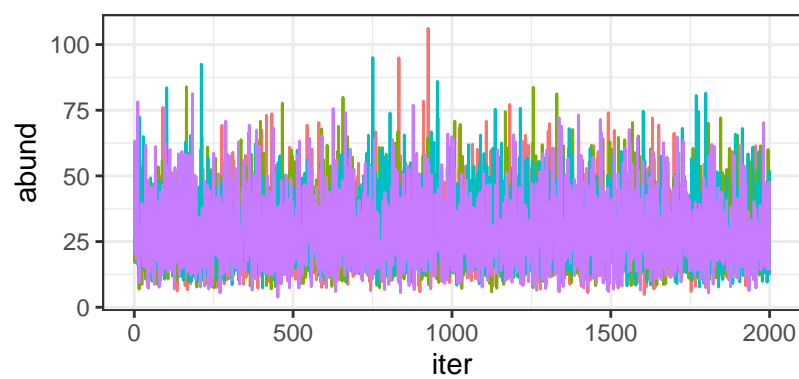
LMA



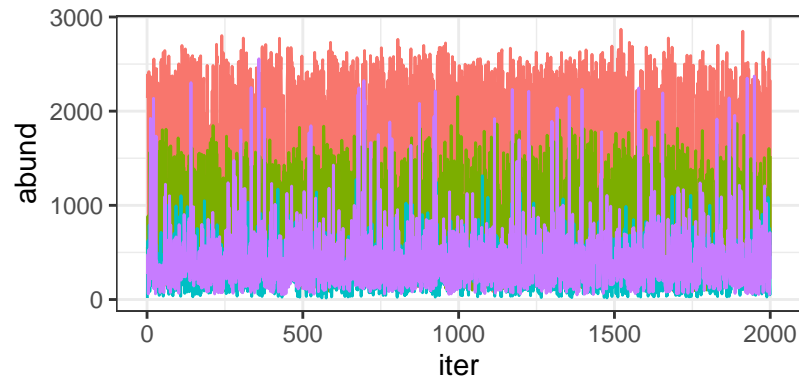
LC2



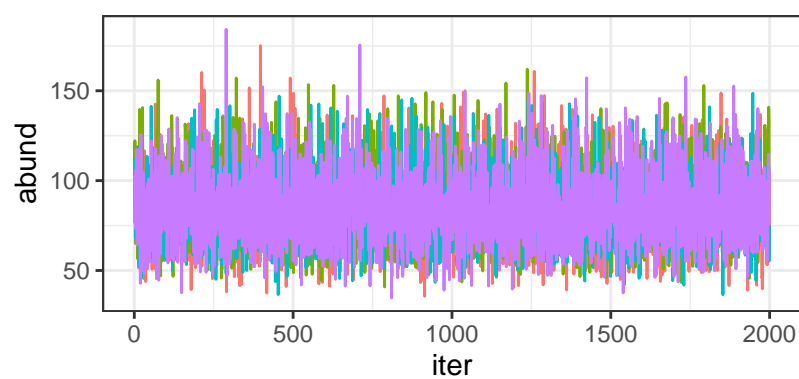
LMA_bb

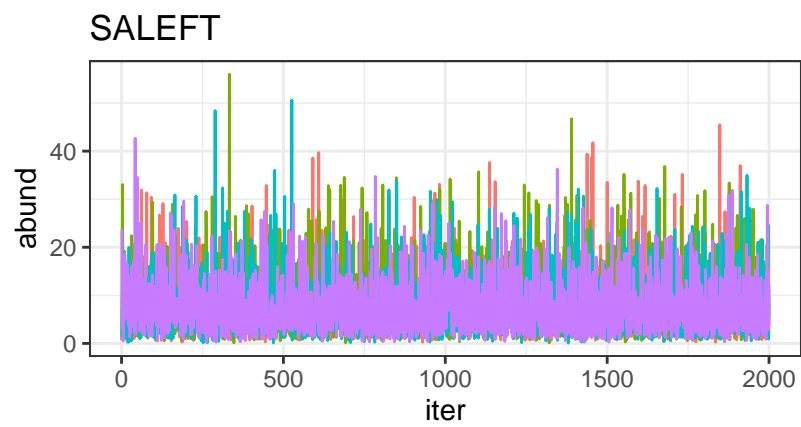
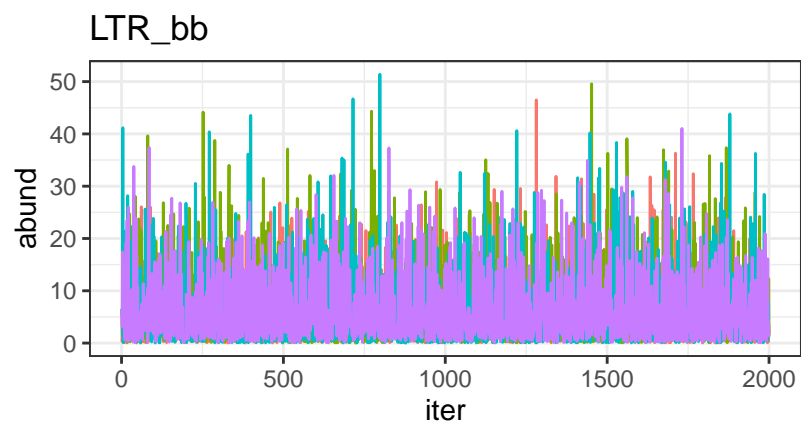
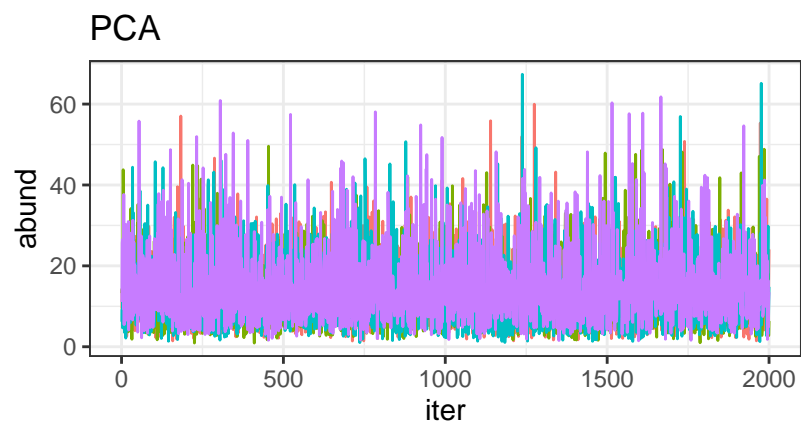
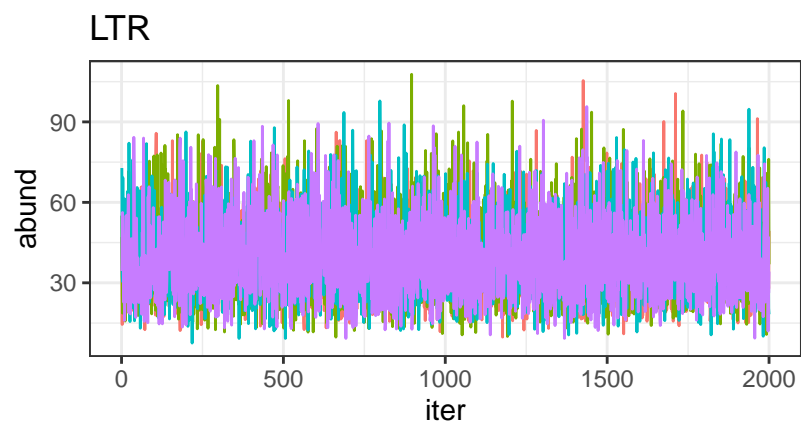
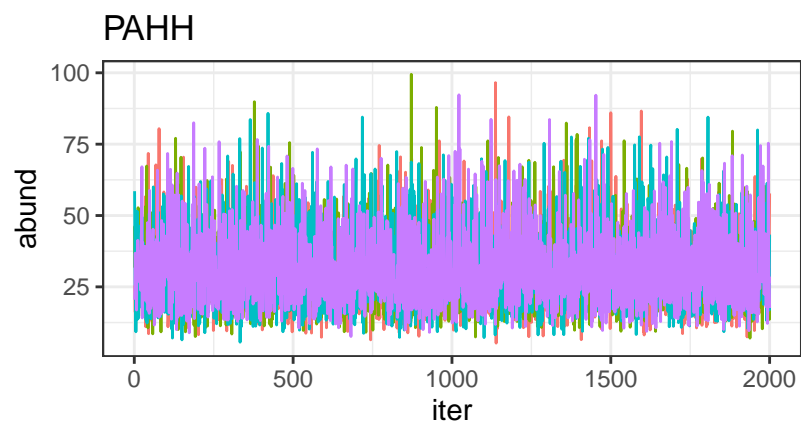
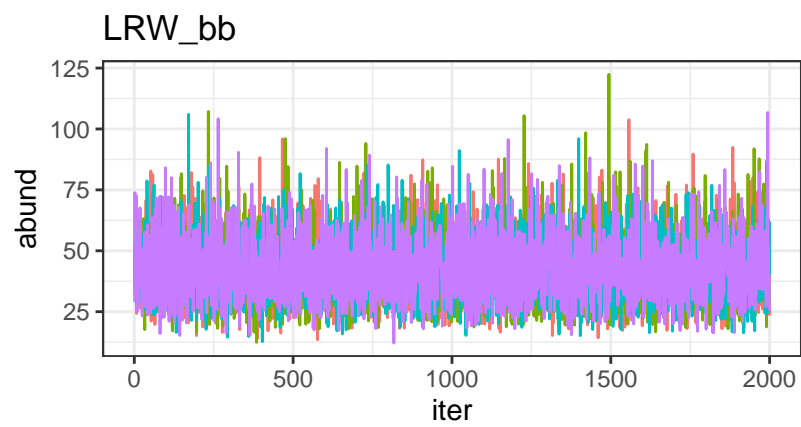
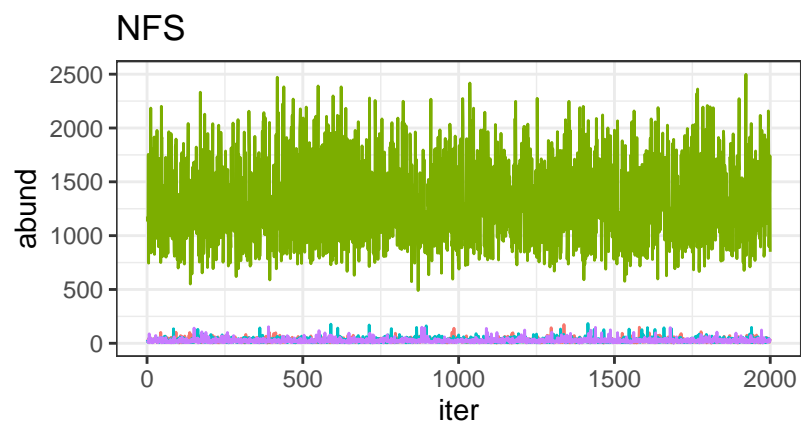
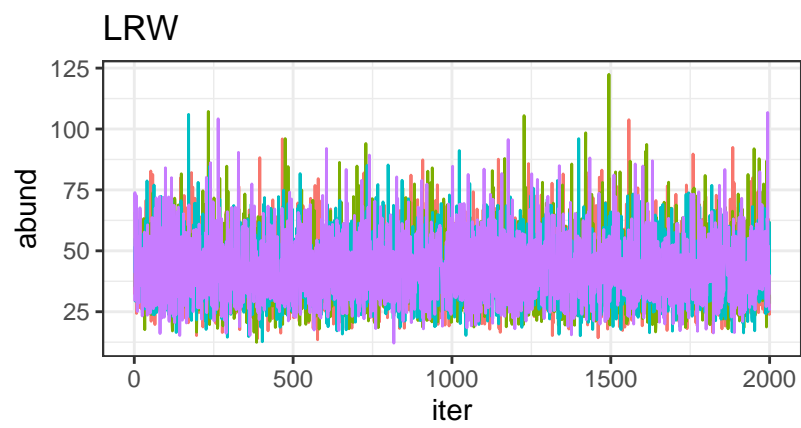
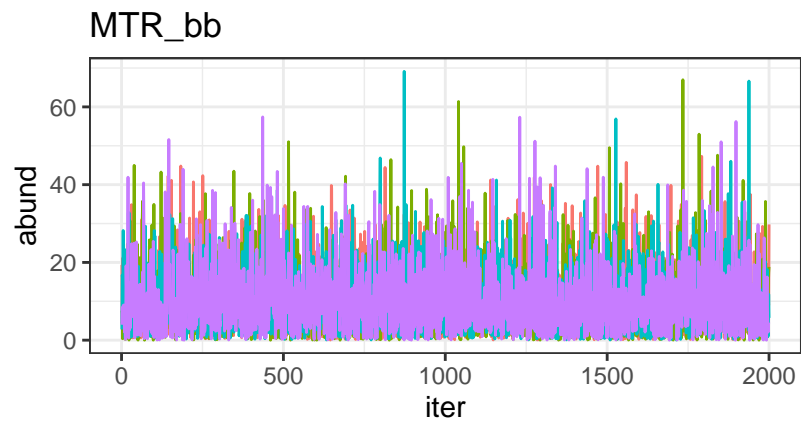
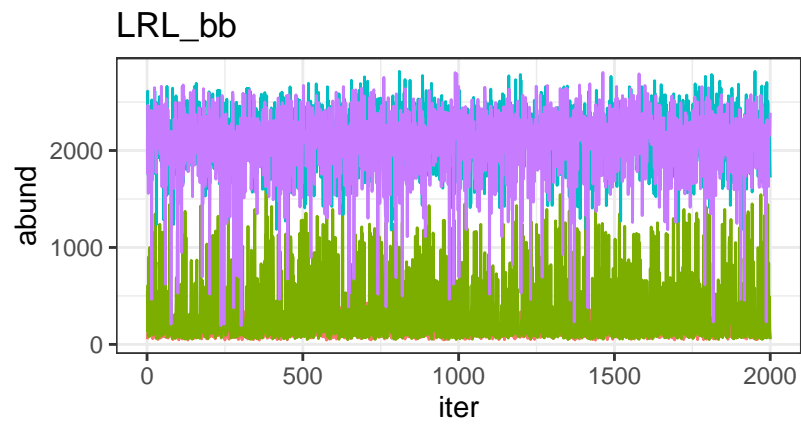
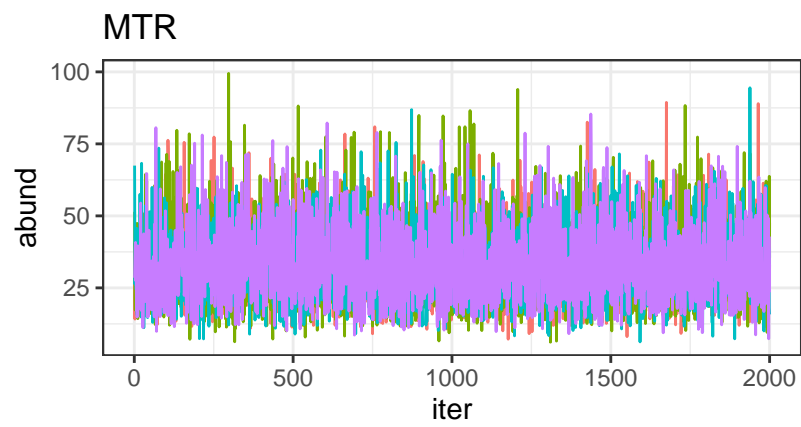
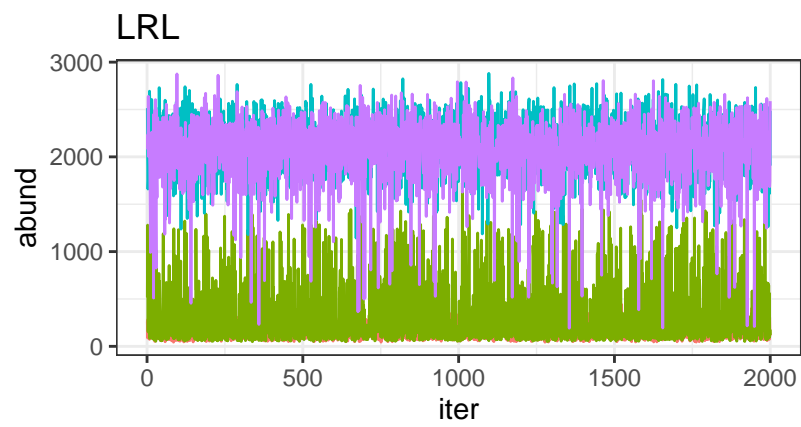


LGR_bb

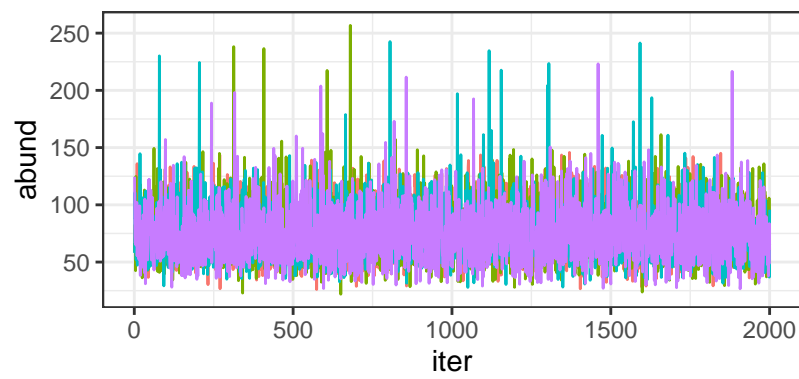


LOSTIW

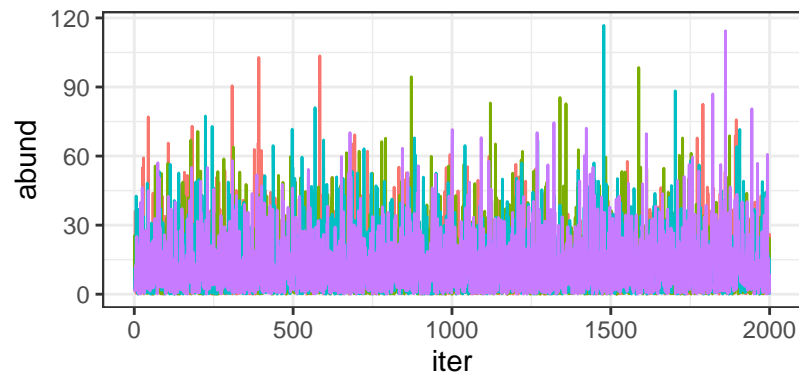




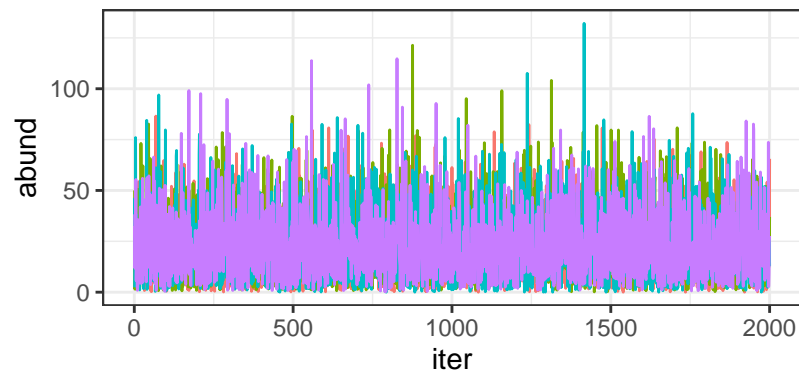
SC1



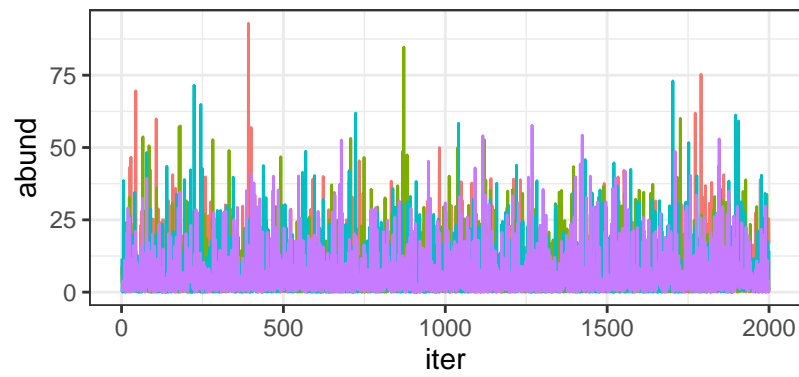
SC4



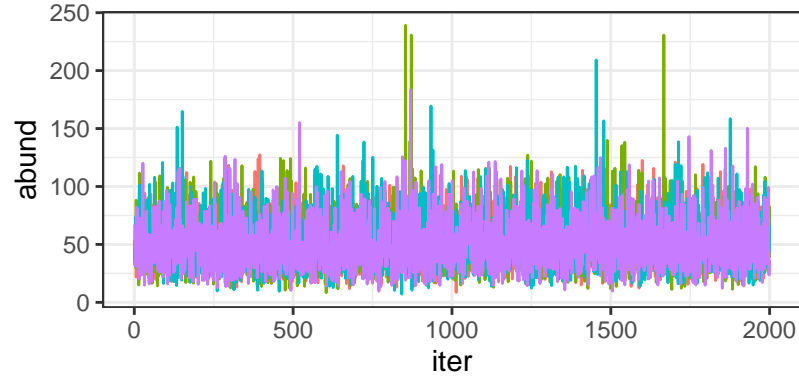
SC1_bb



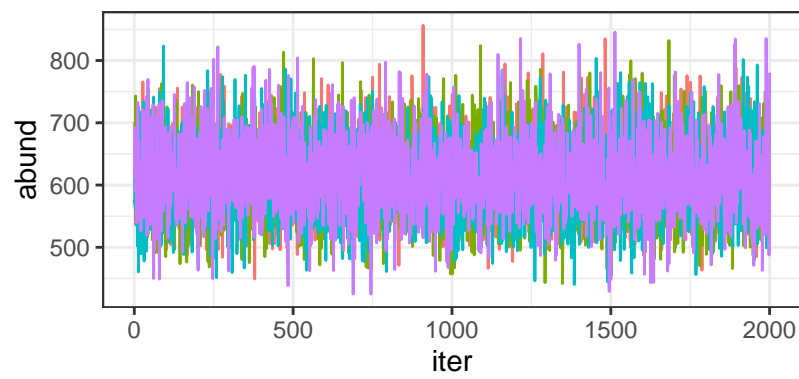
SC4_bb



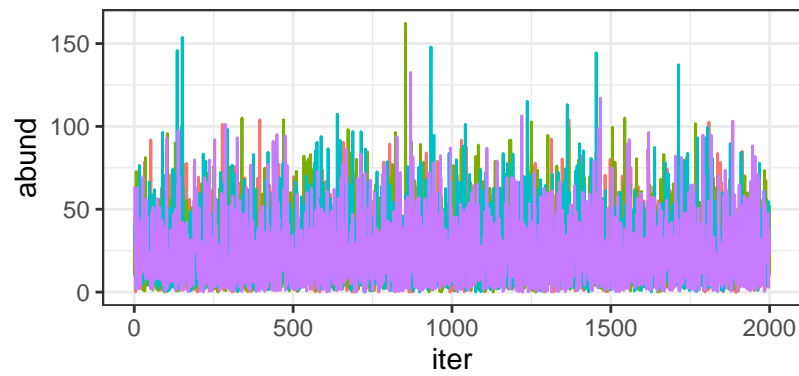
SC2



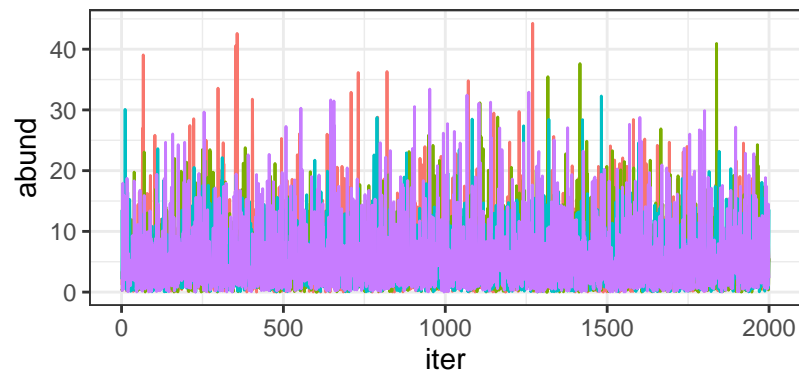
SFG



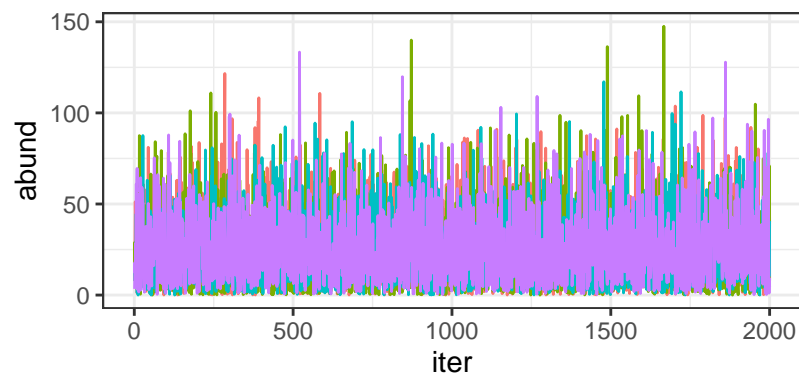
SC2_bb



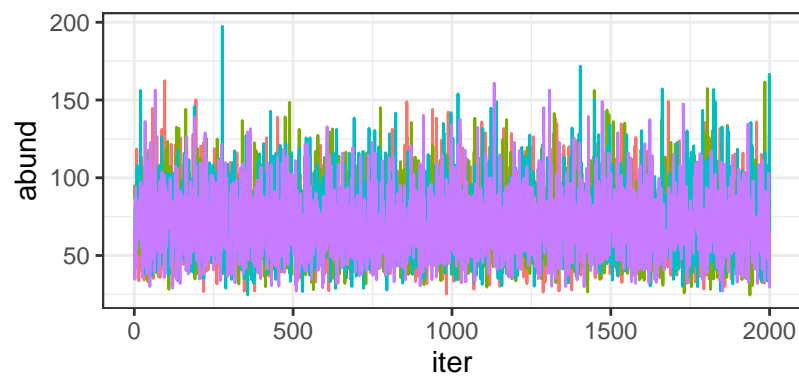
SFG_bb



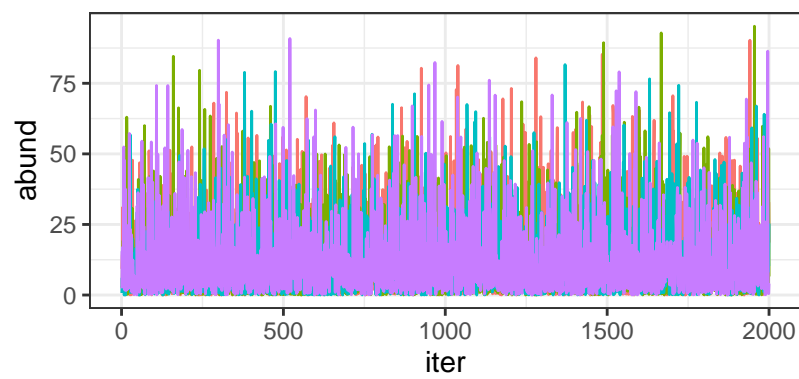
SC3



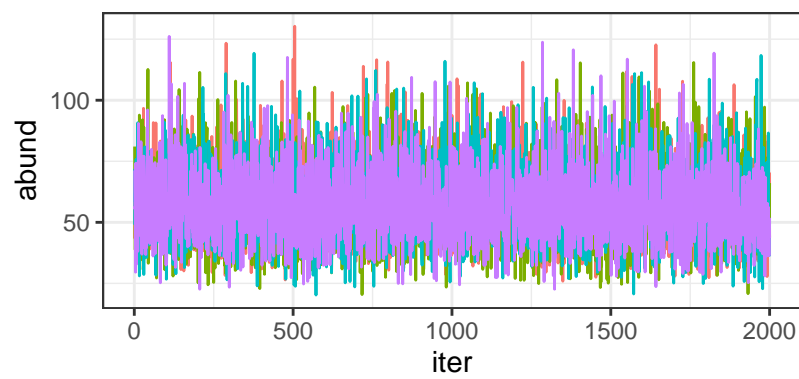
STL



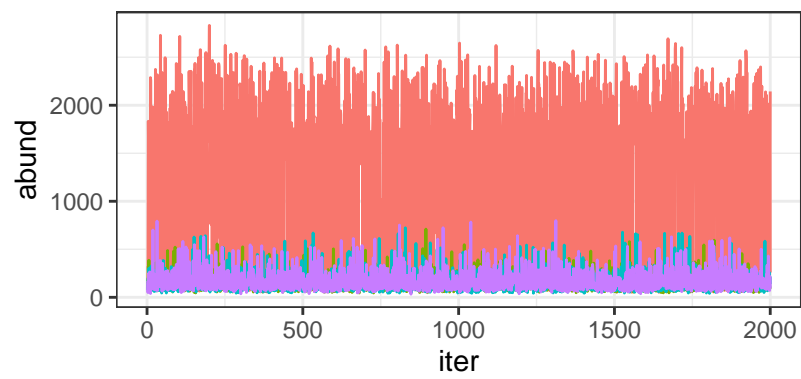
SC3_bb



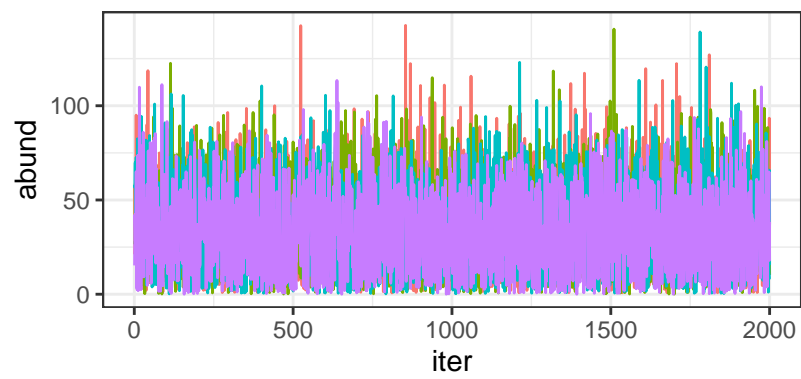
STR



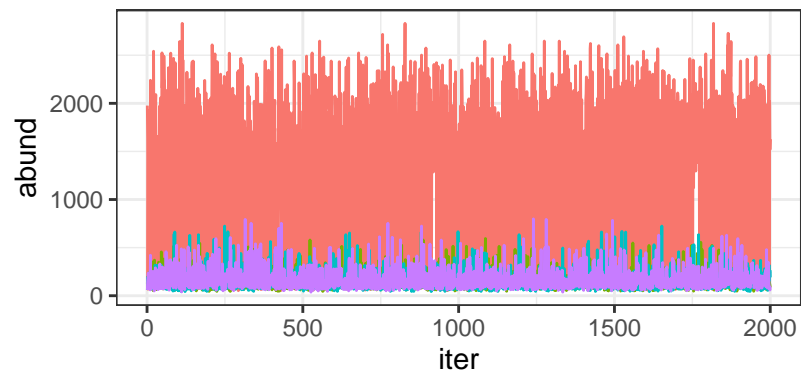
SW1



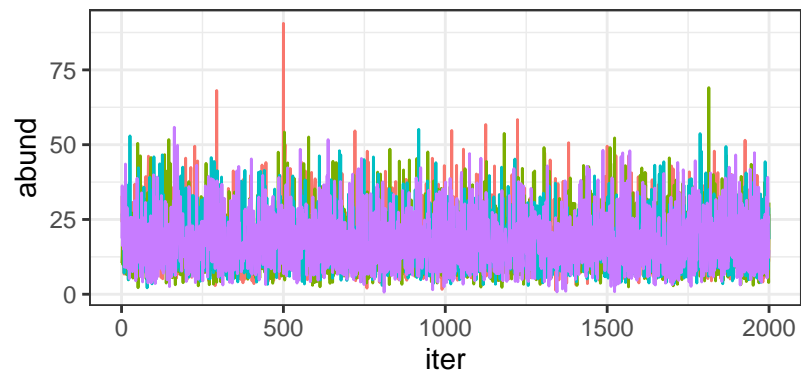
UGR_bb



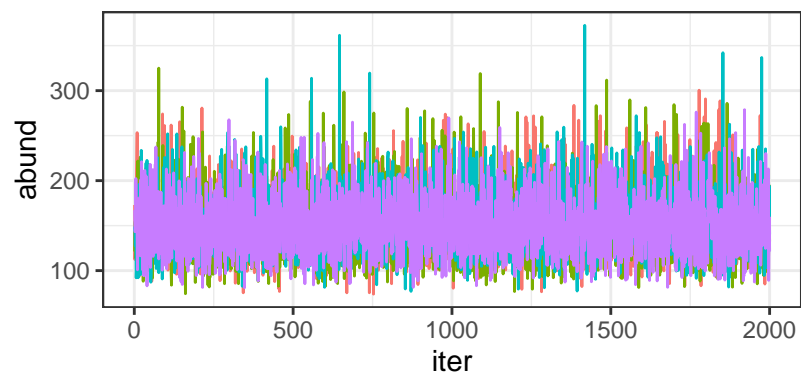
SW1_bb



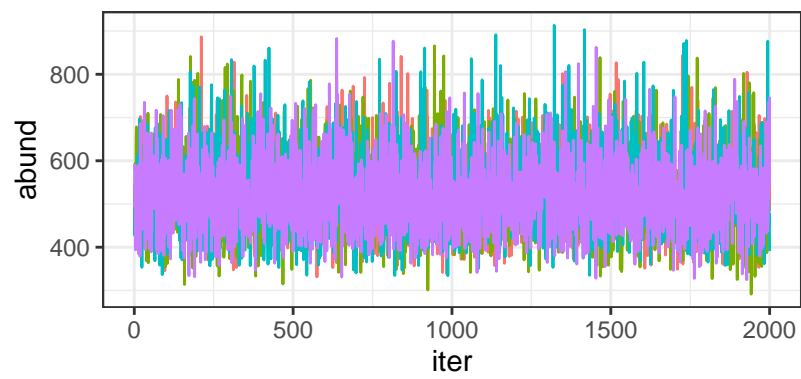
UGS



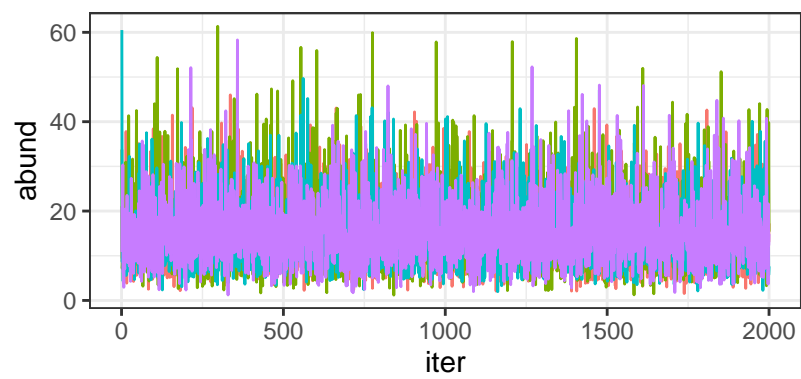
TAY



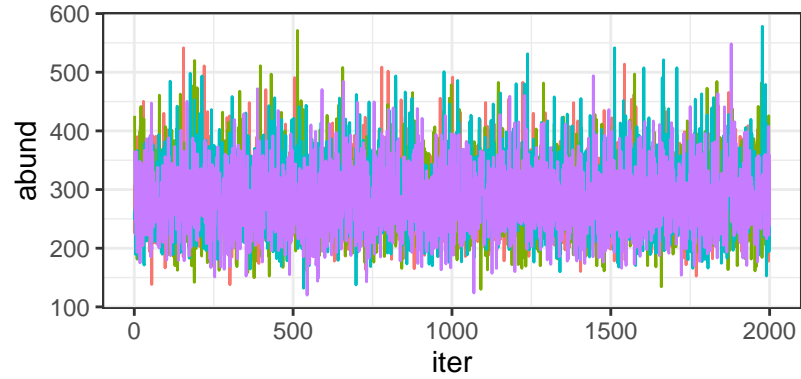
USE



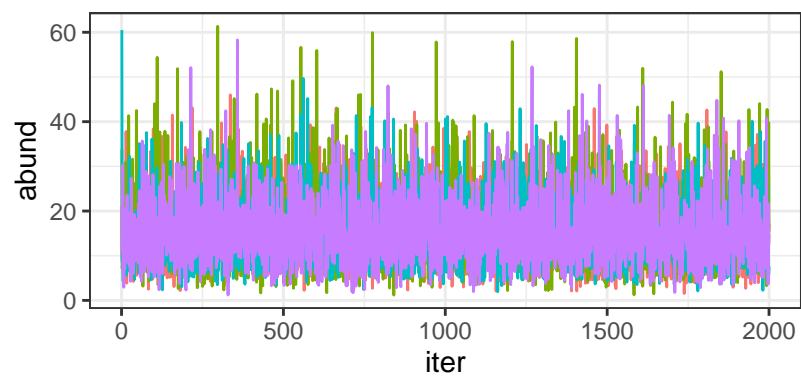
TFH



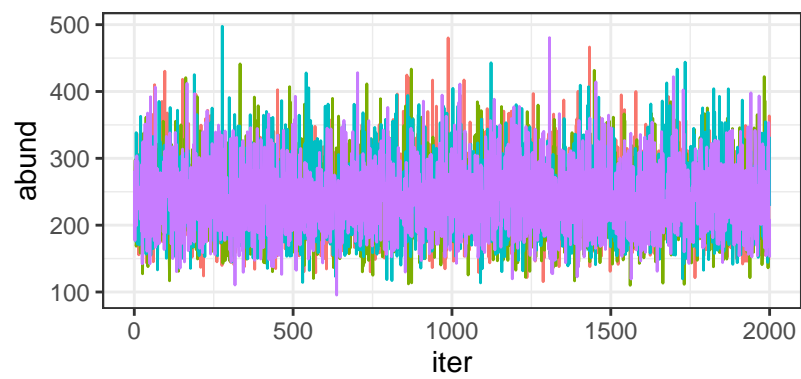
USE_bb



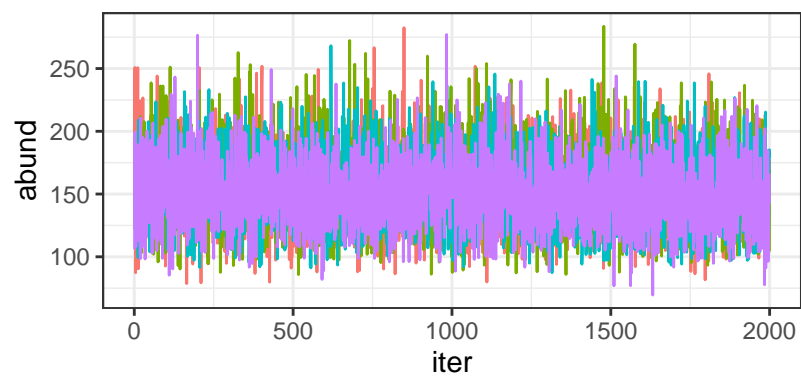
TFH_bb



USI



UGR



USI_bb

