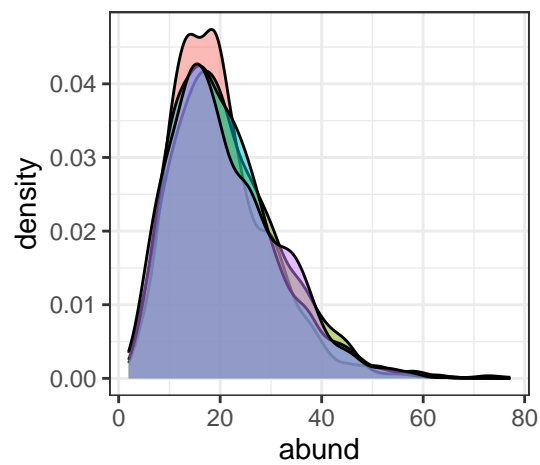
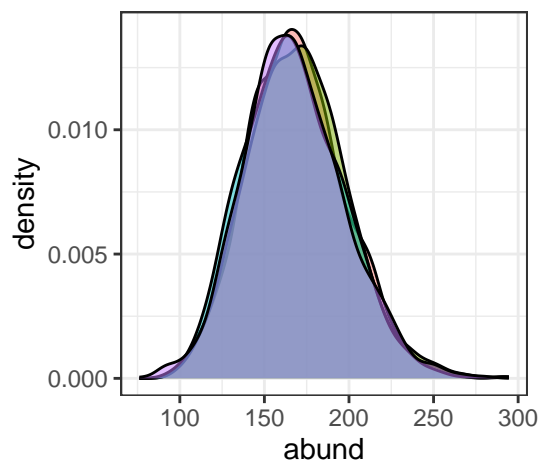


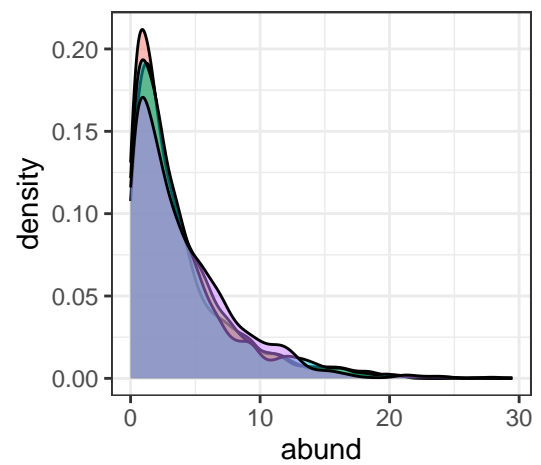
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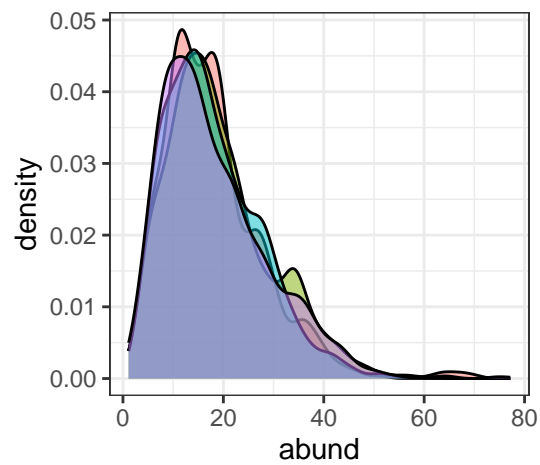
ESS



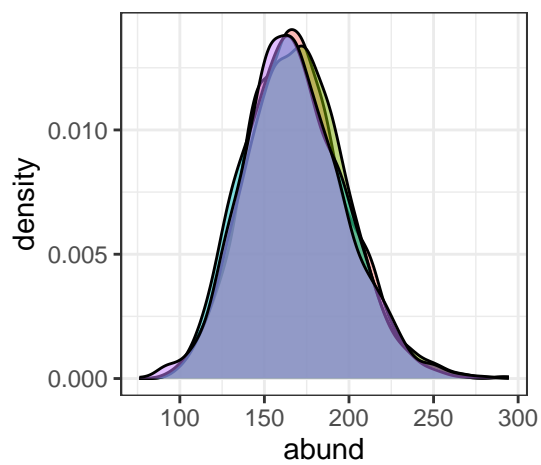
EVU_bb



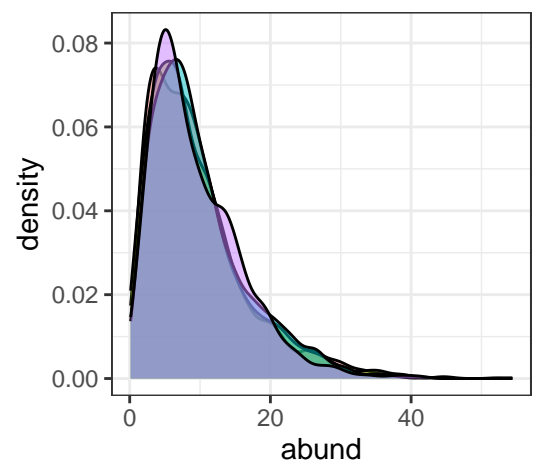
COC



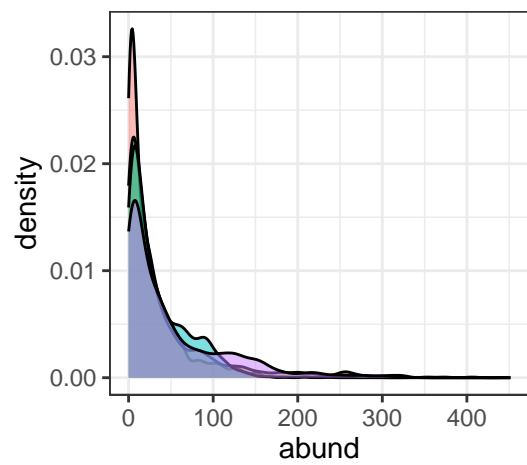
ESS_bb



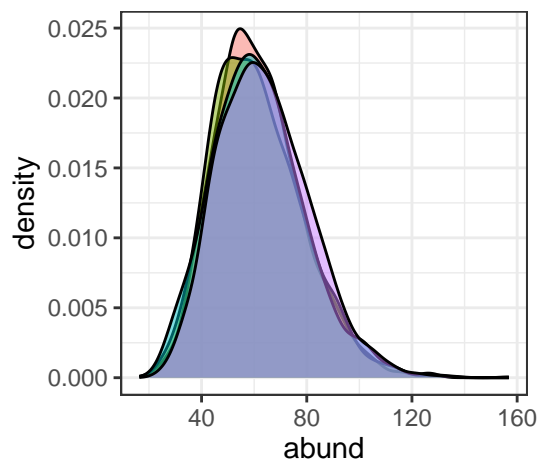
GEORGC



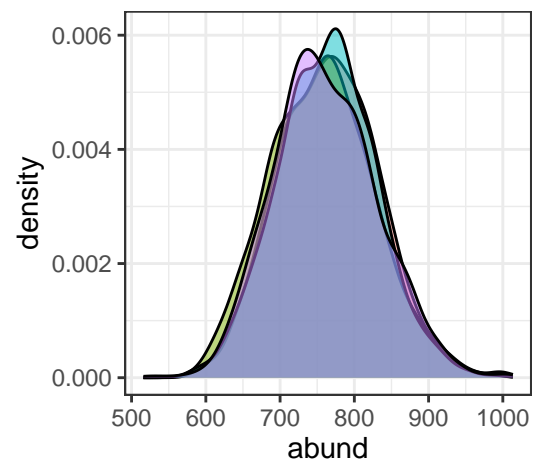
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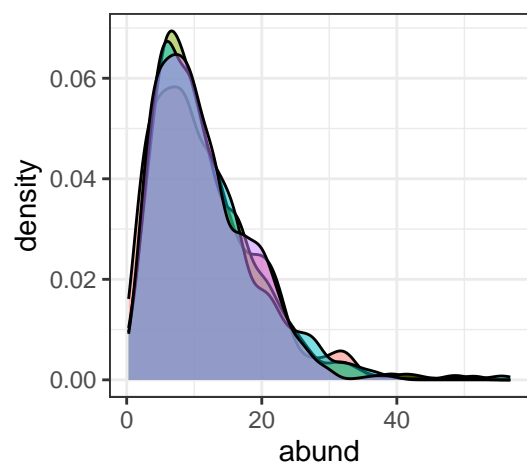
EVL



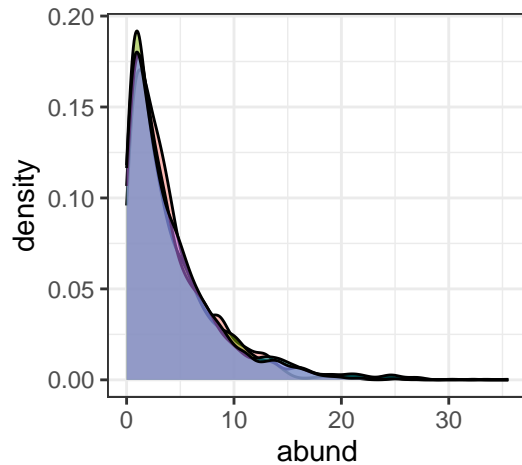
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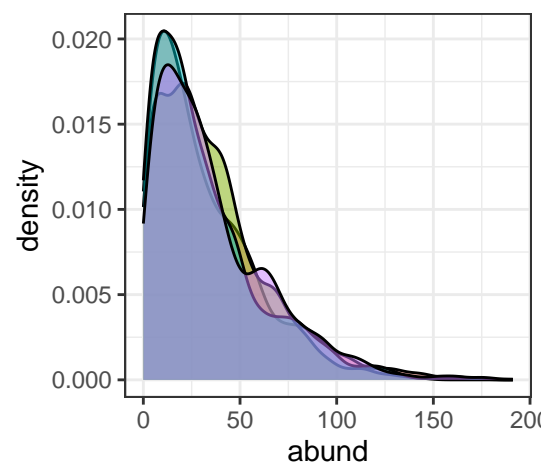
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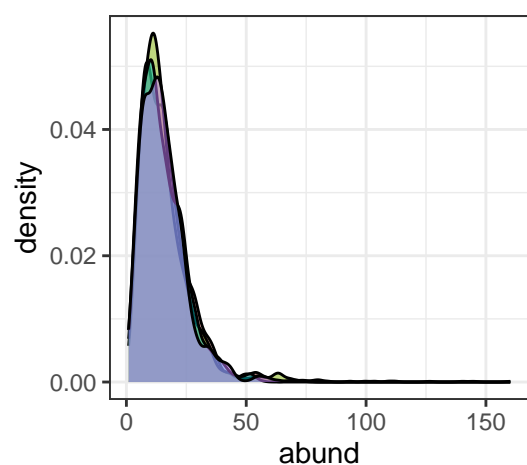
EVL_bb



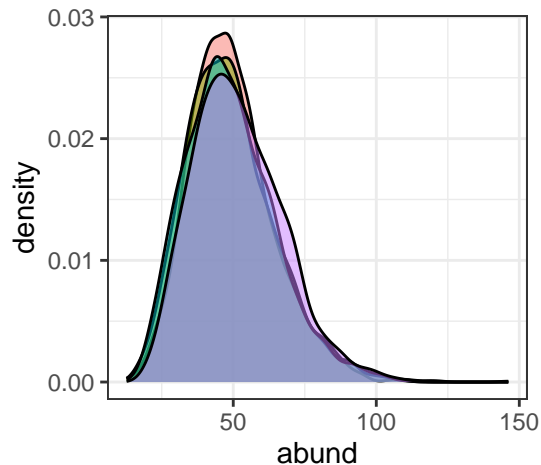
GOA_bb



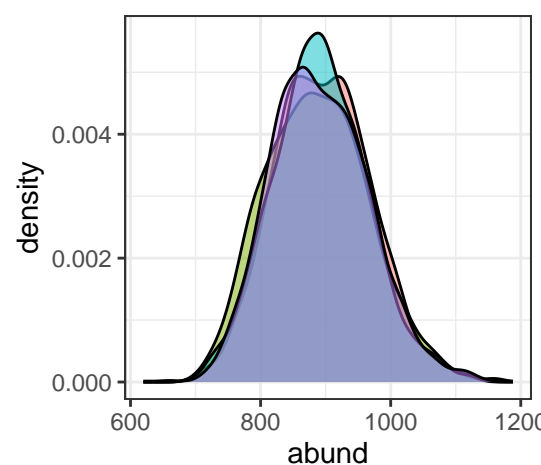
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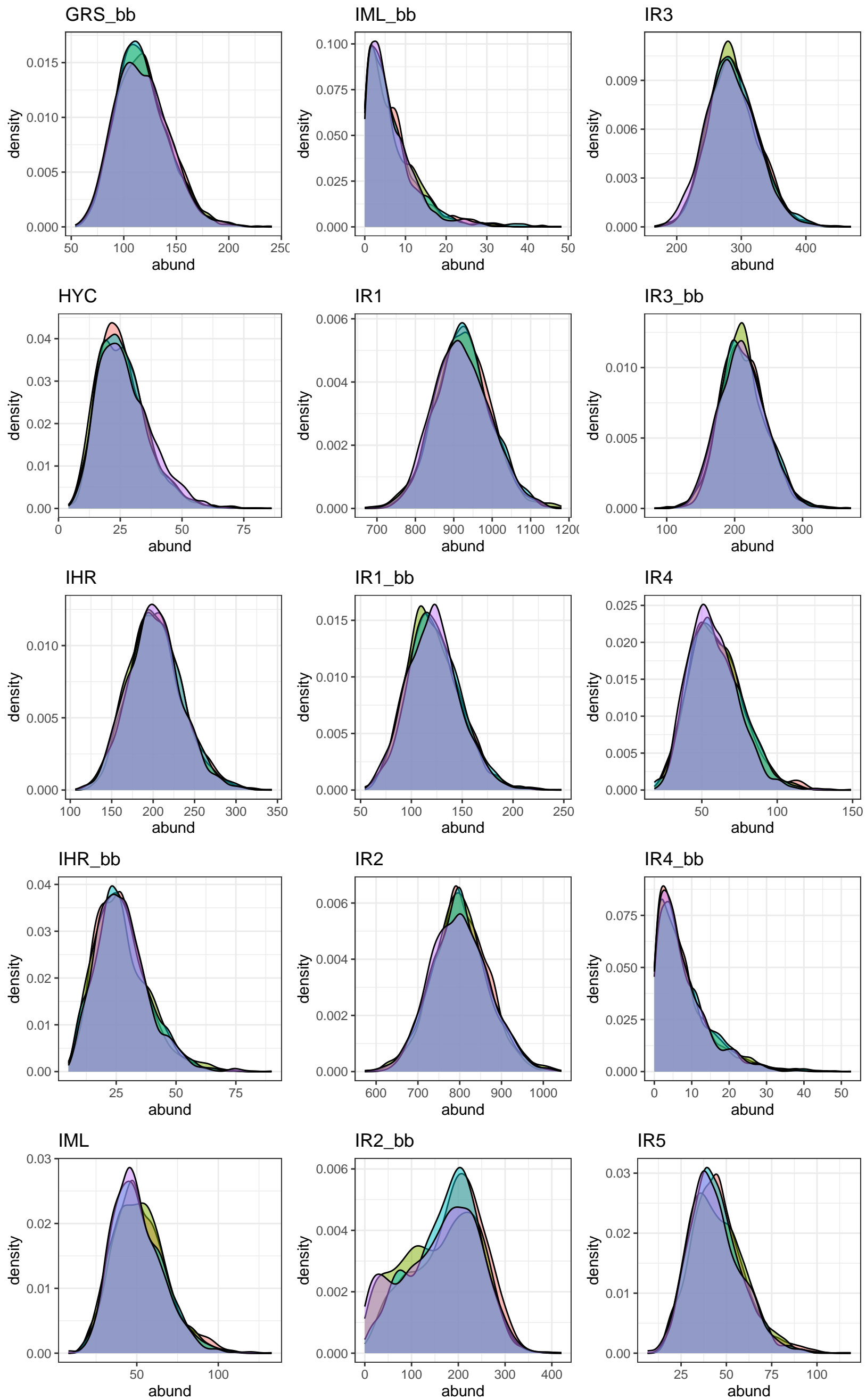


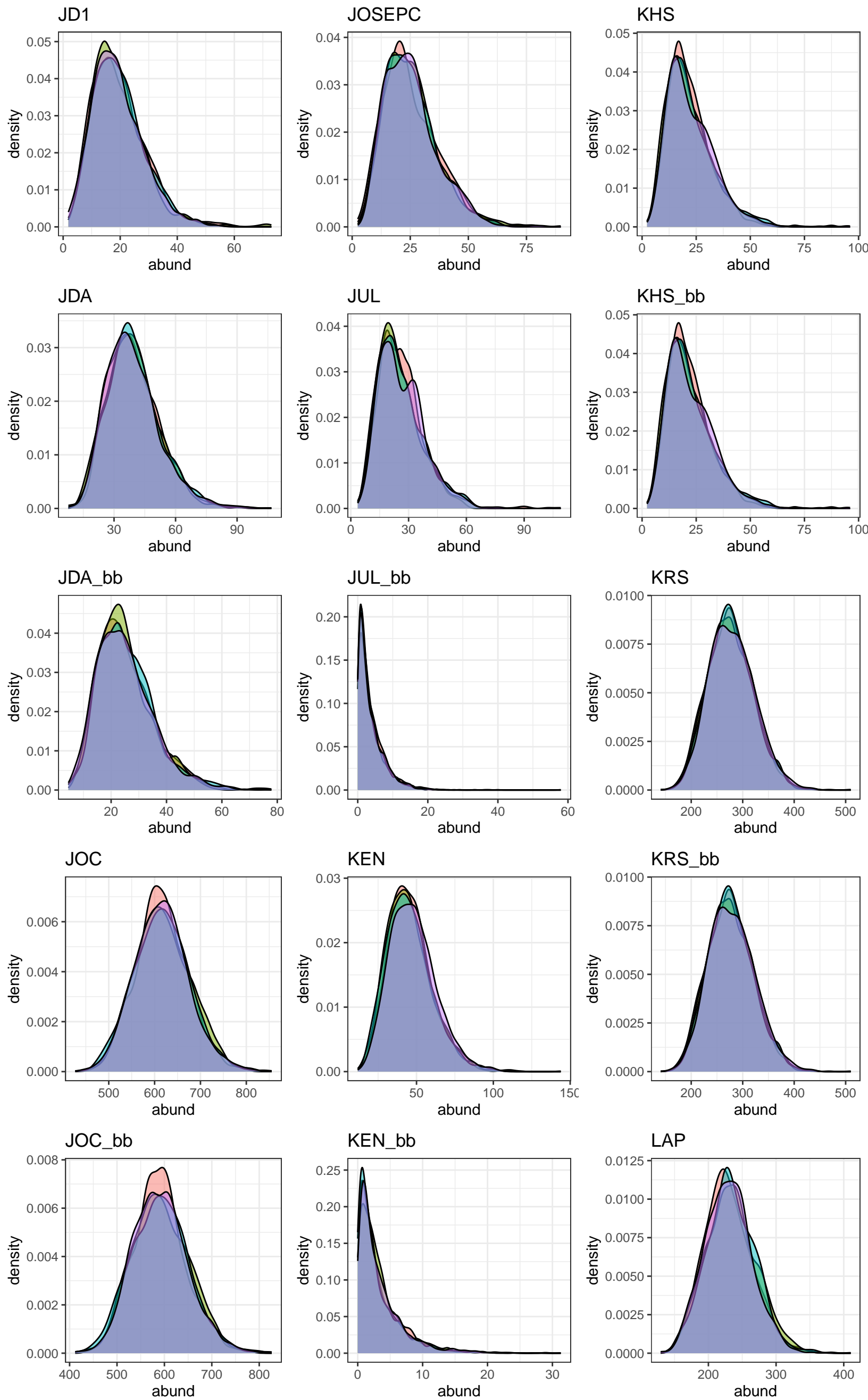
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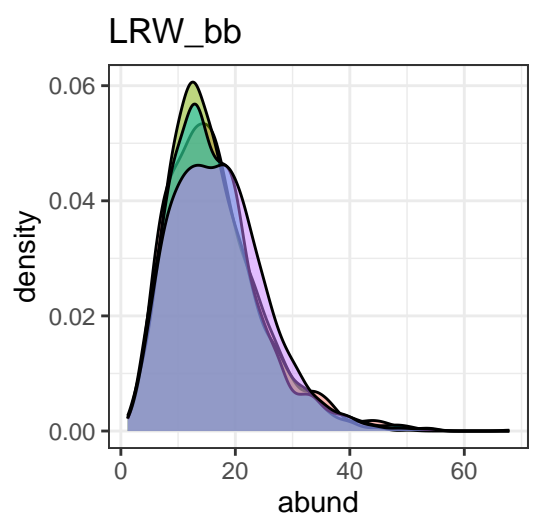
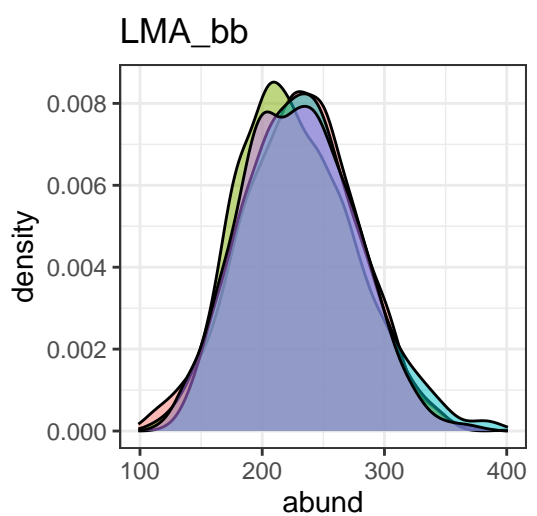
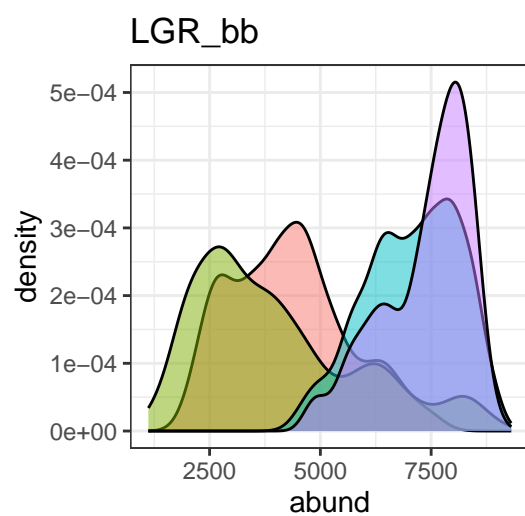
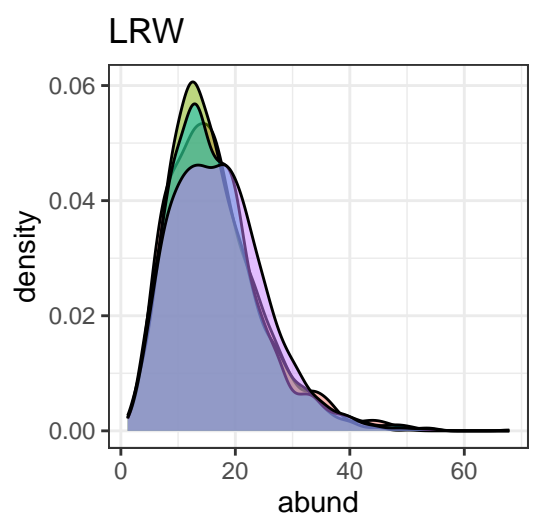
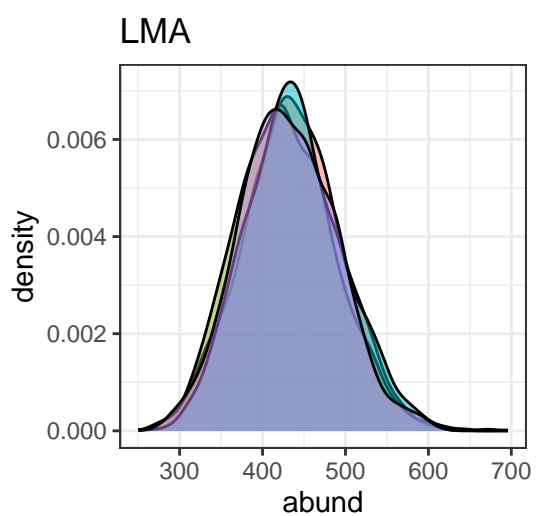
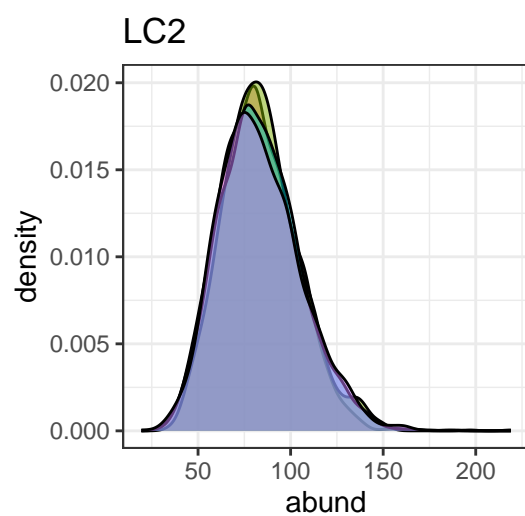
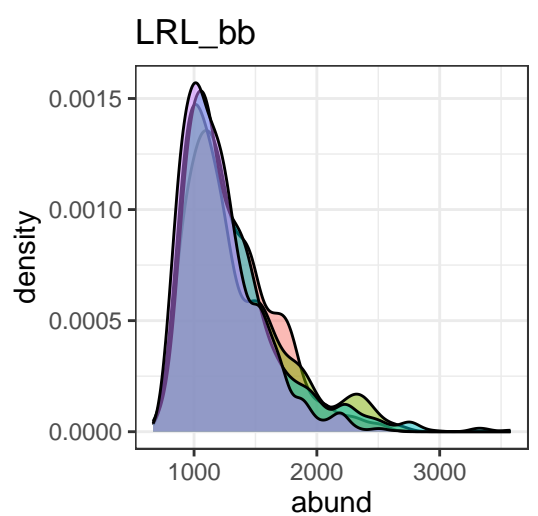
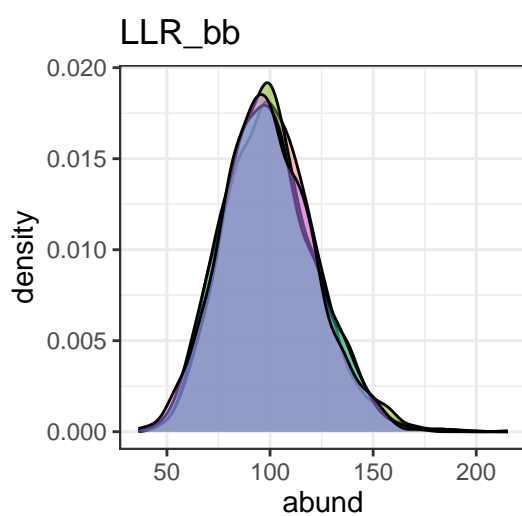
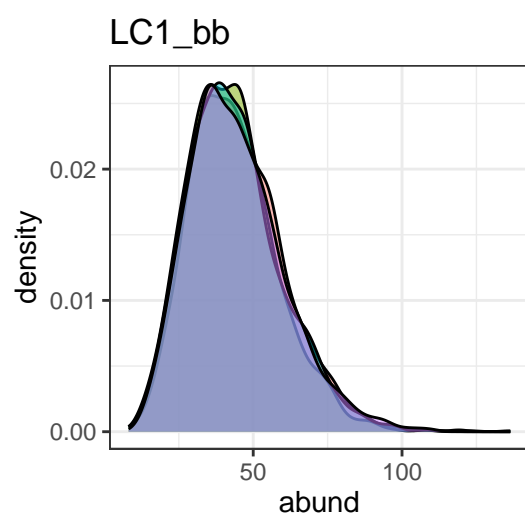
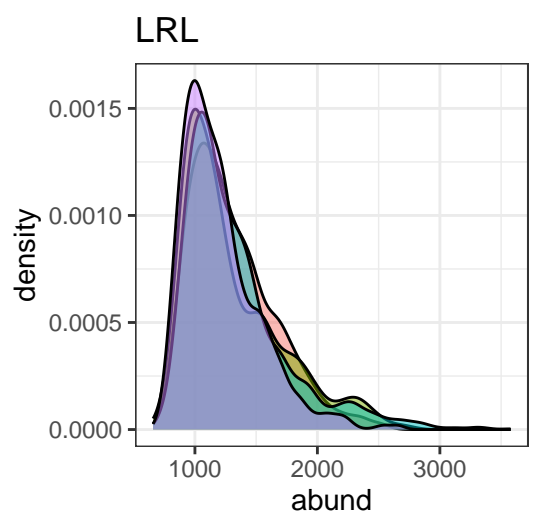
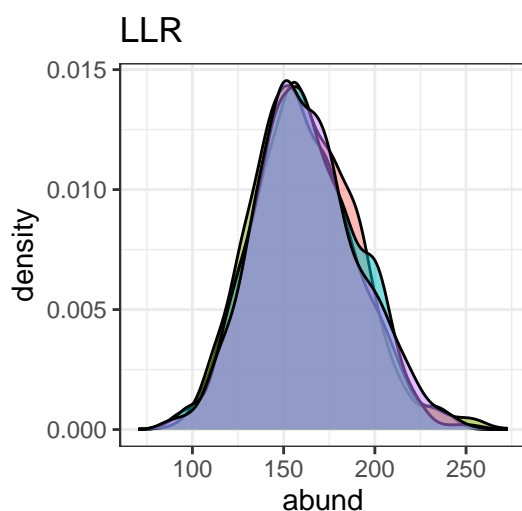
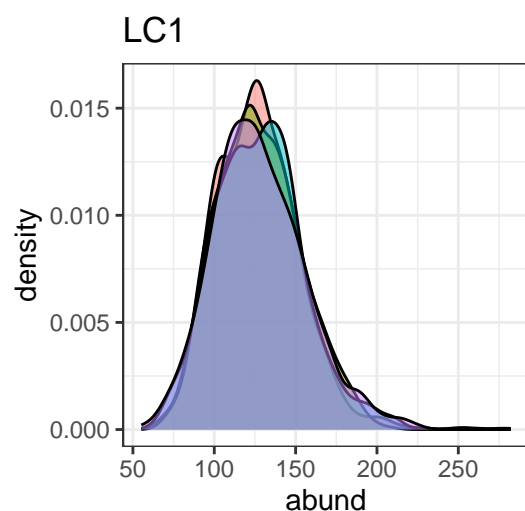
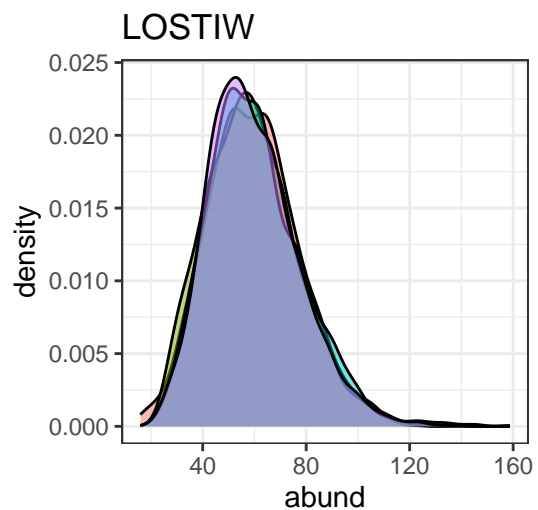
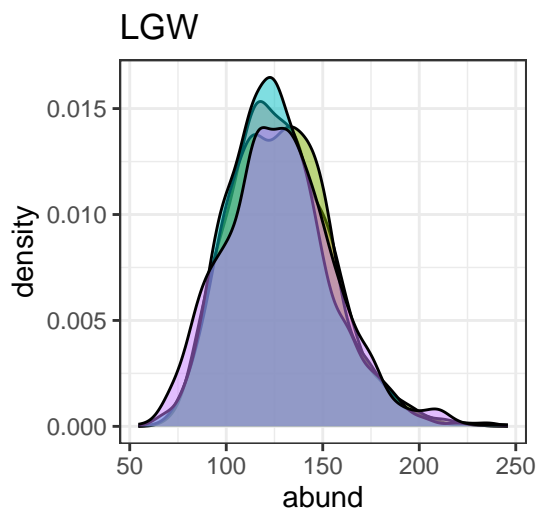
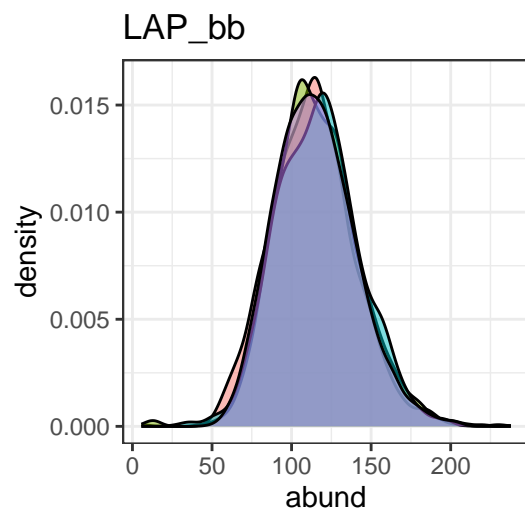


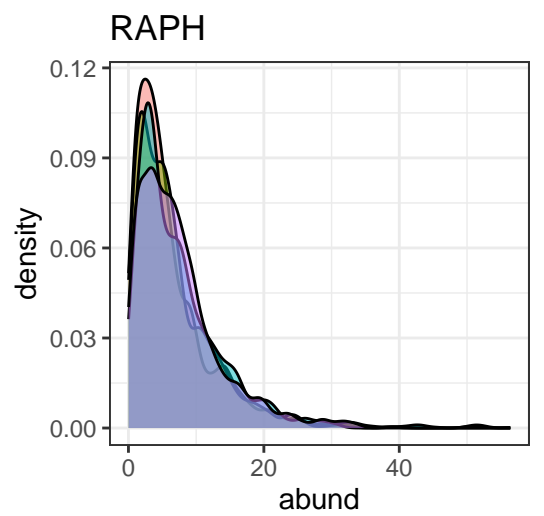
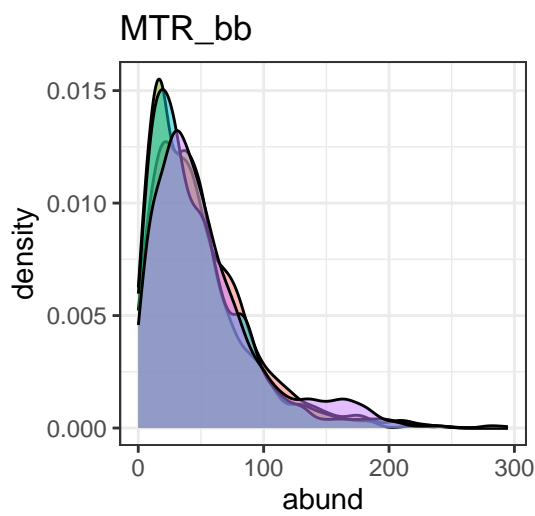
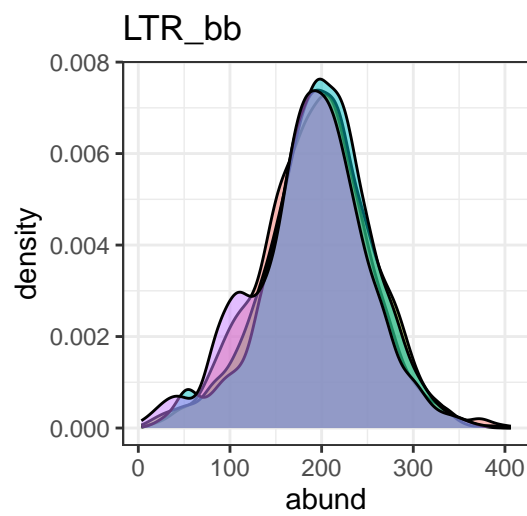
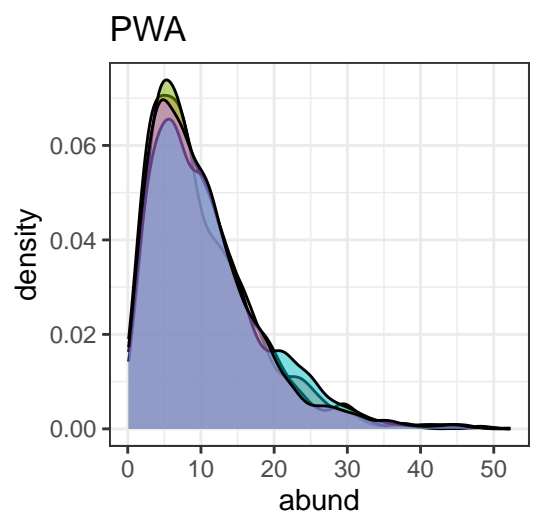
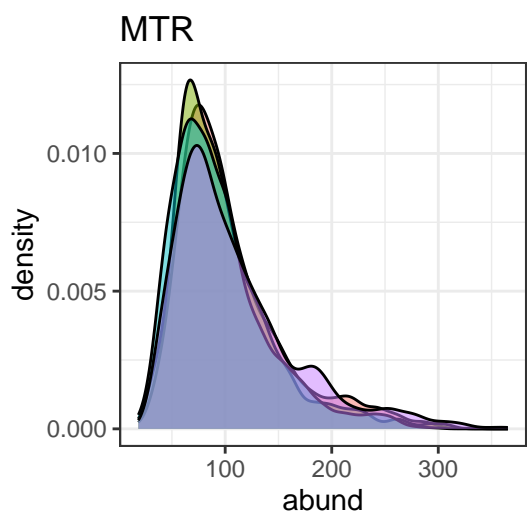
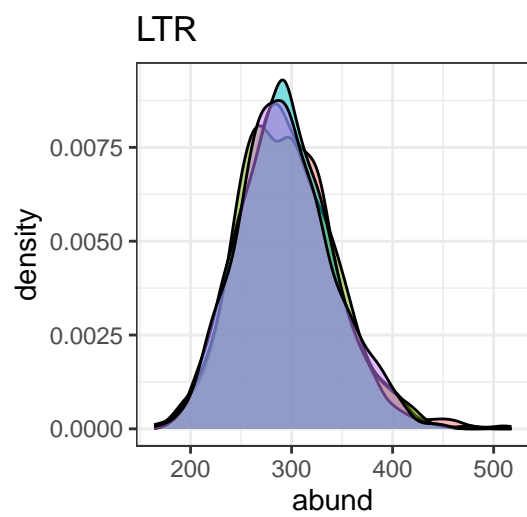
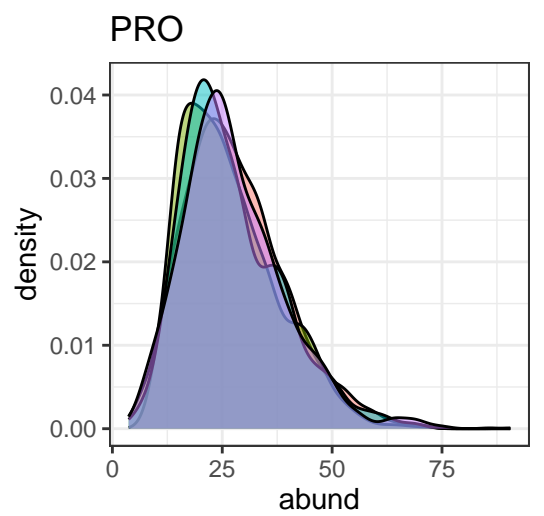
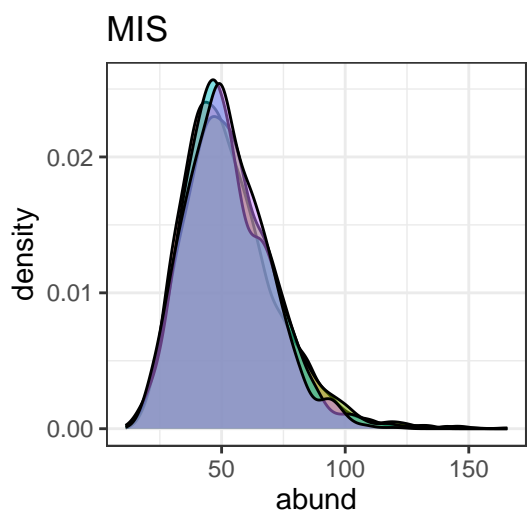
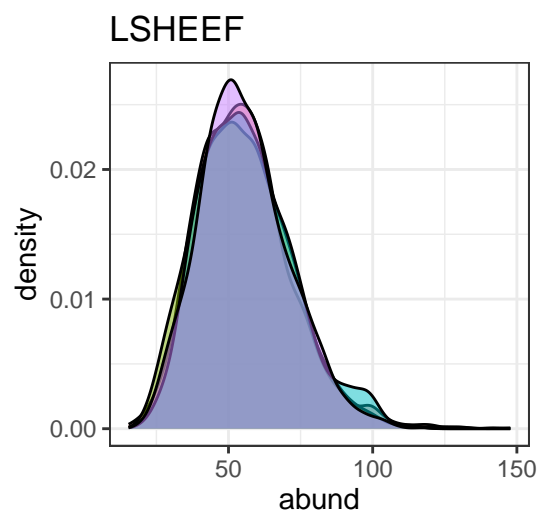
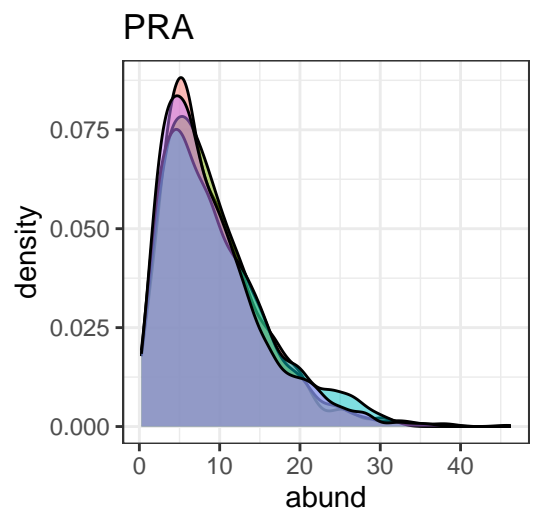
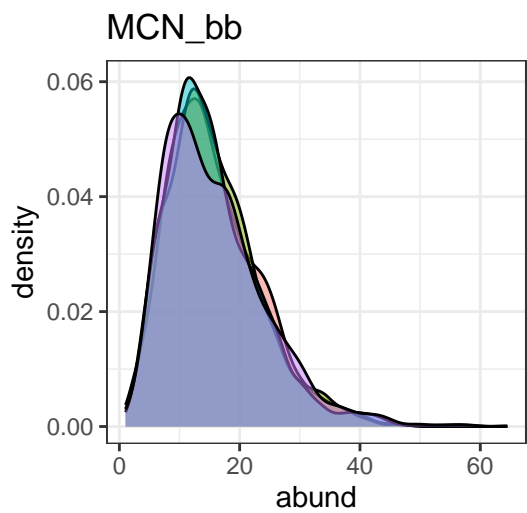
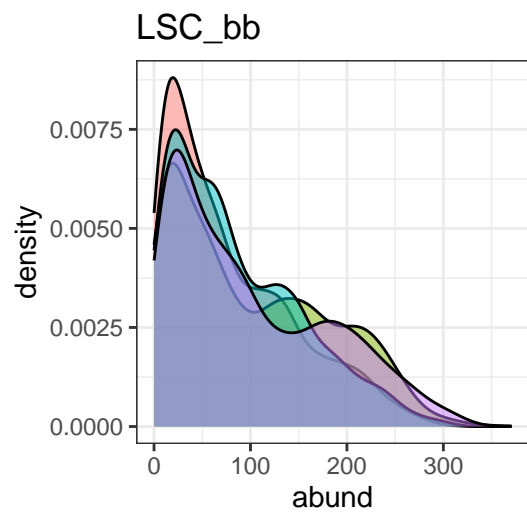
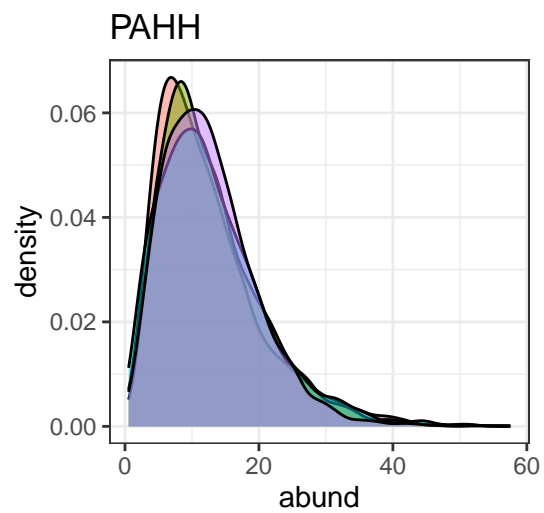
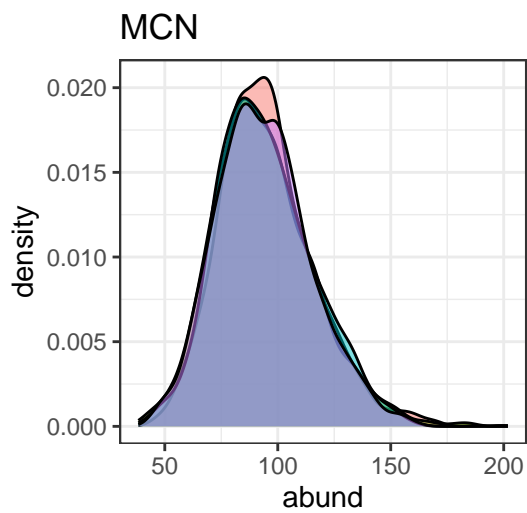
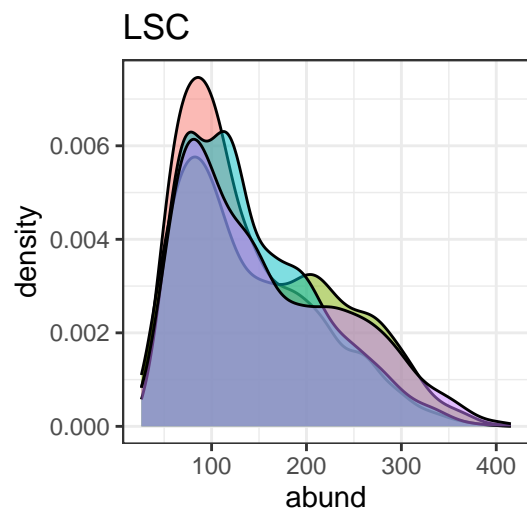
GRS



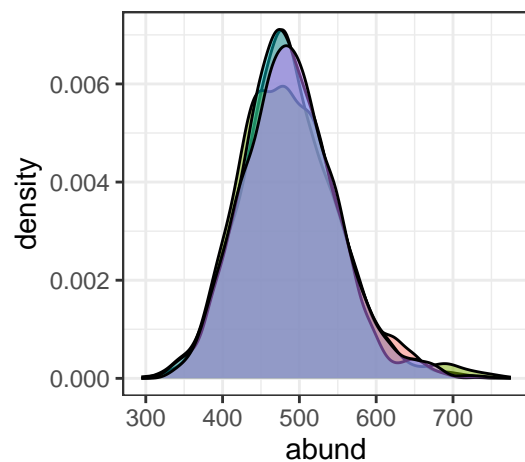




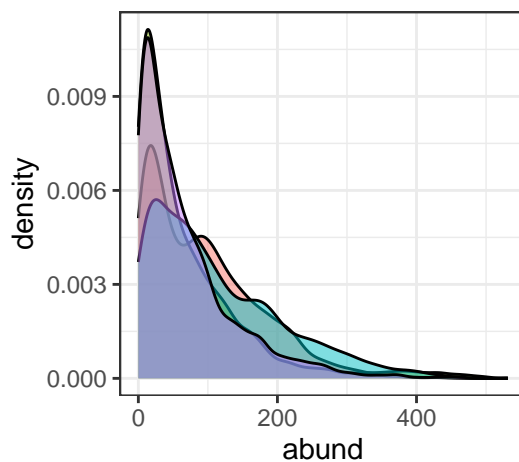




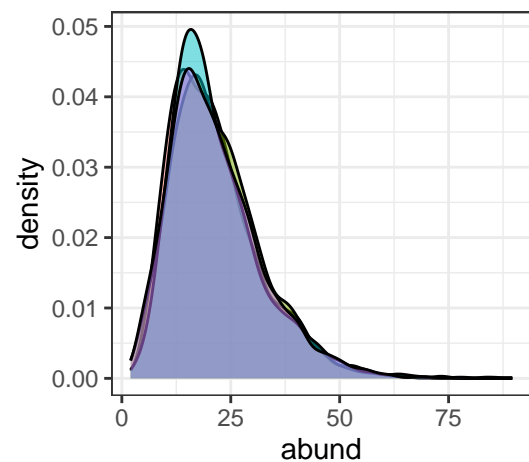
SC1



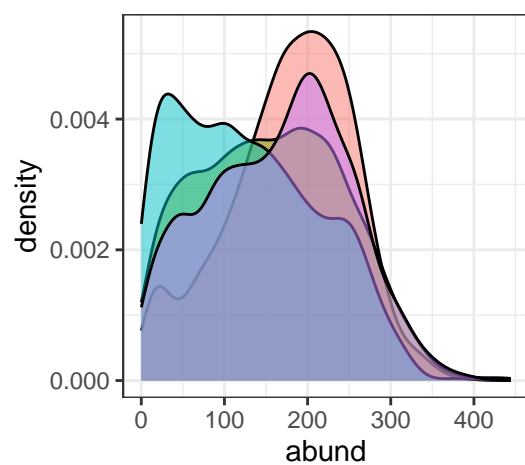
SC3_bb



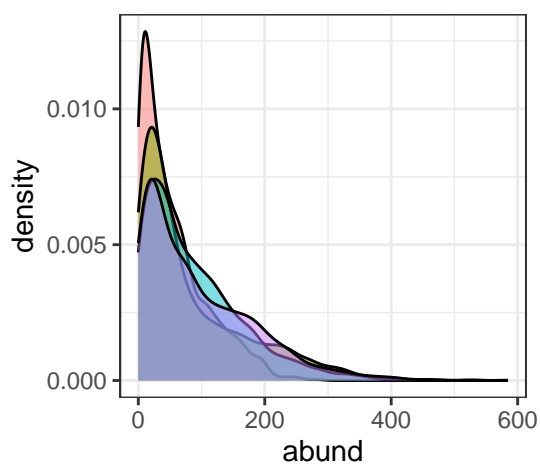
STL



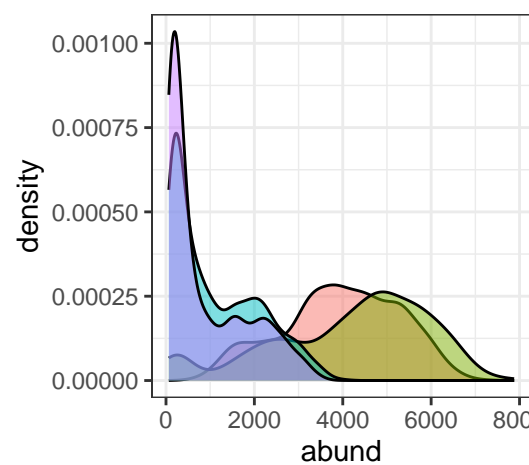
SC1_bb



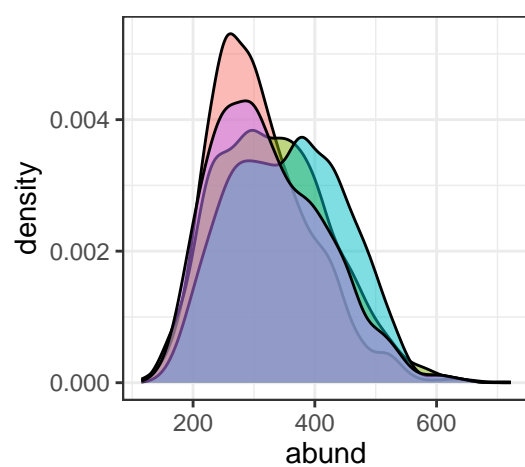
SC4



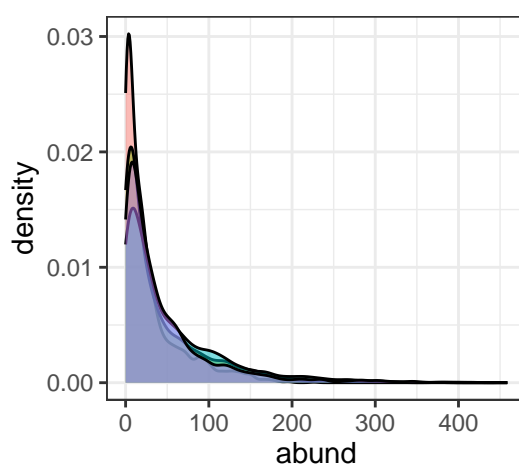
SW1



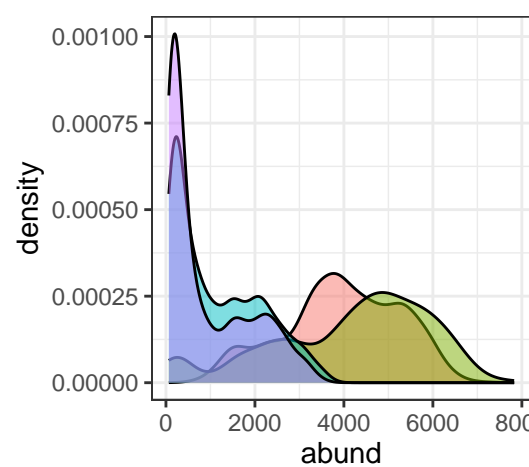
SC2



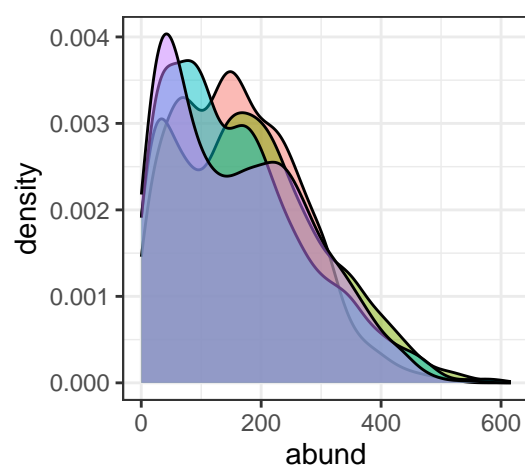
SC4_bb



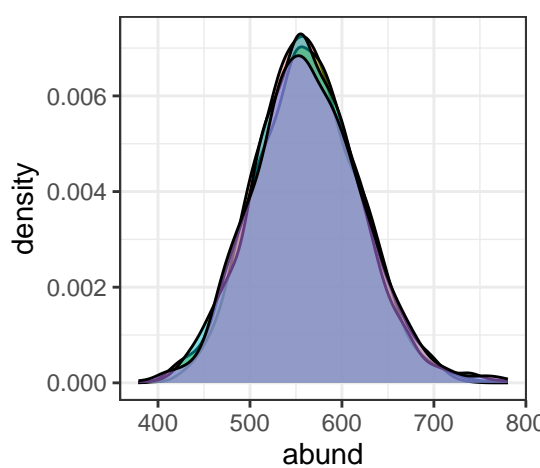
SW1_bb



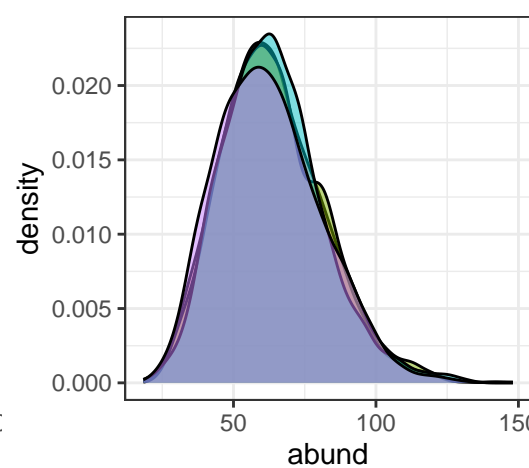
SC2_bb



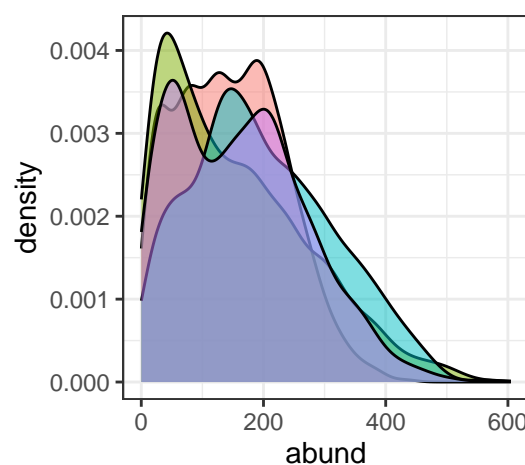
SFG



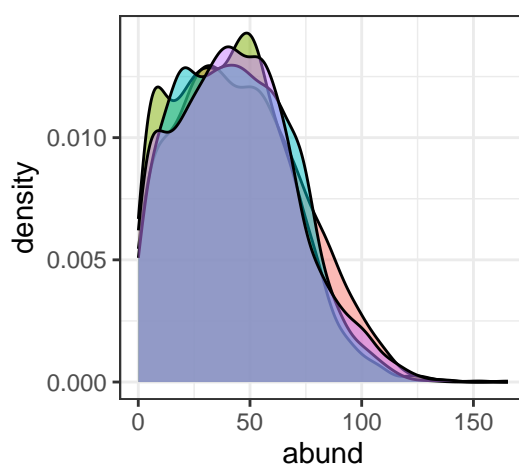
SWT



SC3



SFG_bb



SWT_bb

