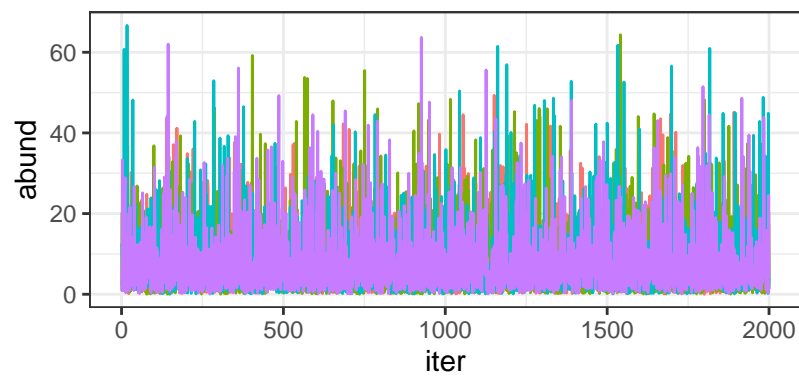
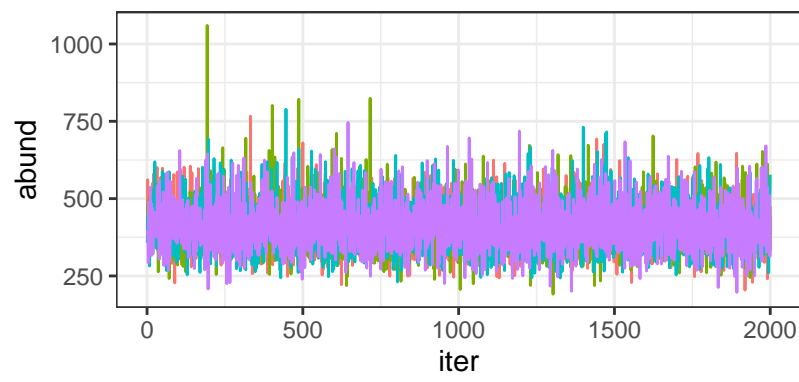


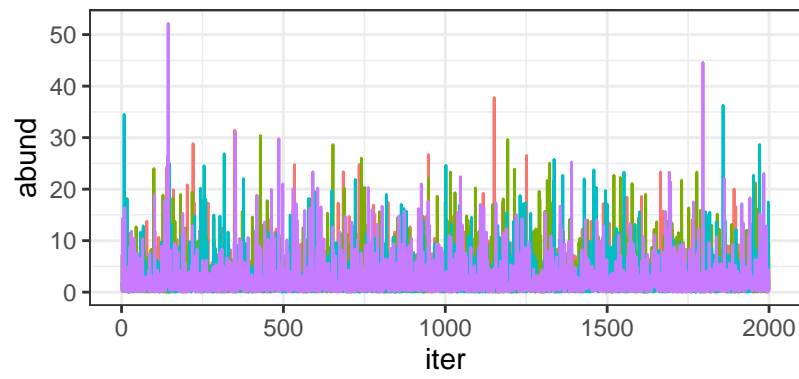
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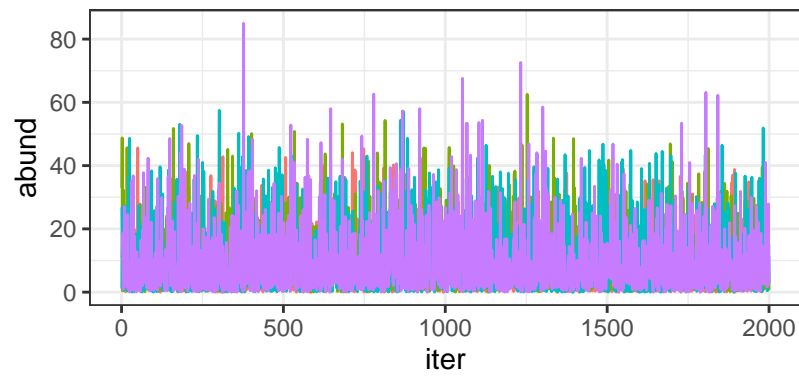
CCU



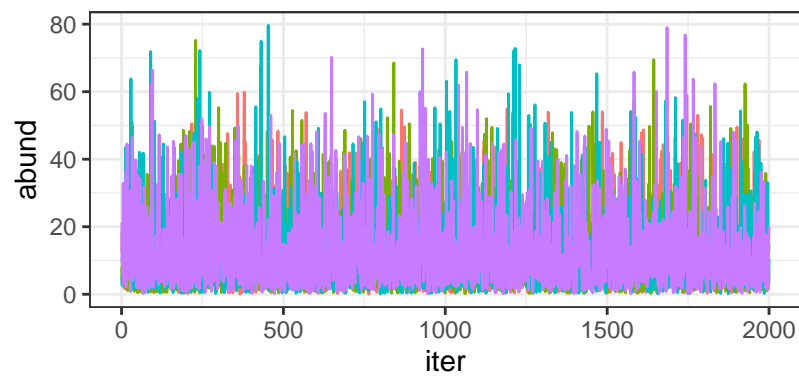
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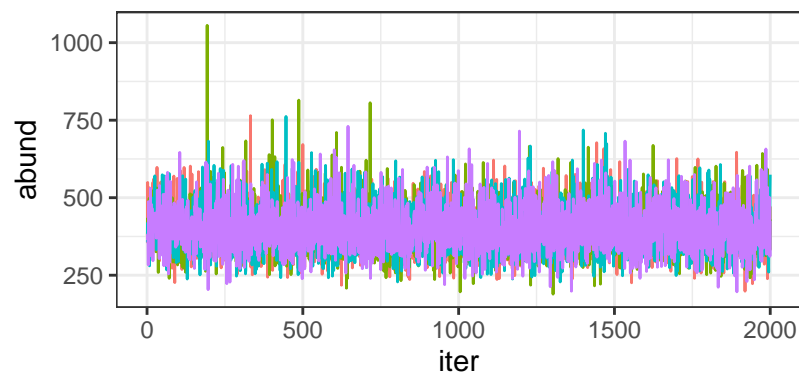
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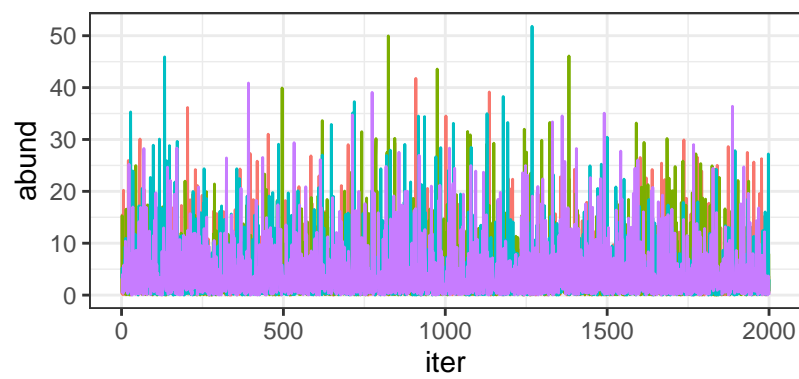
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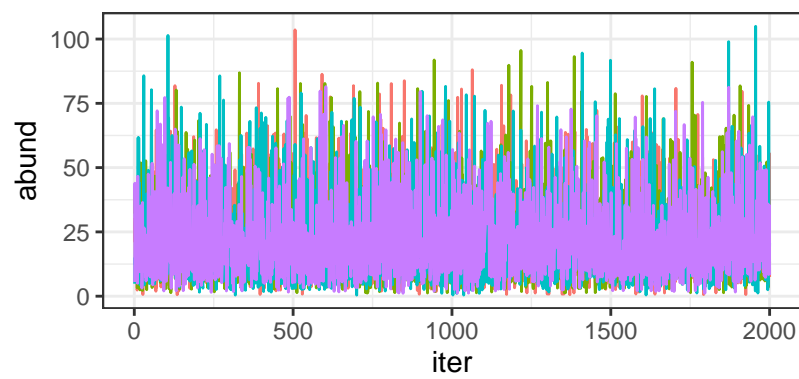
CCW



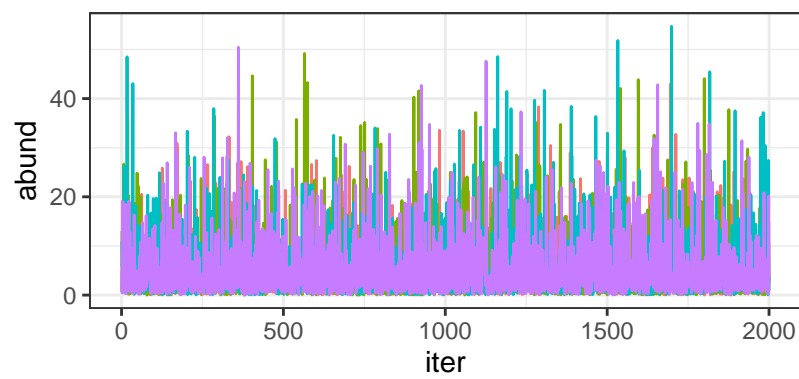
ACM_bb



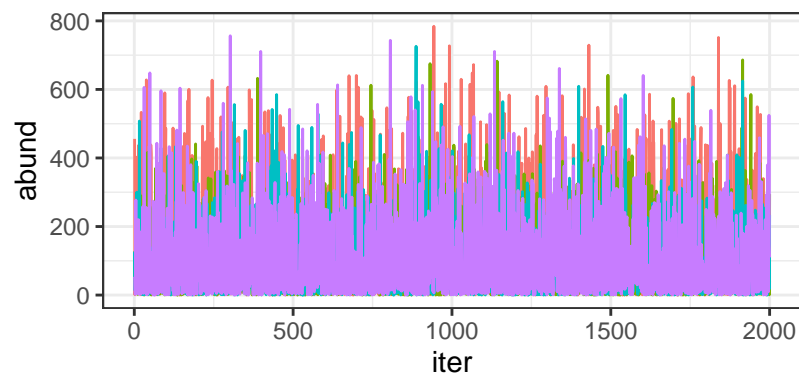
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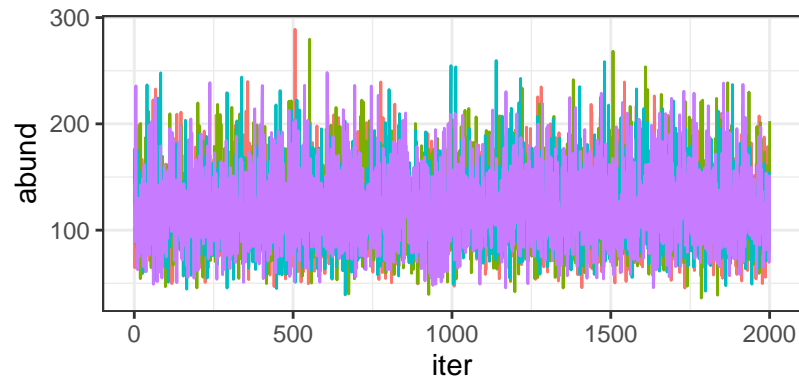
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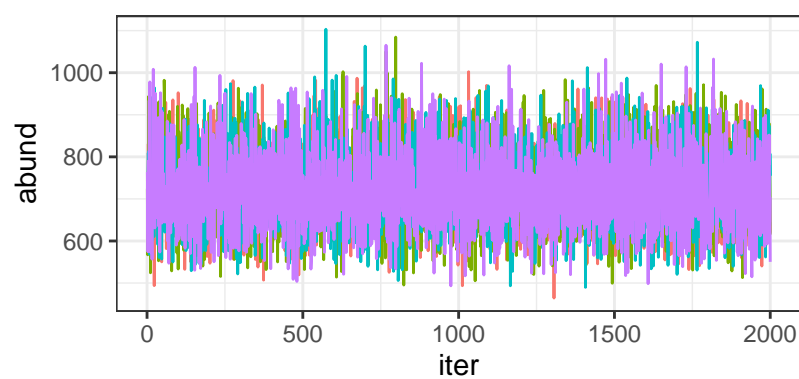
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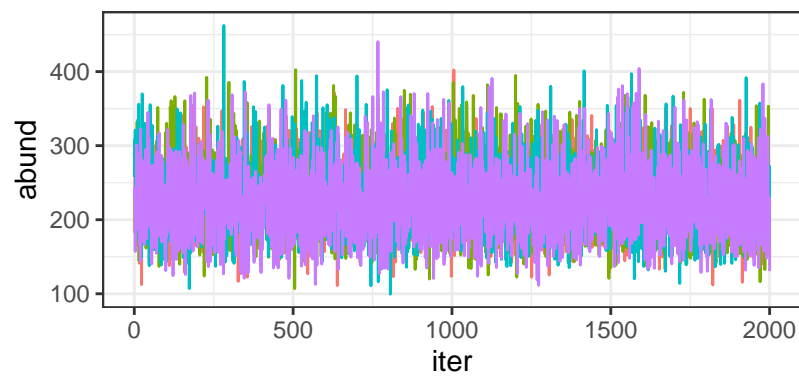
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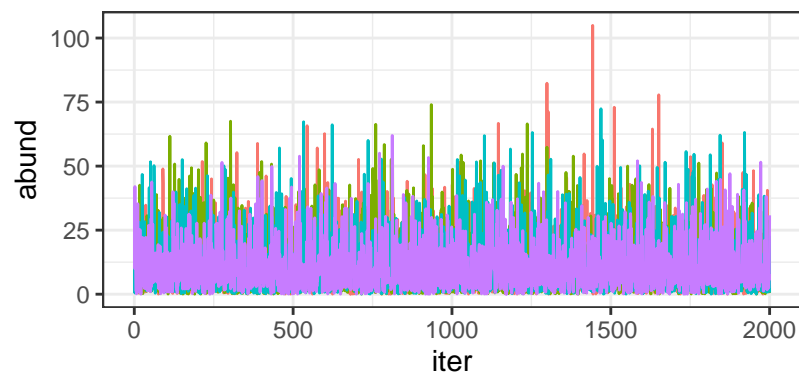
ESS



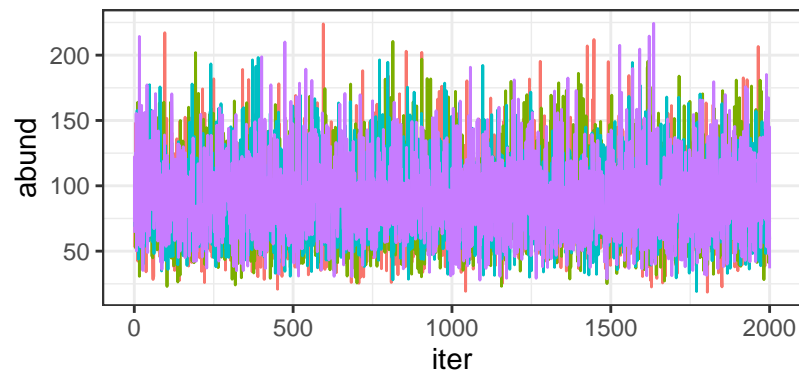
ESS_bb



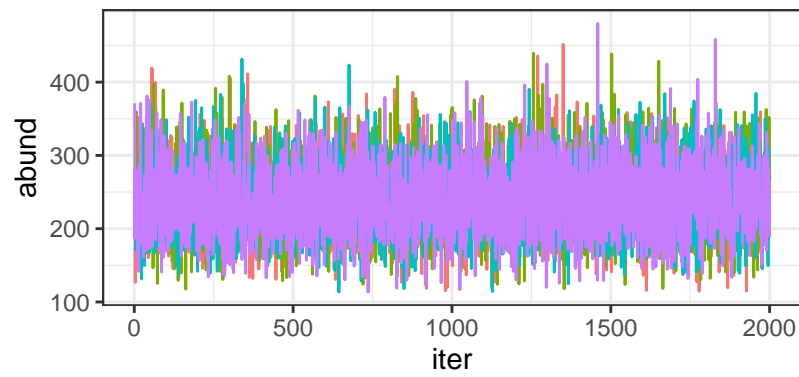
GOA_bb



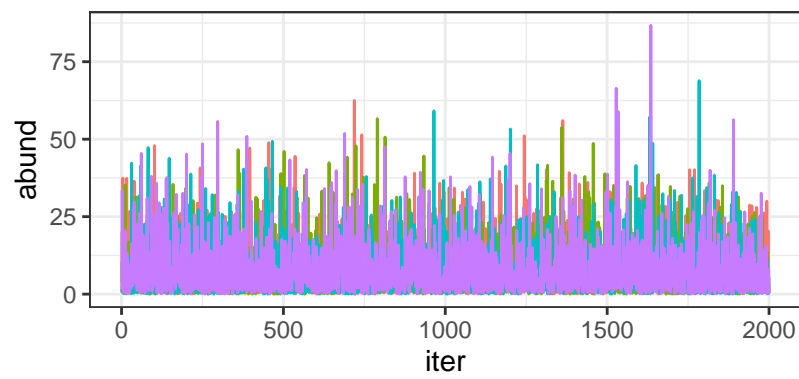
EVL



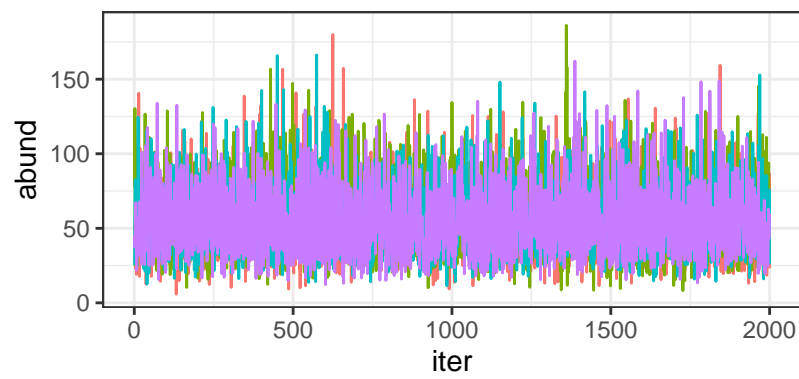
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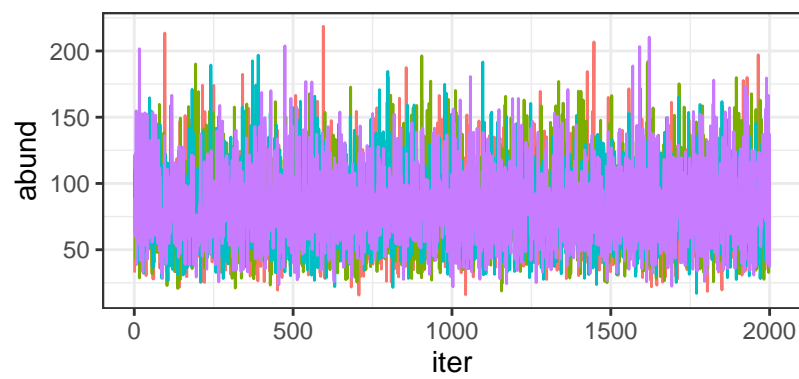
EVL_bb



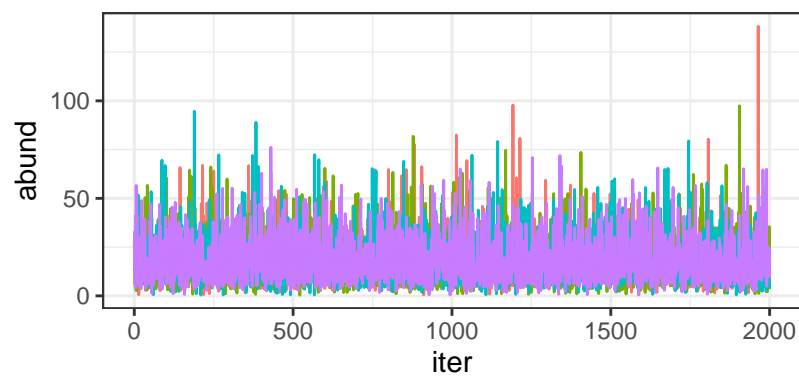
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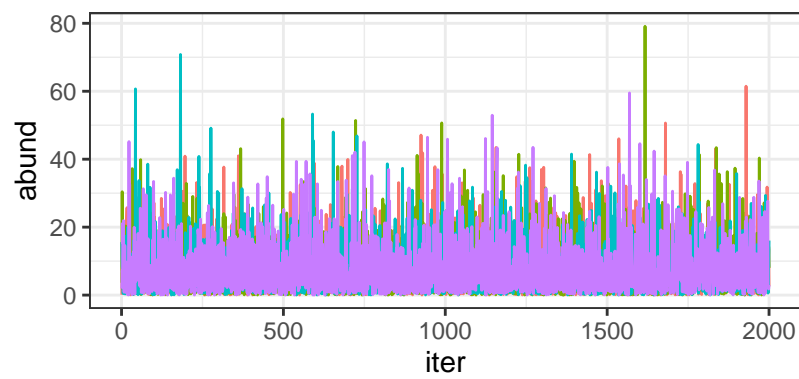
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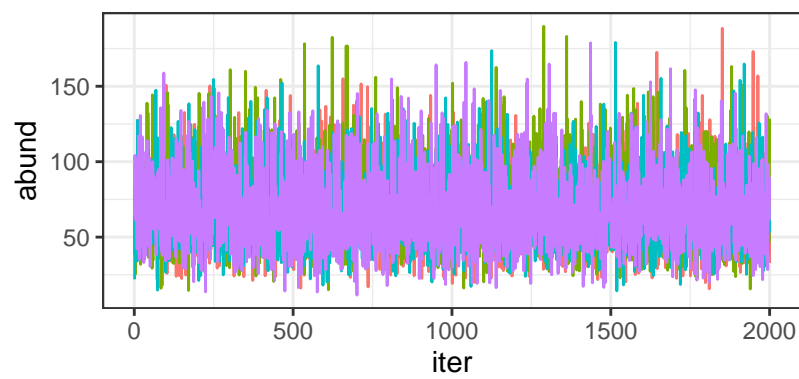
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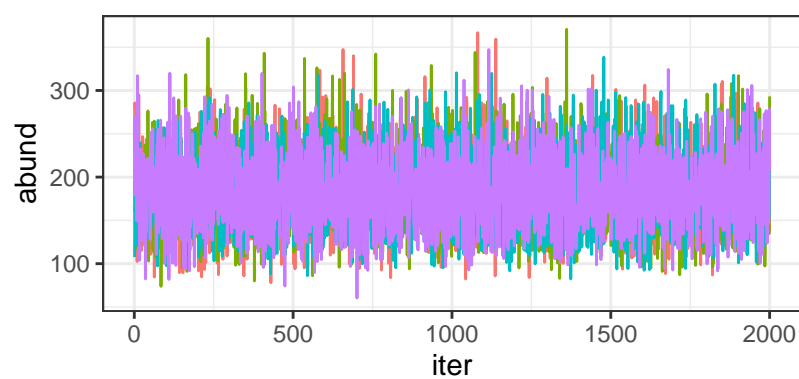
EVL_bb



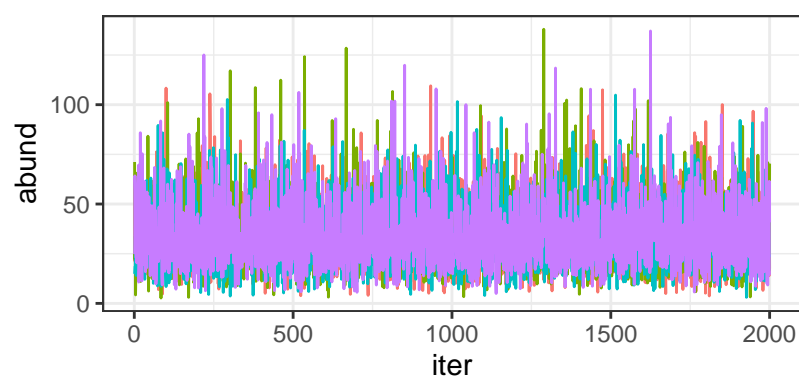
IHR



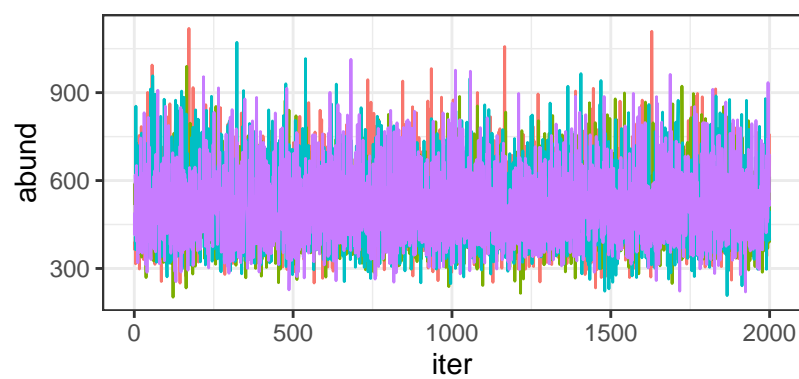
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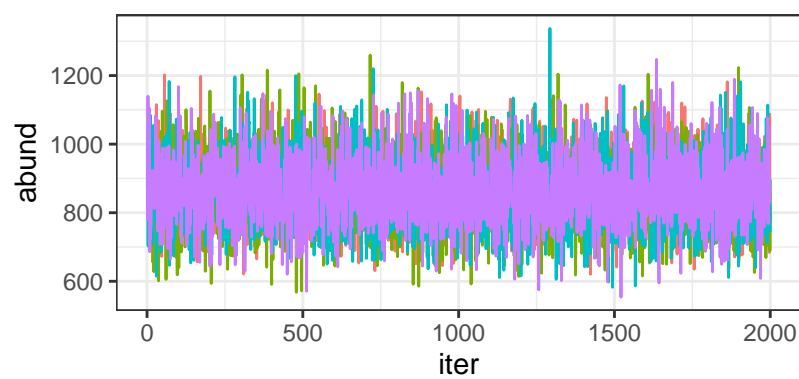
IHR_bb



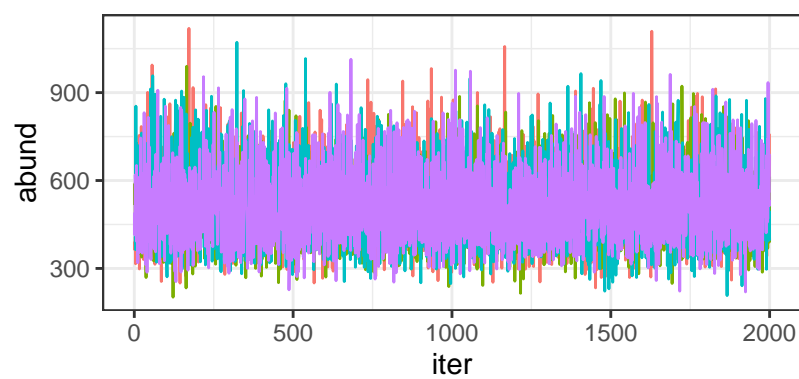
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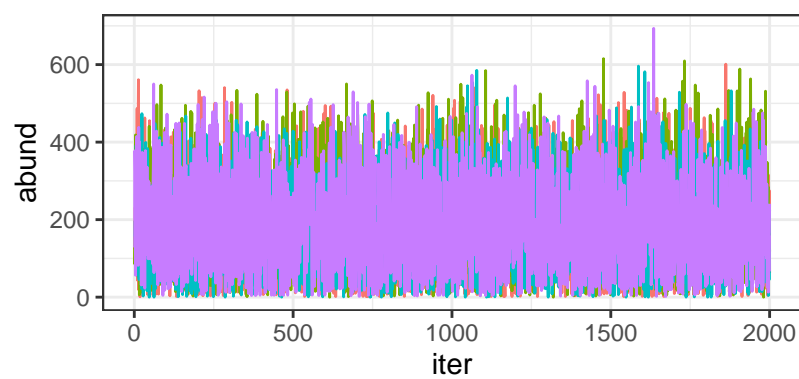
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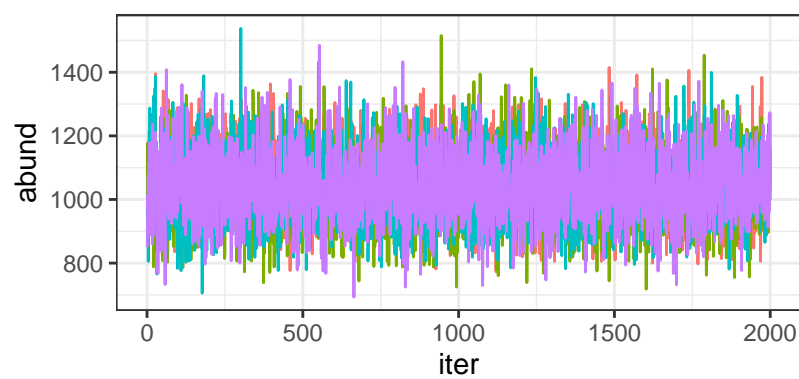
IML_bb



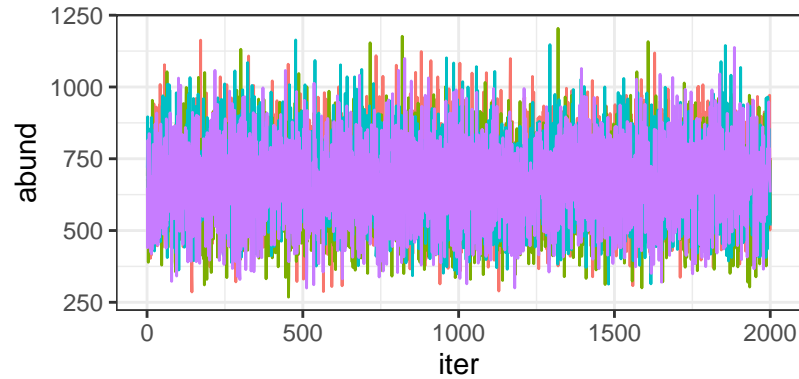
IR3_bb



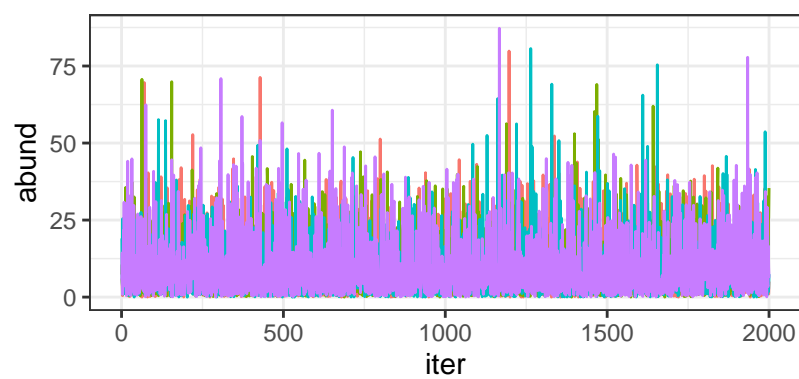
IR1



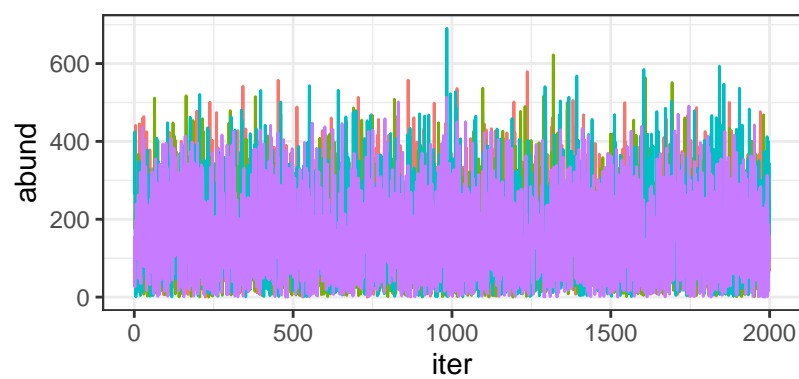
IR4



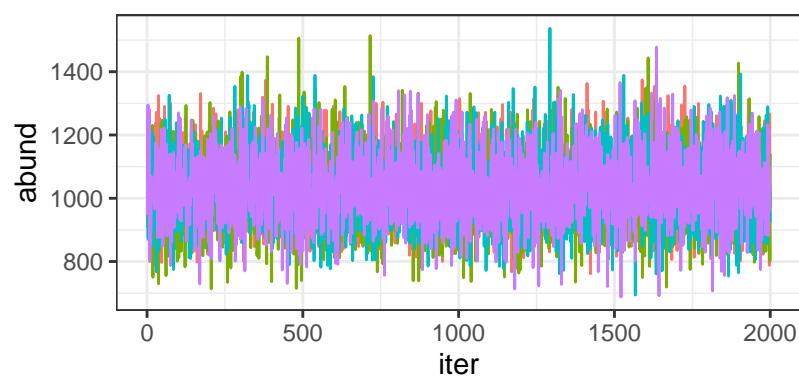
IR1_bb



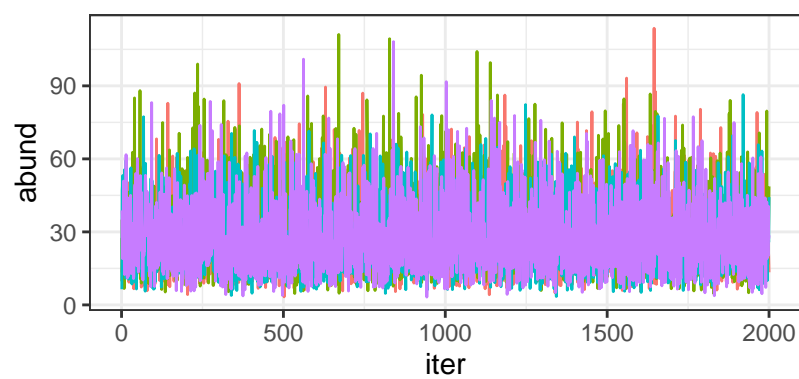
IR4_bb



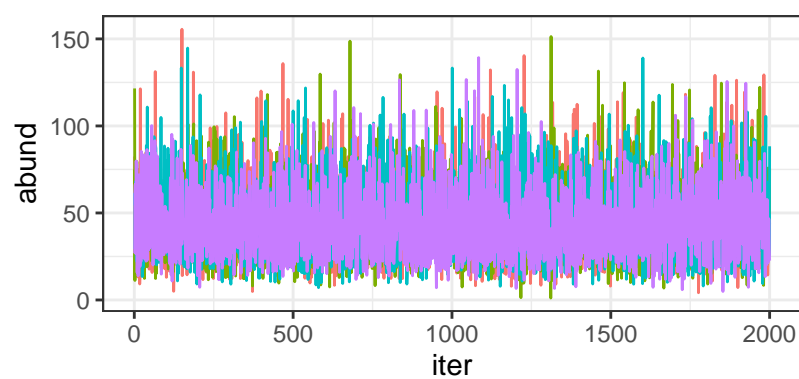
IR2



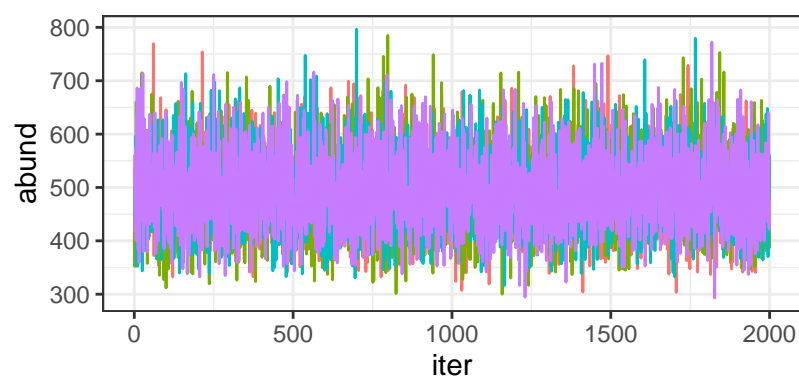
JD1



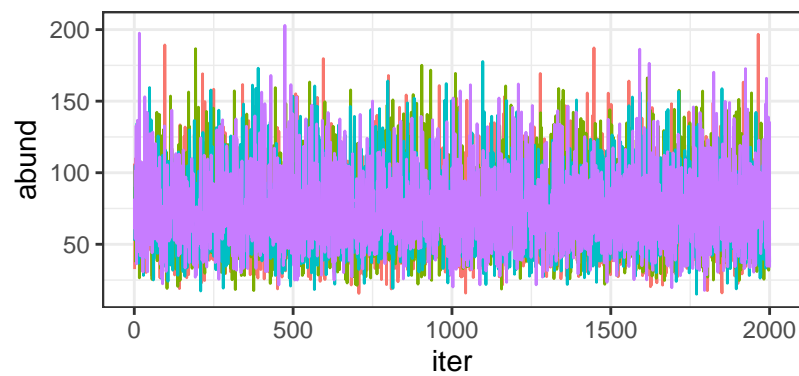
IR2_bb



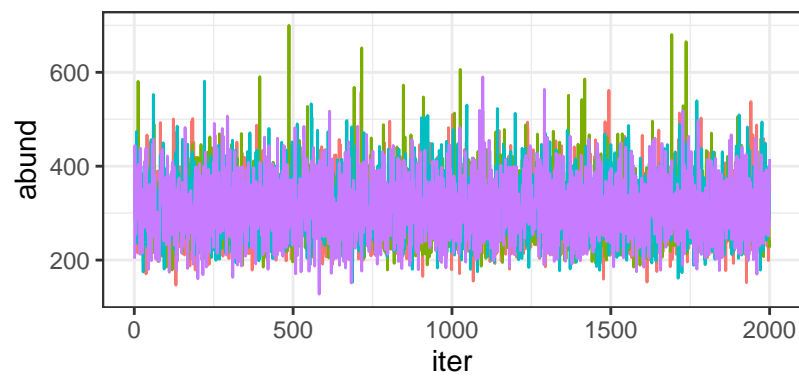
JOHNSC



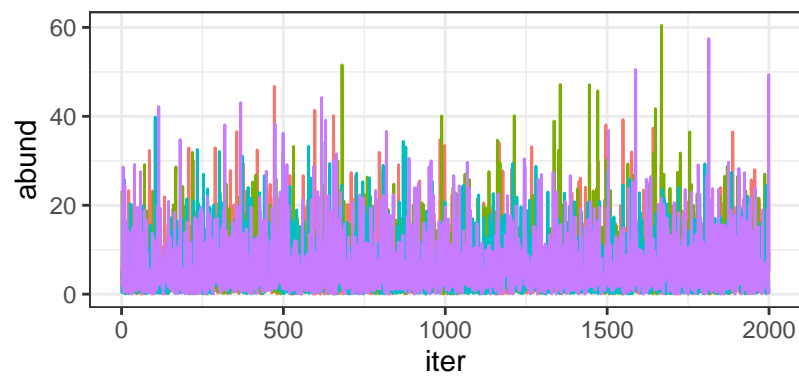
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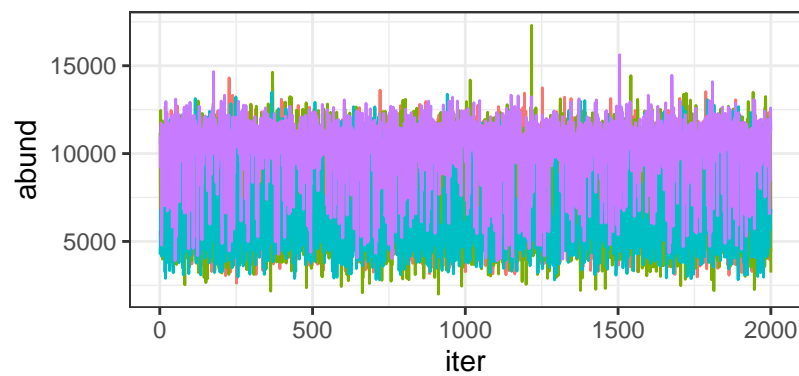
LC2



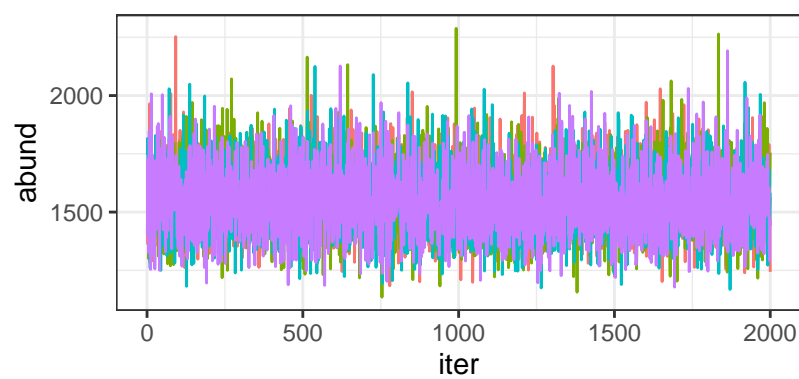
KEN_bb



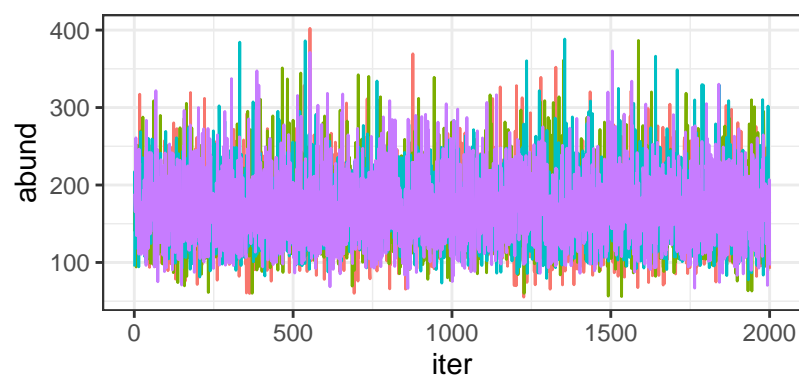
LGR_bb



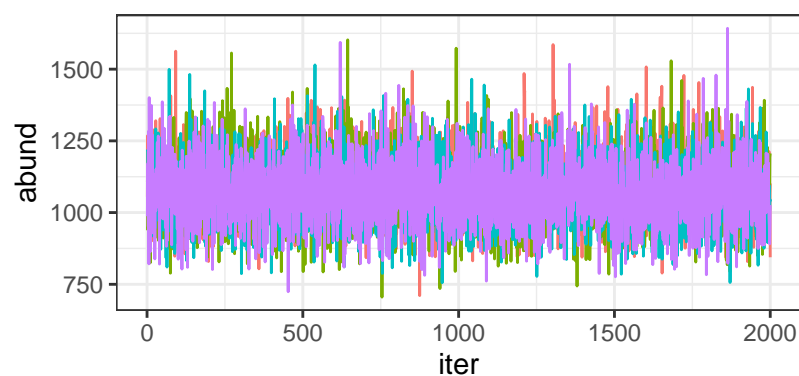
KRS



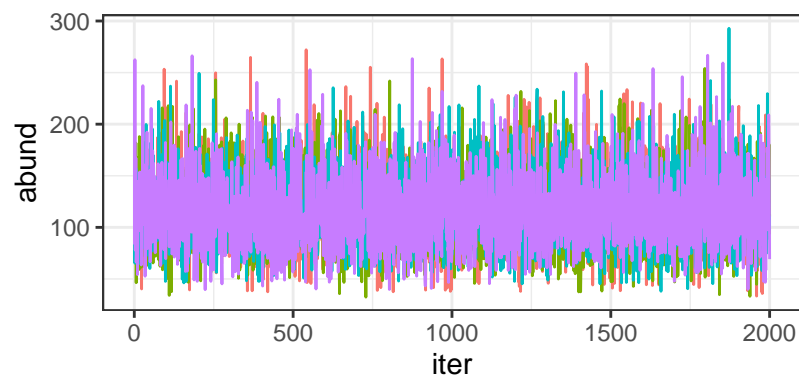
LGW



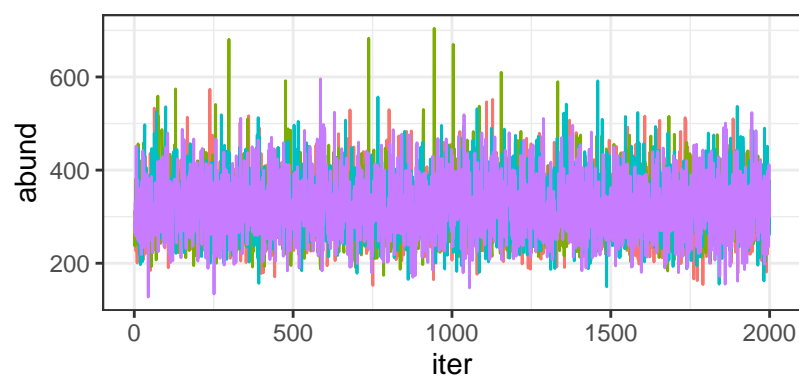
KRS_bb



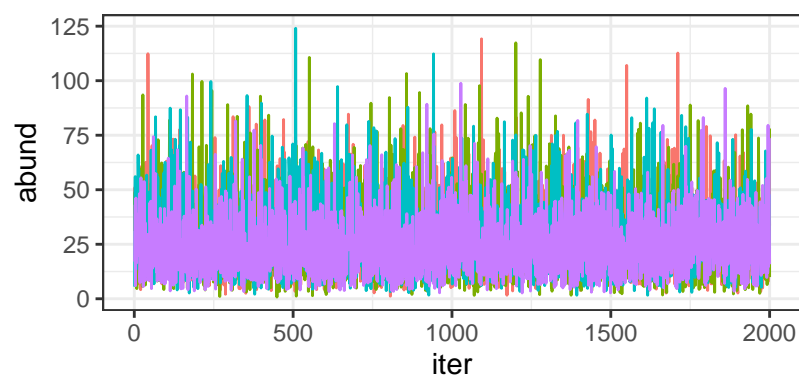
LLR



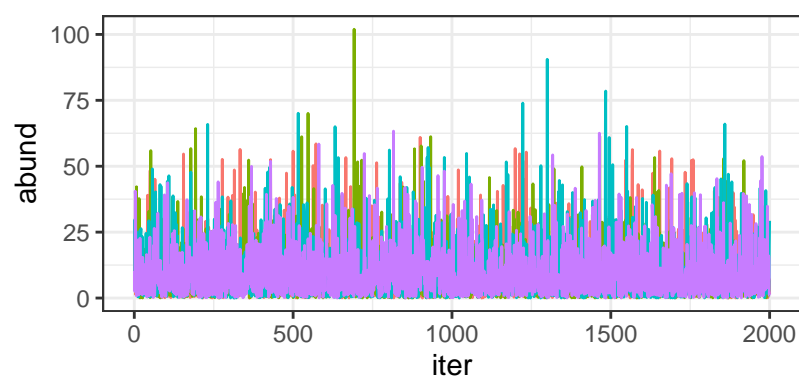
LC1



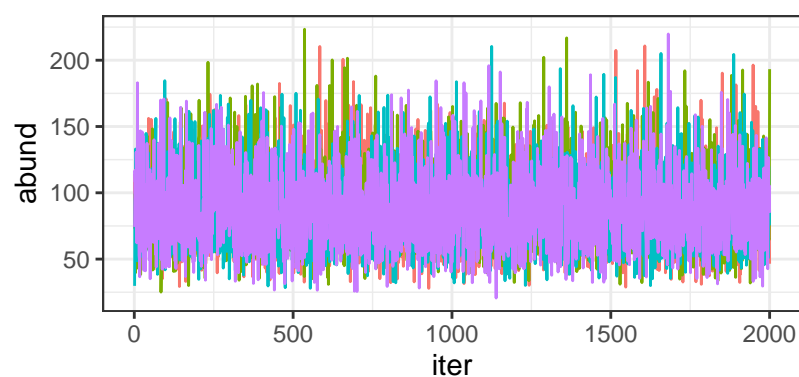
LLR_bb

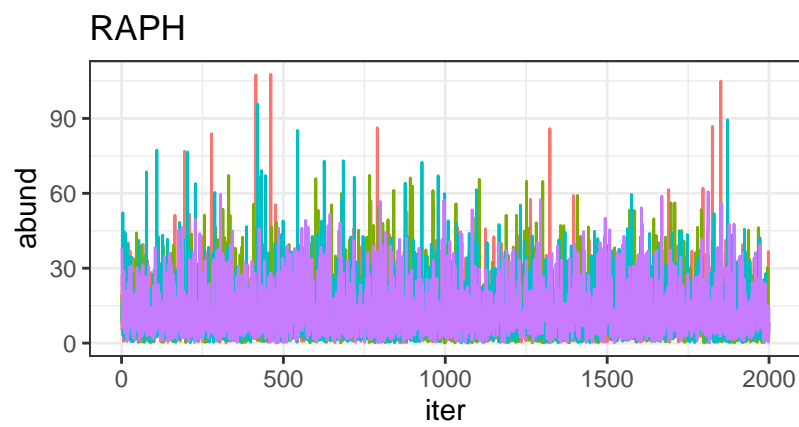
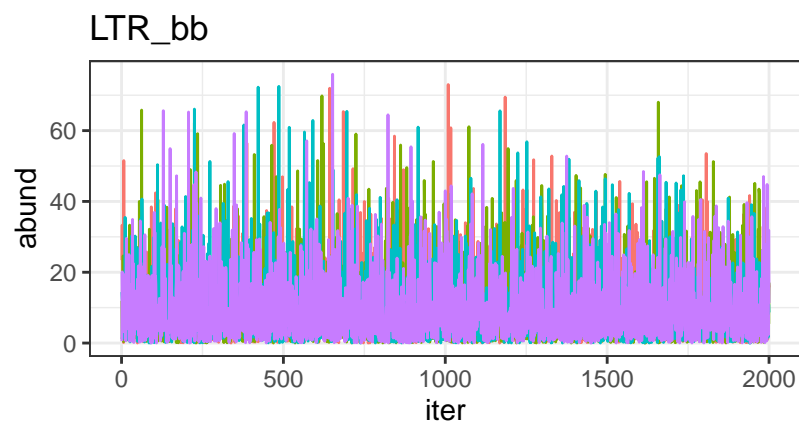
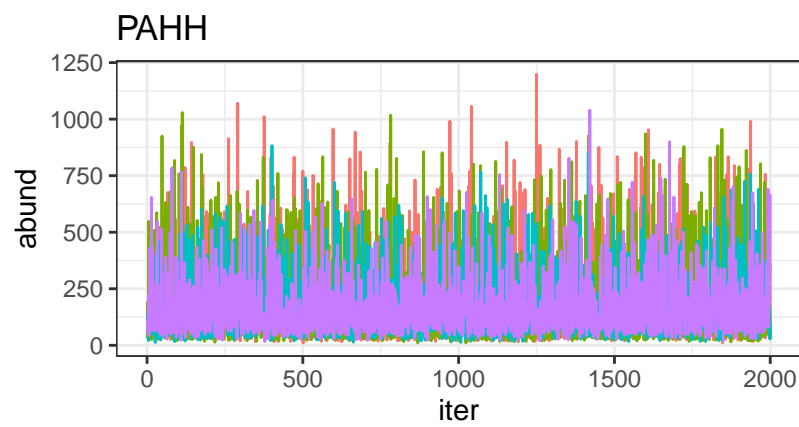
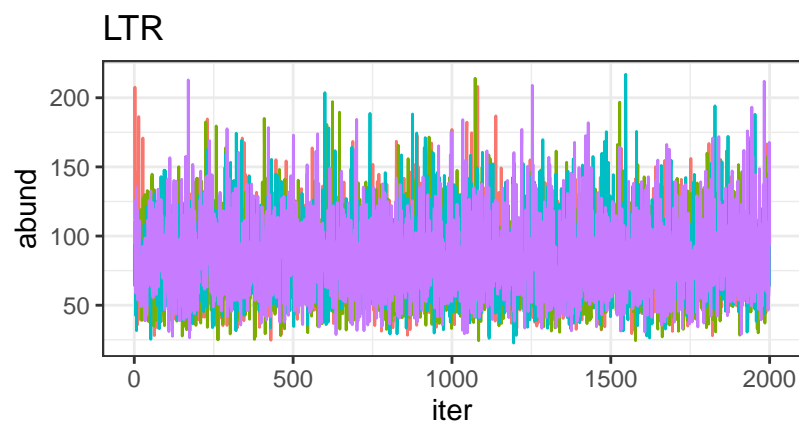
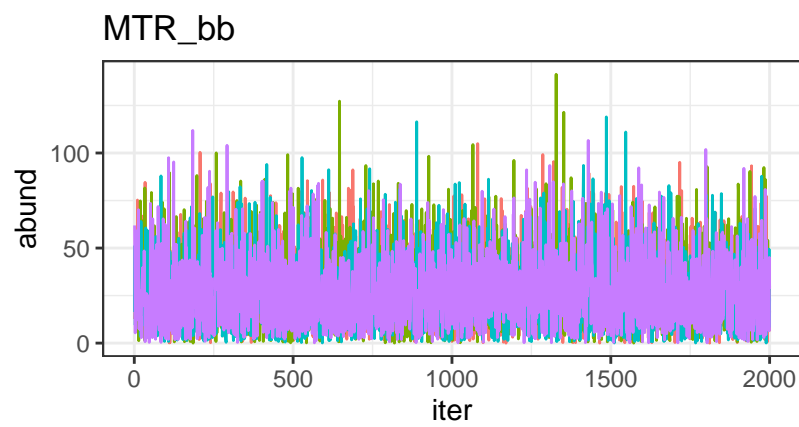
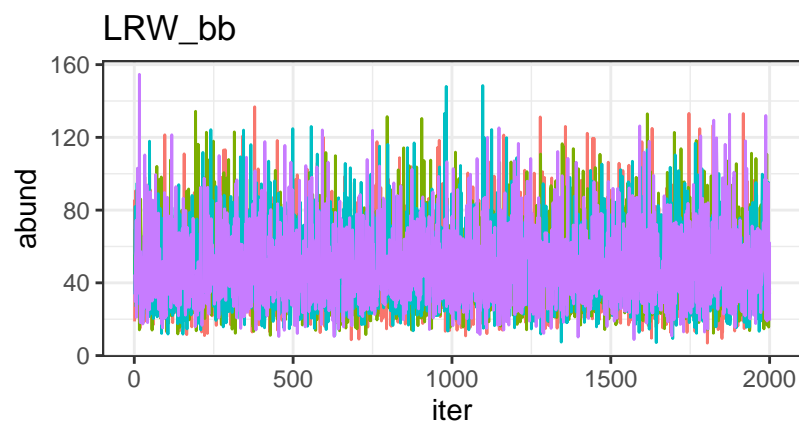
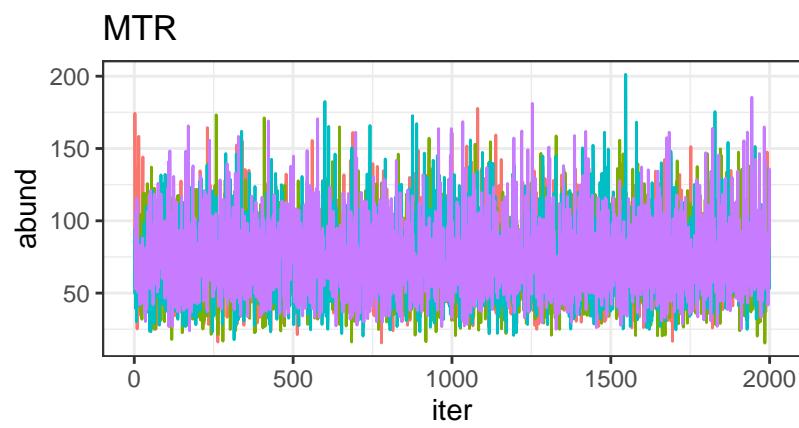
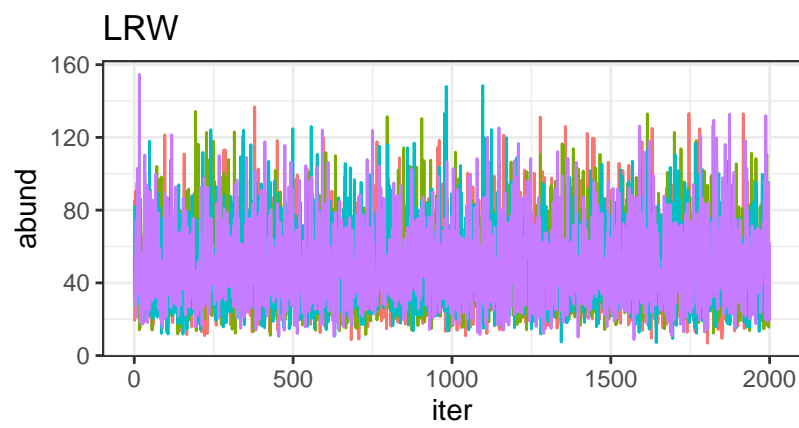
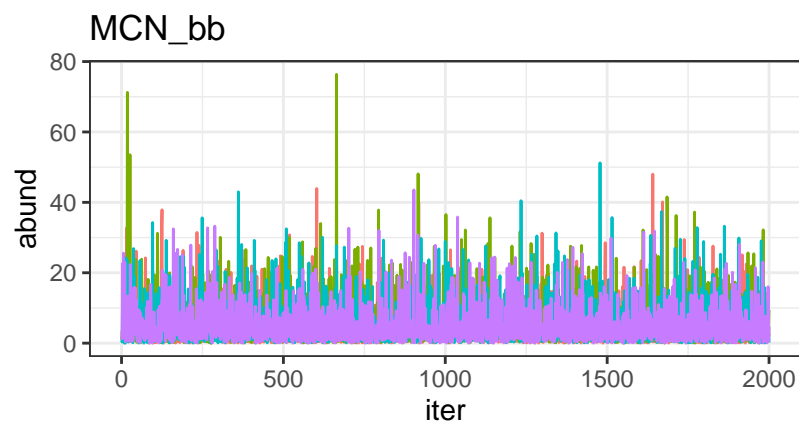
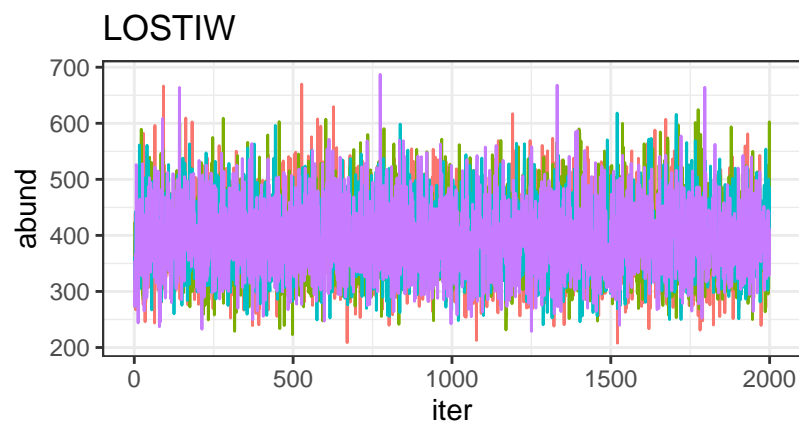
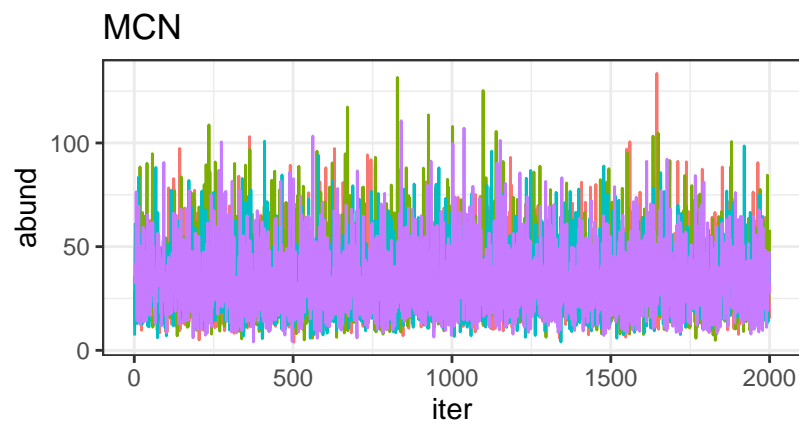
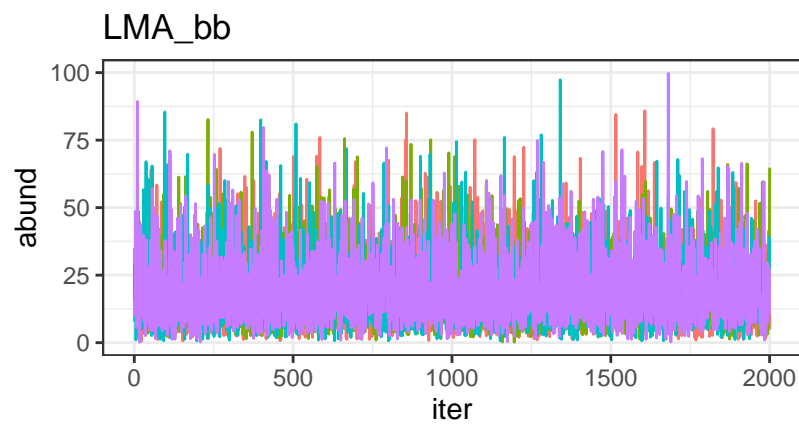


LC1_bb

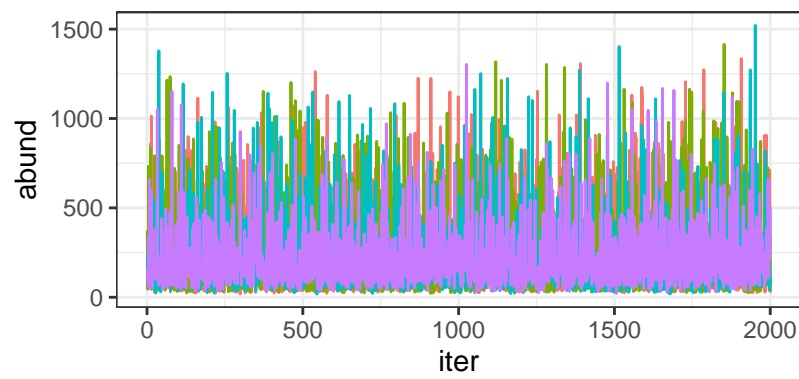


LMA

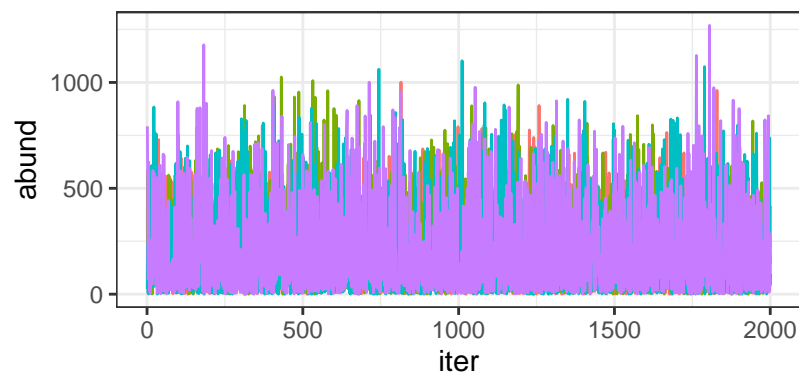




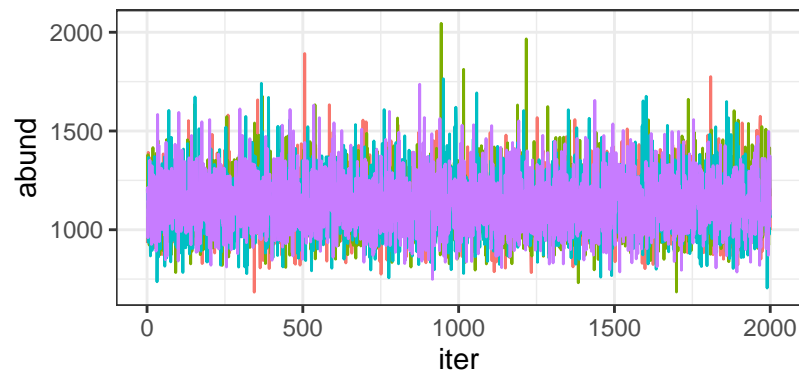
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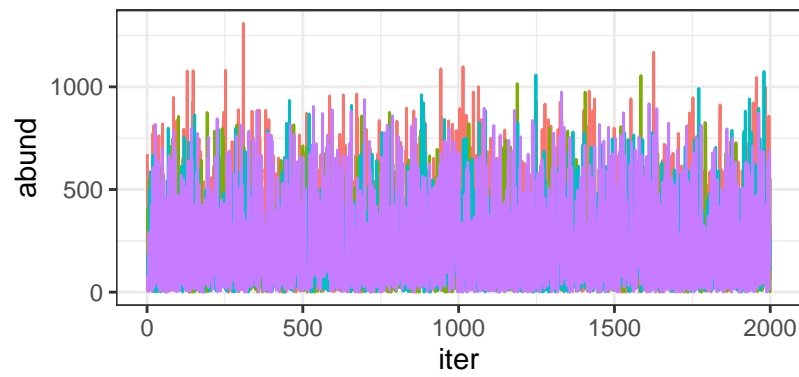
SC3_bb



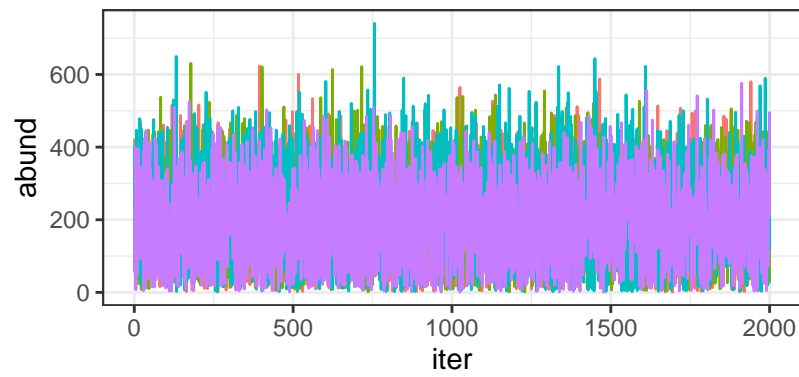
SC1



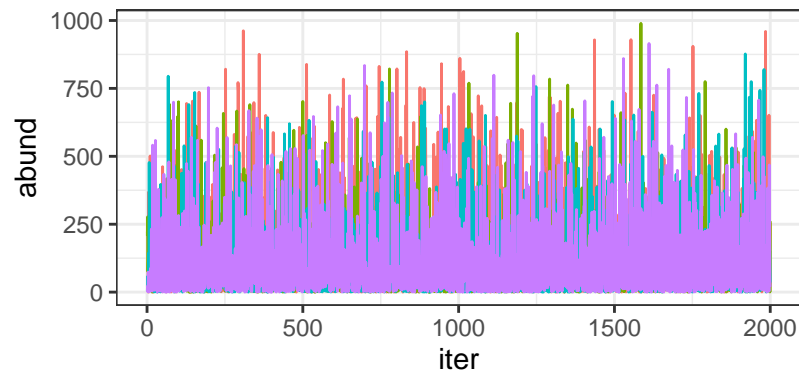
SC4



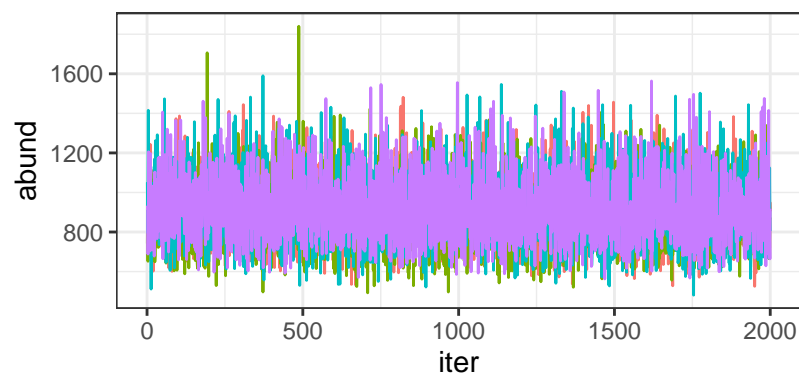
SC1_bb



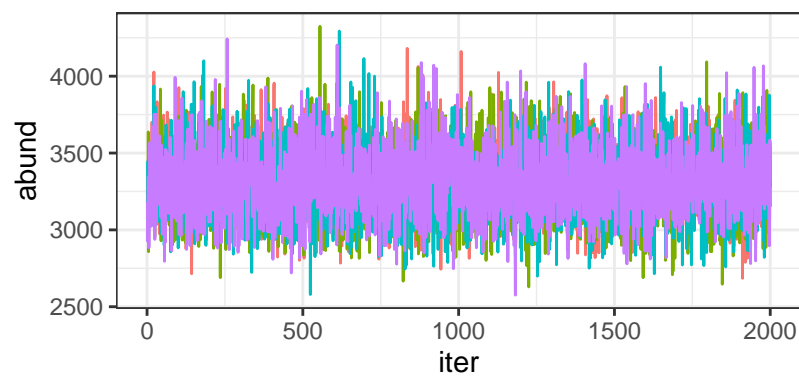
SC4_bb



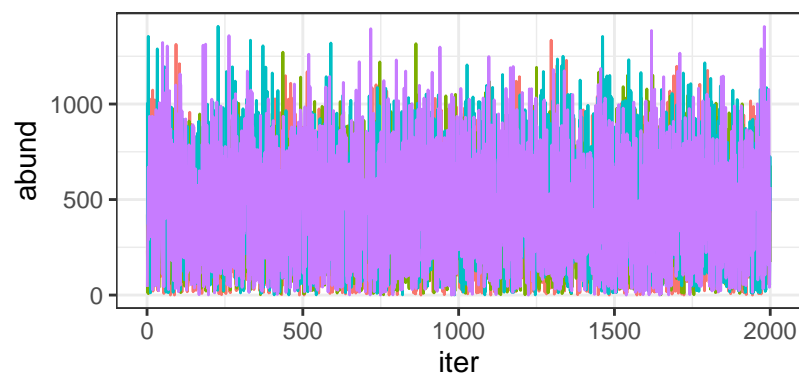
SC2



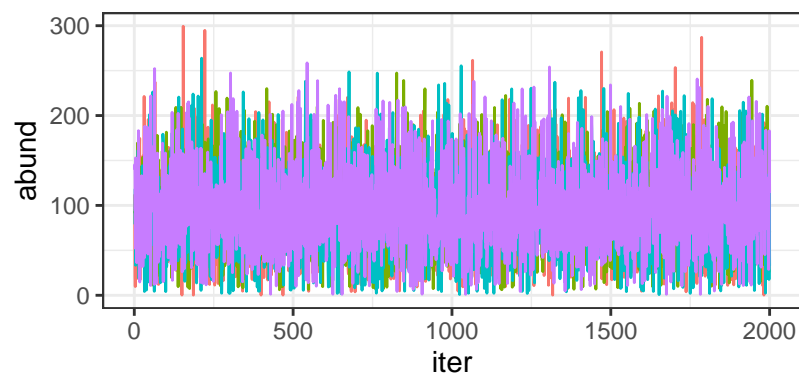
SFG



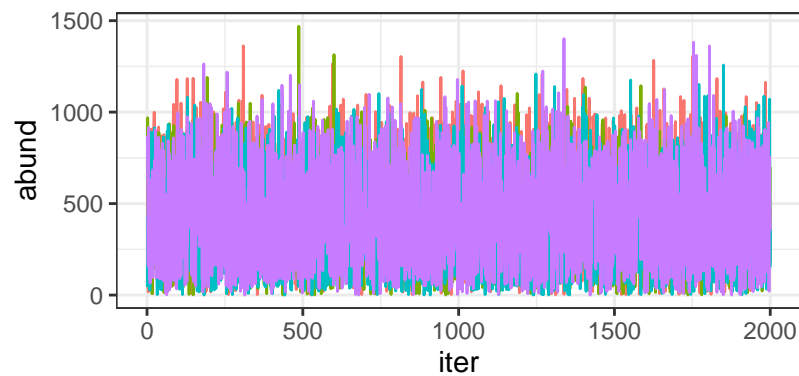
SC2_bb



SFG_bb



SC3



STL

