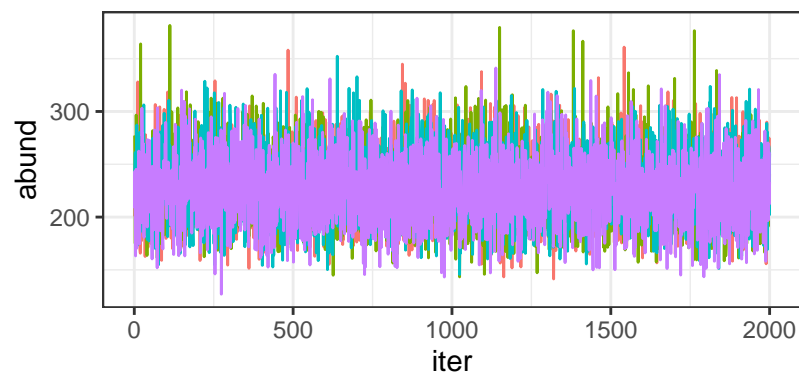
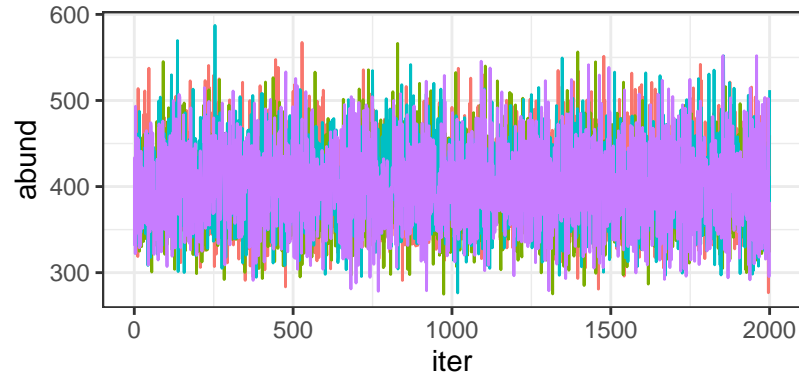


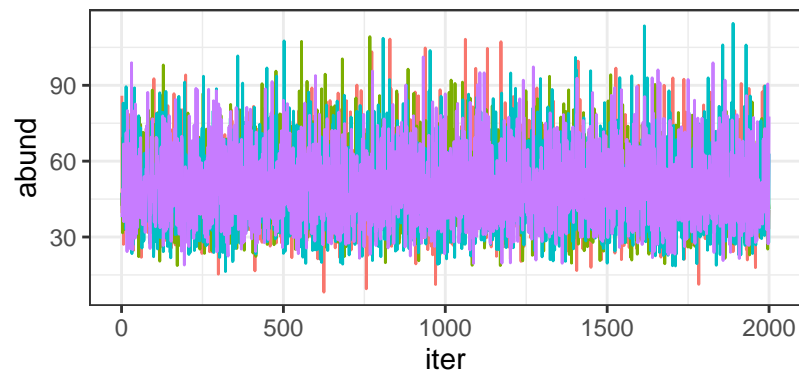
BRC



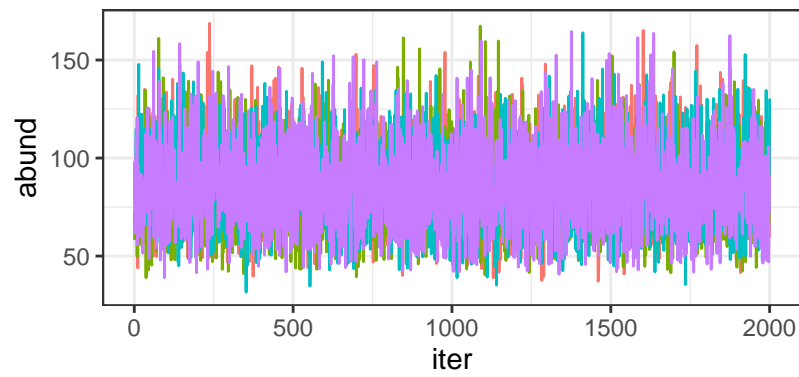
ESS



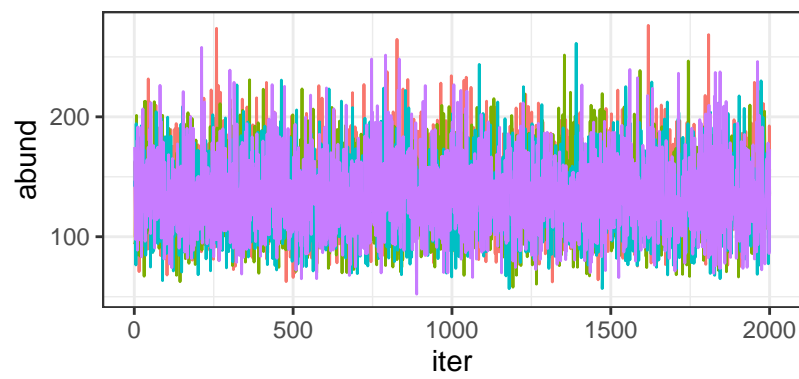
BSC



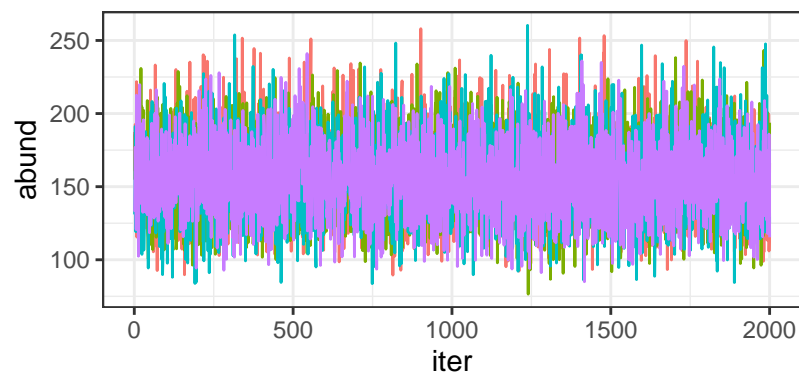
ESS_bb



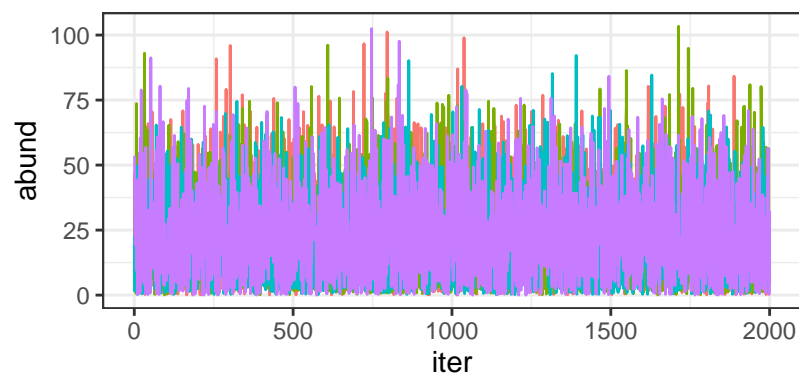
CCU



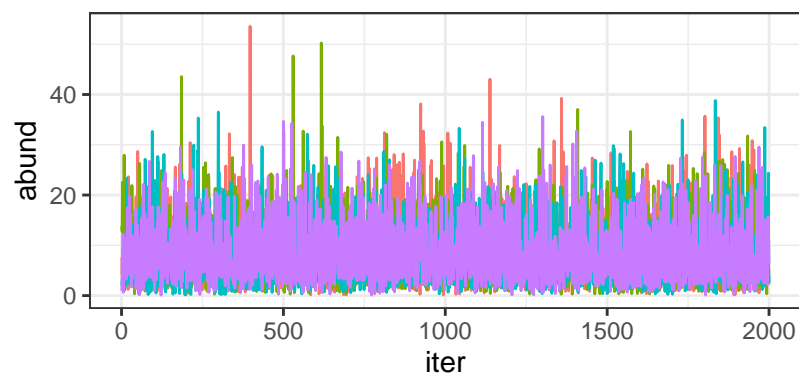
EVL



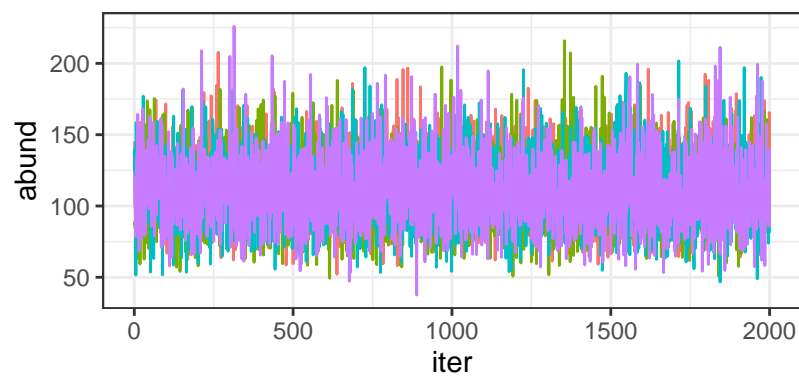
CCU_bb



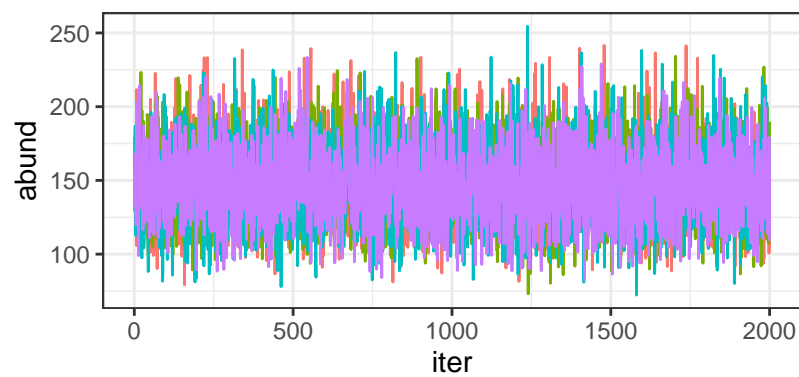
EVL_bb



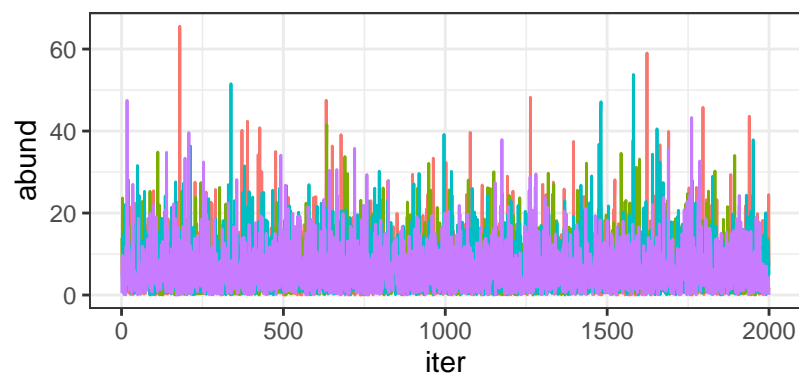
CCW



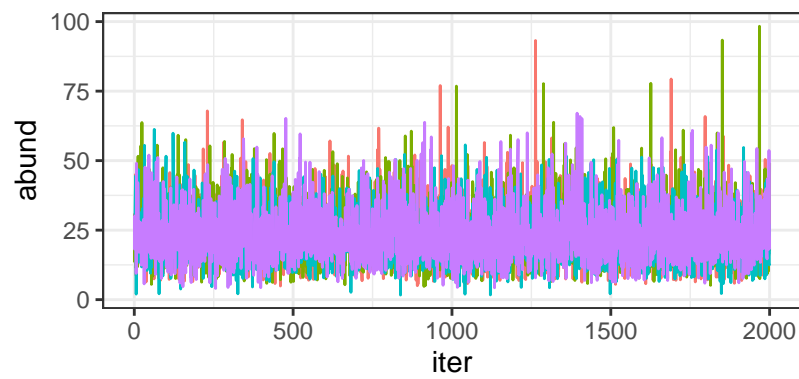
EVL_bb



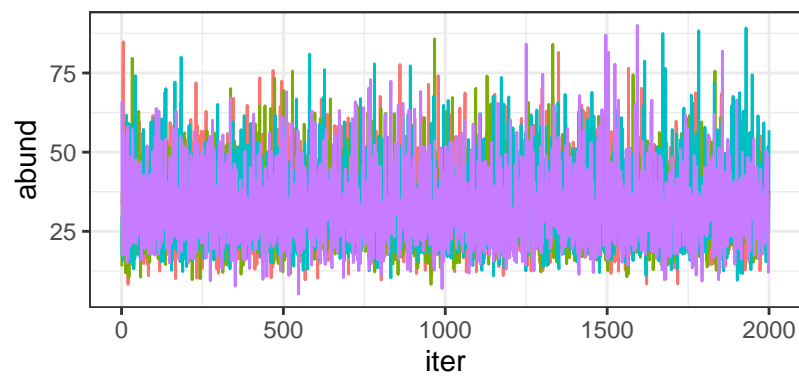
CRA



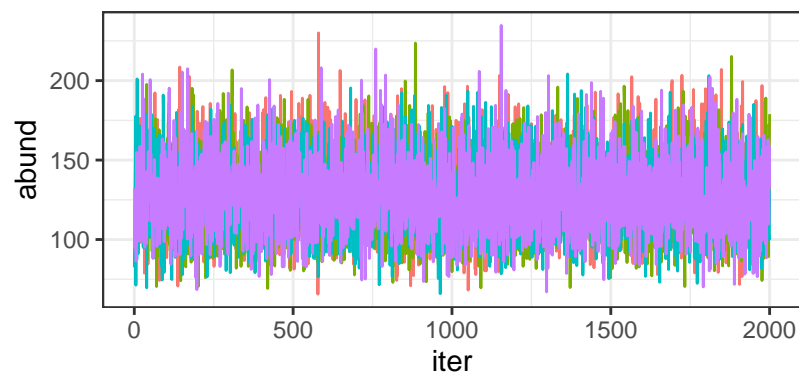
EVL_bb



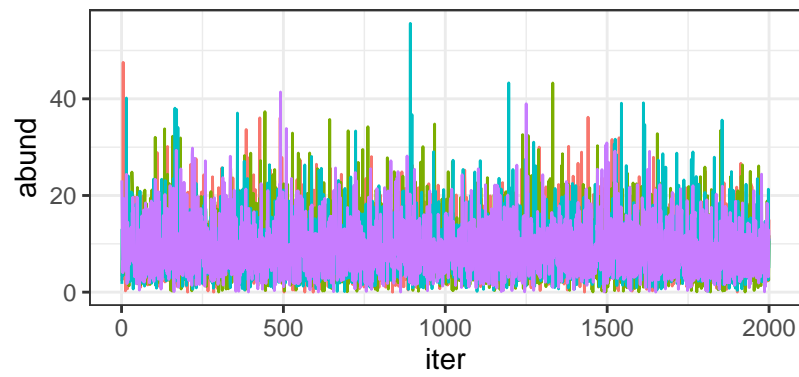
GOA



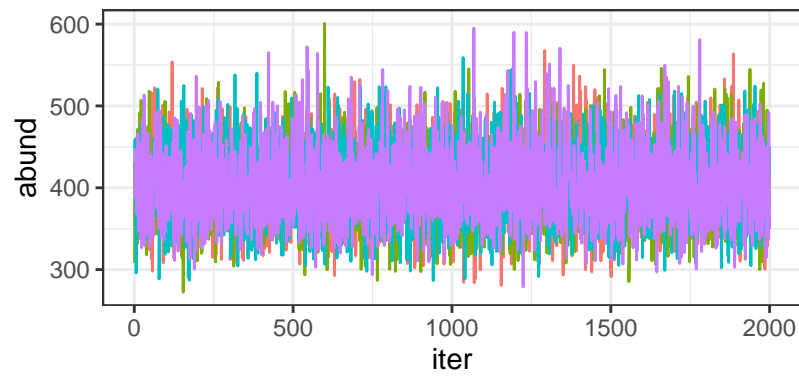
IML_bb



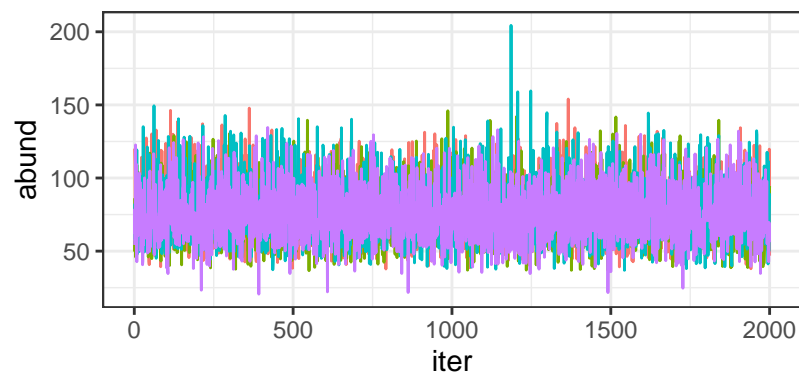
GOA_bb



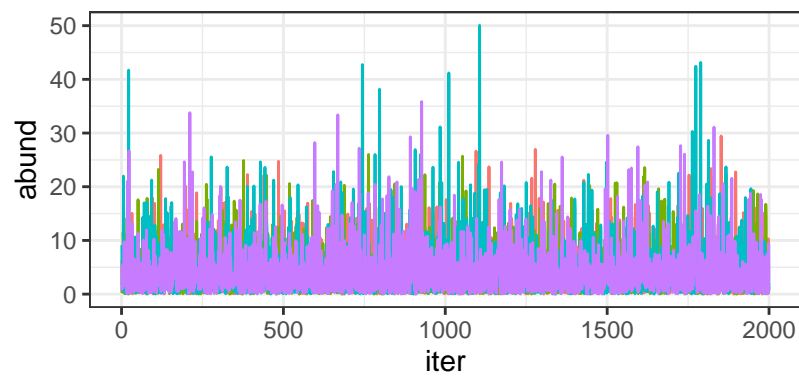
IR1



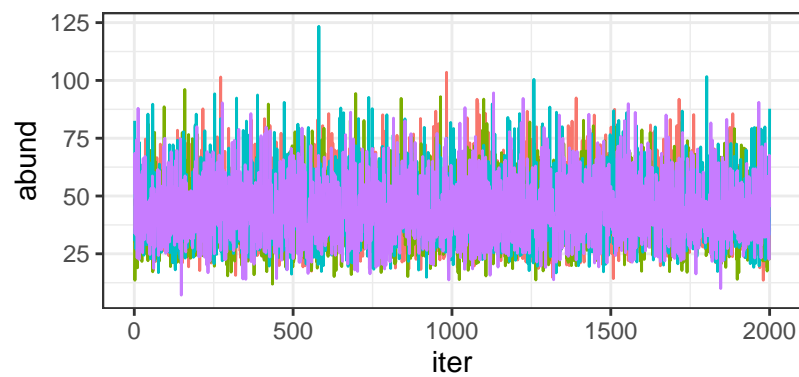
GRS



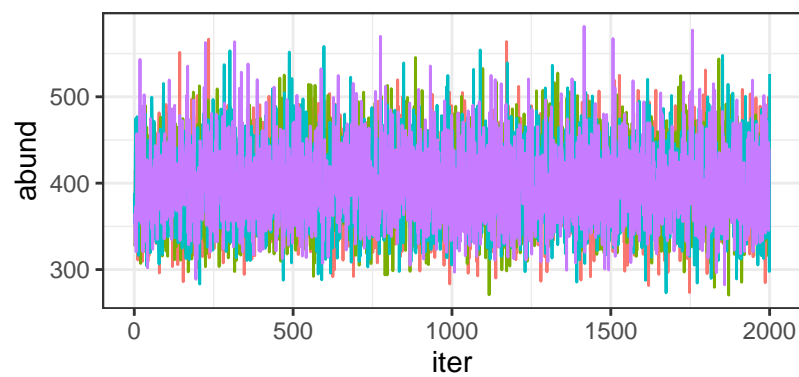
IR1_bb



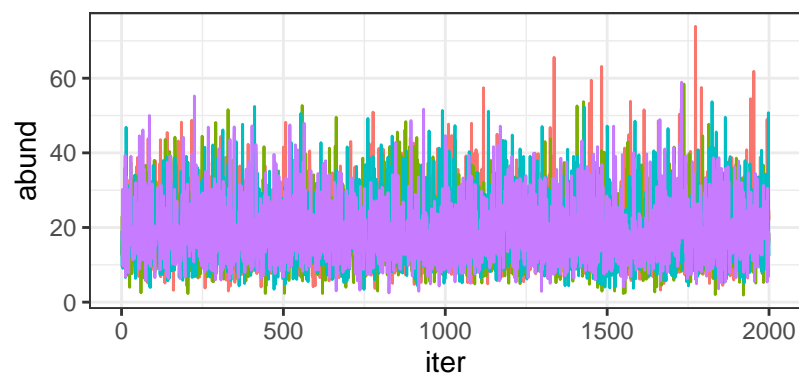
GRS_bb



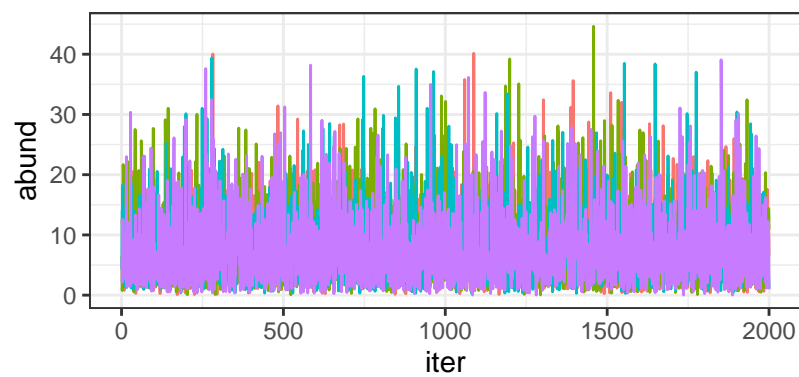
IR2



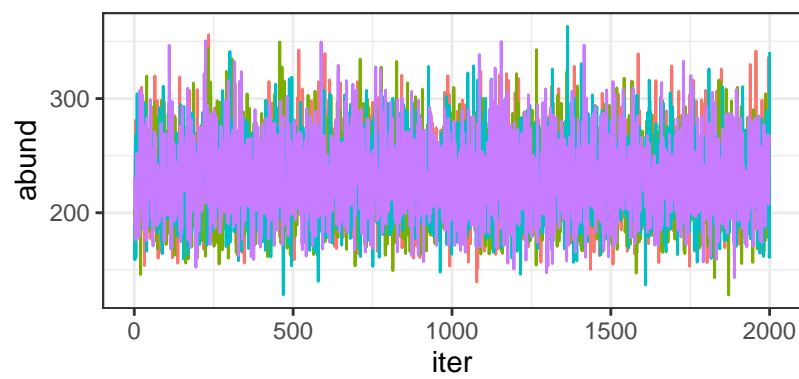
HYC



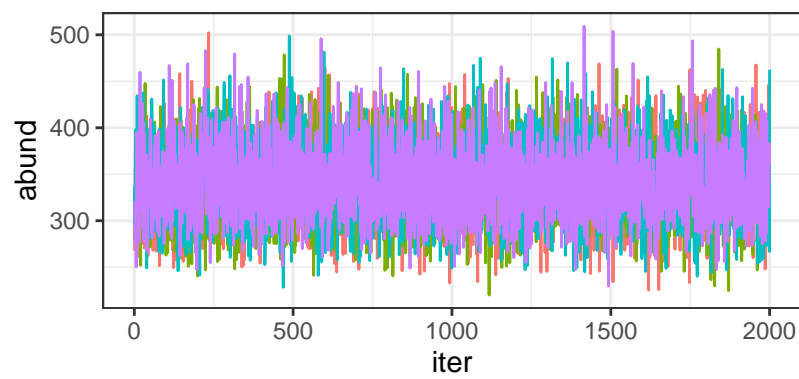
IR2_bb



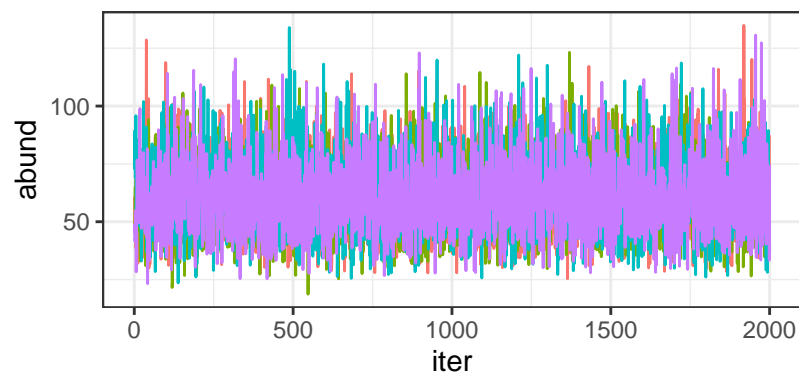
IML



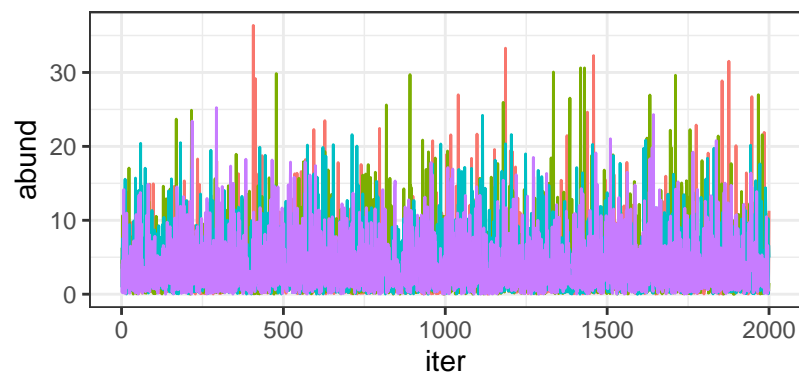
IR3



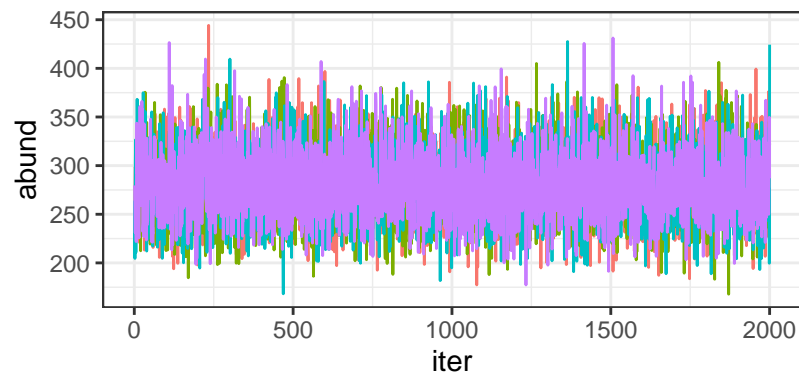
IR3_bb



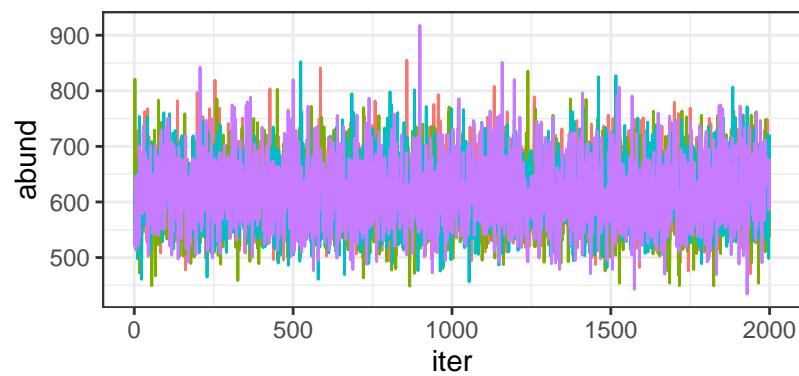
KEN_bb



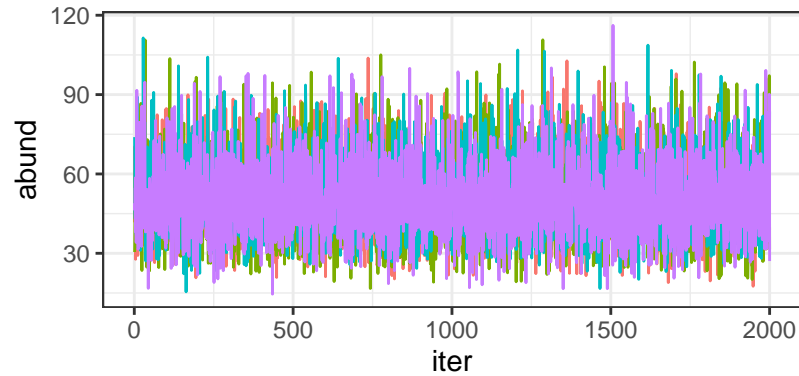
IR4



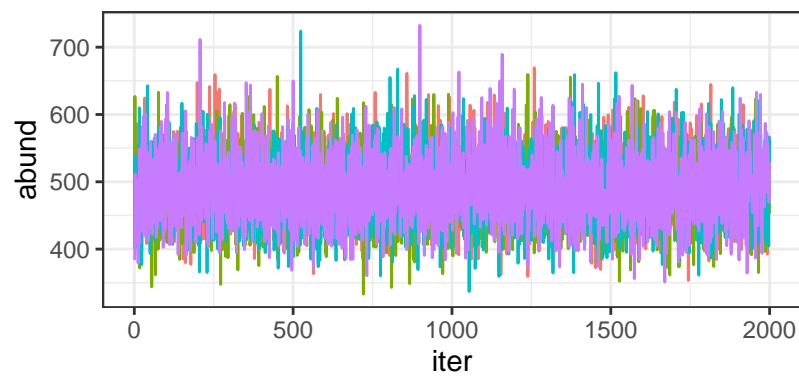
KRS



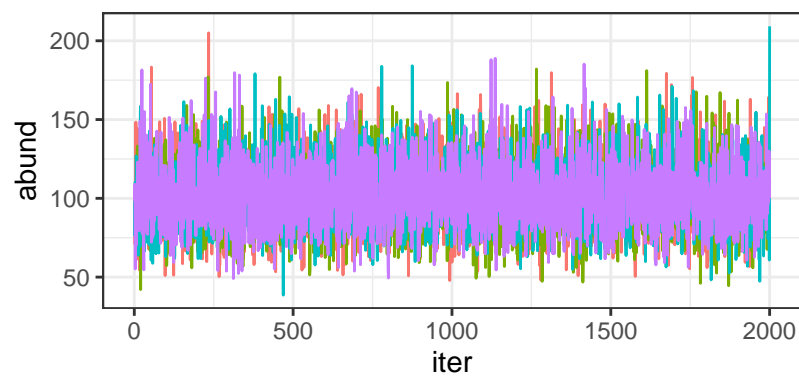
IR4_bb



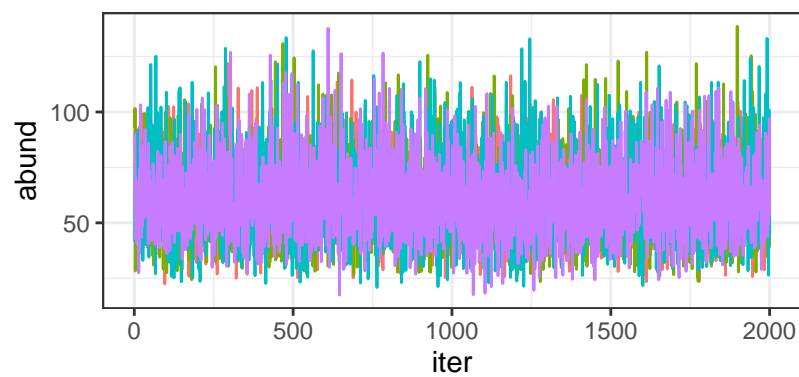
KRS_bb



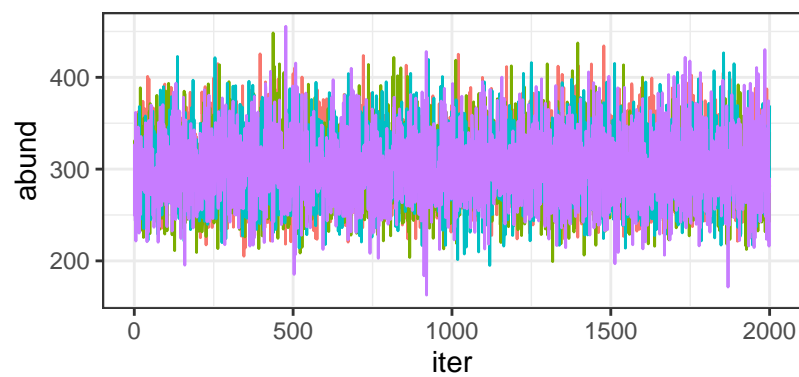
IR5



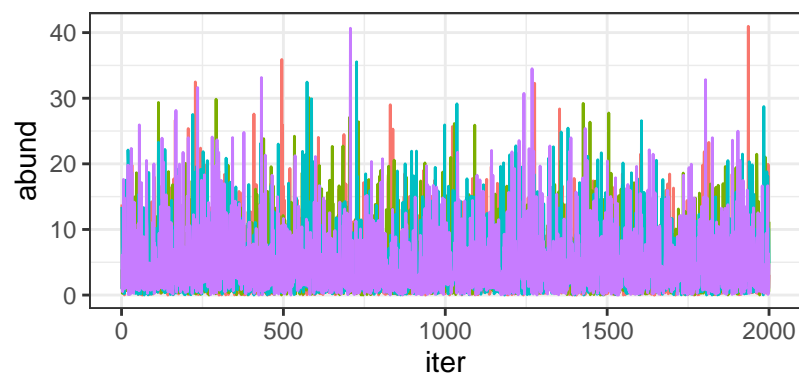
LC1



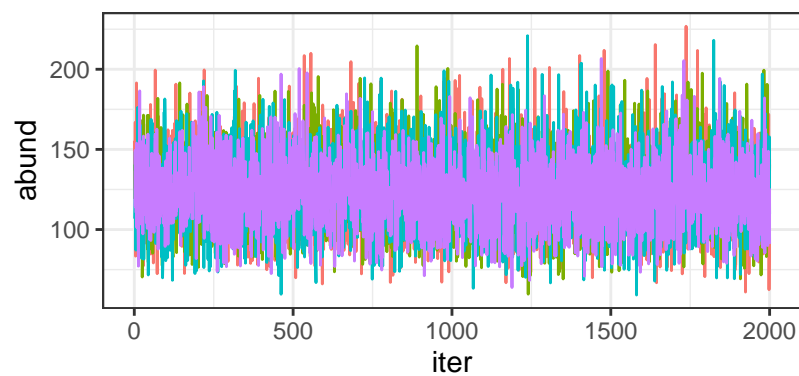
JOHNSC



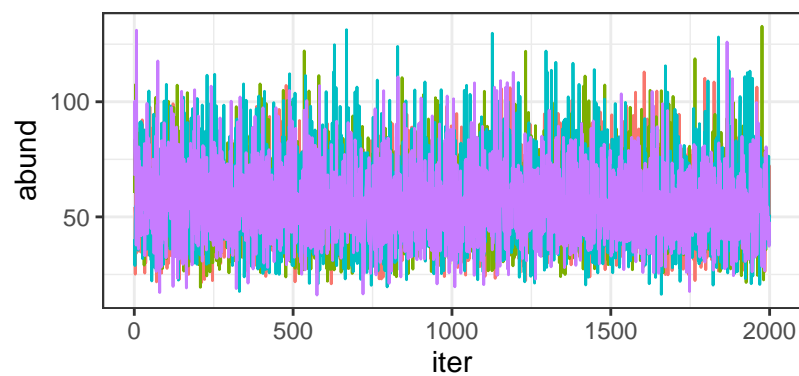
LC1_bb



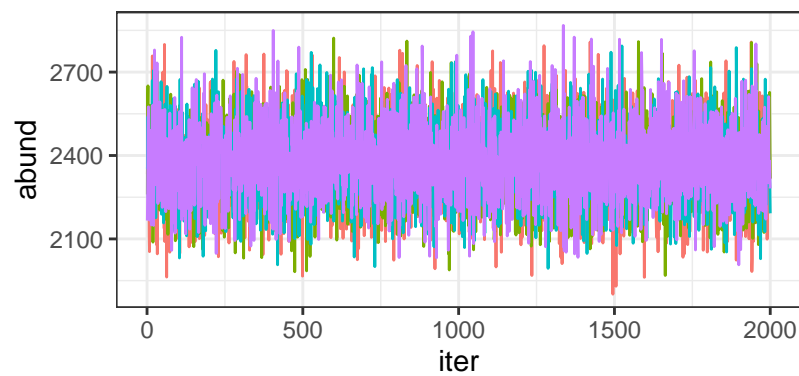
KEN



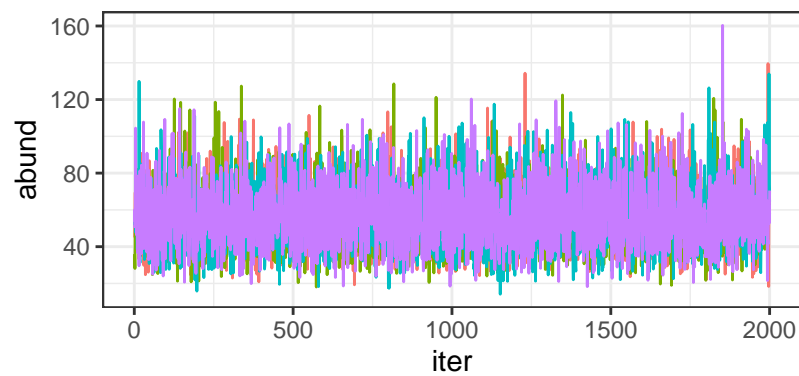
LC2



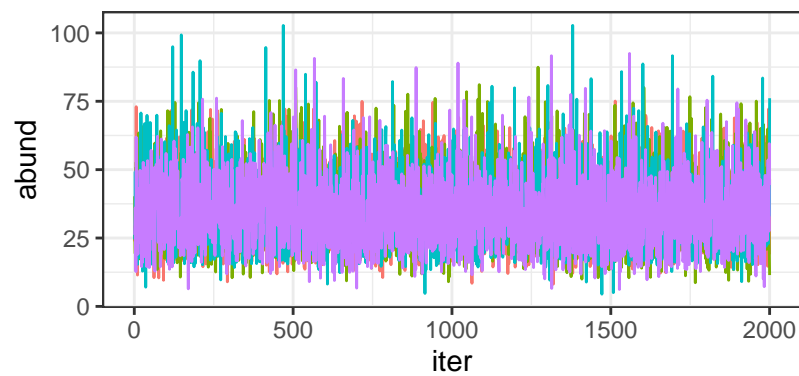
LGR_bb



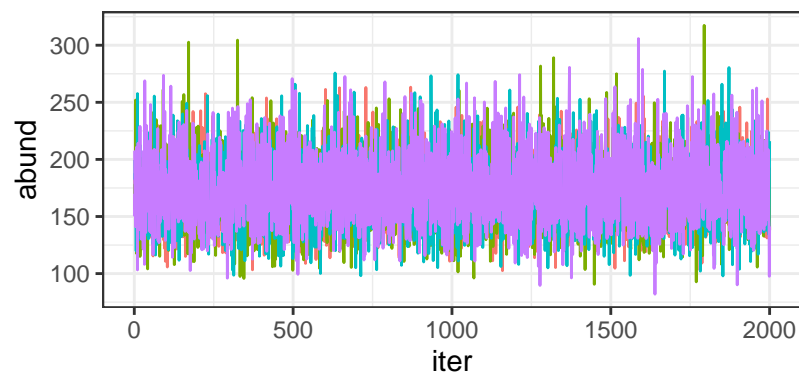
LRL_bb



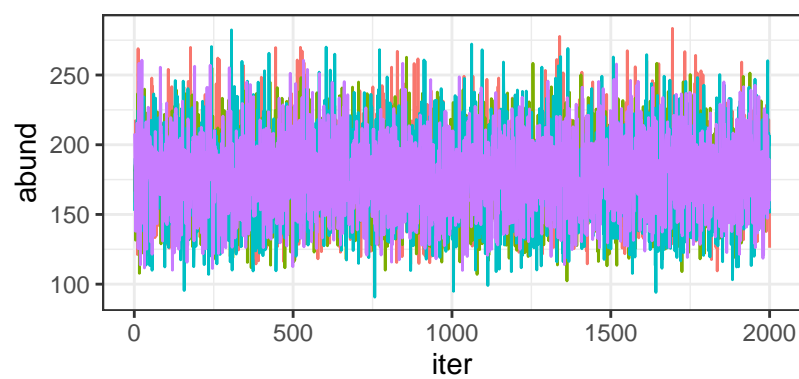
LGW



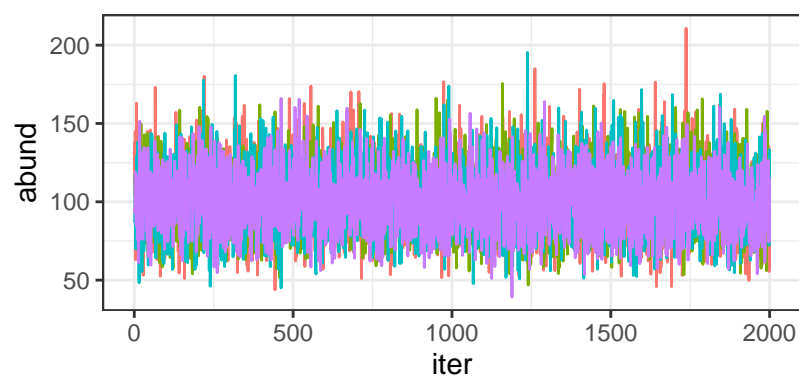
LRU



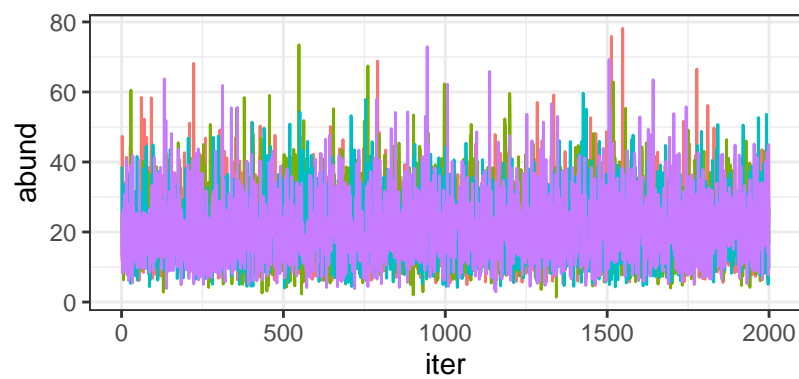
LLR



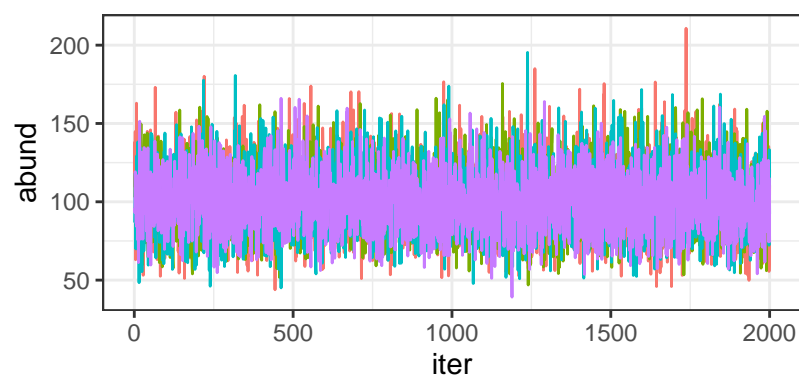
LRW



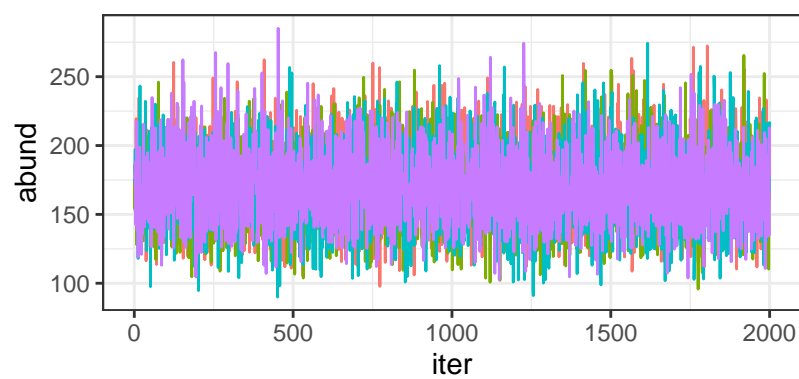
LLR_bb



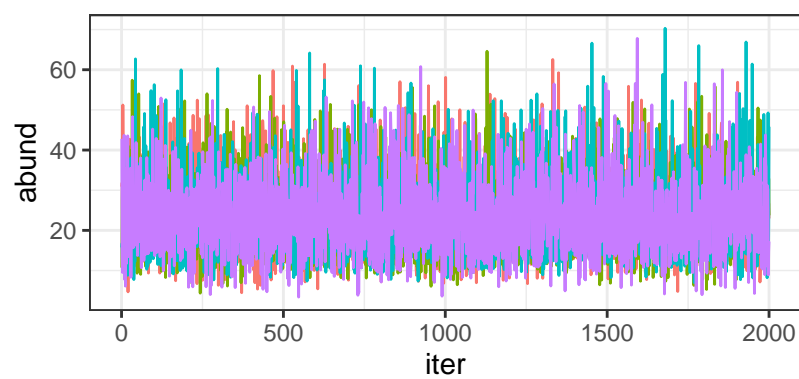
LRW_bb



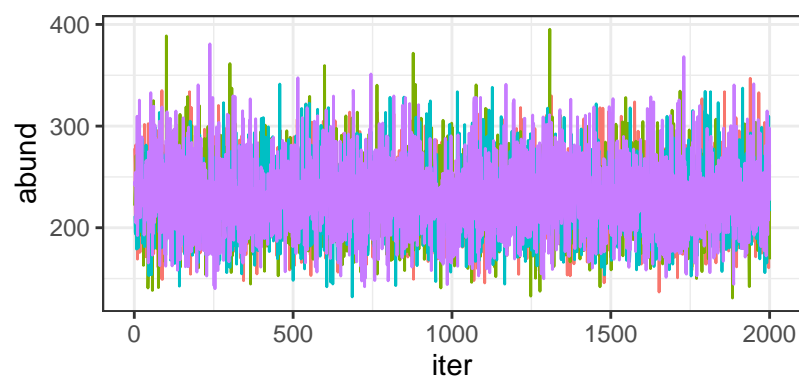
LOSTIW



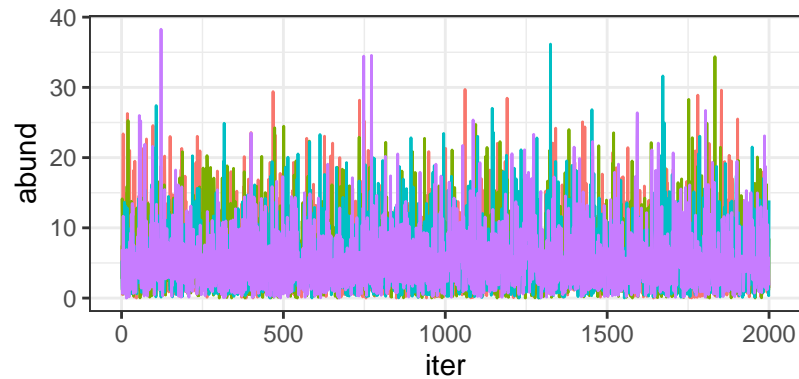
LTR



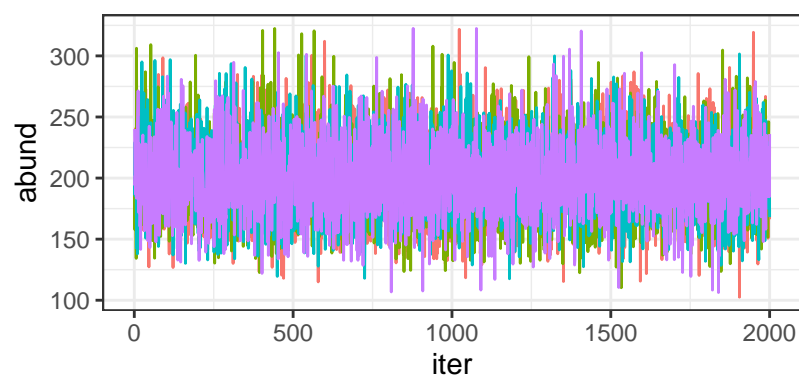
LRL



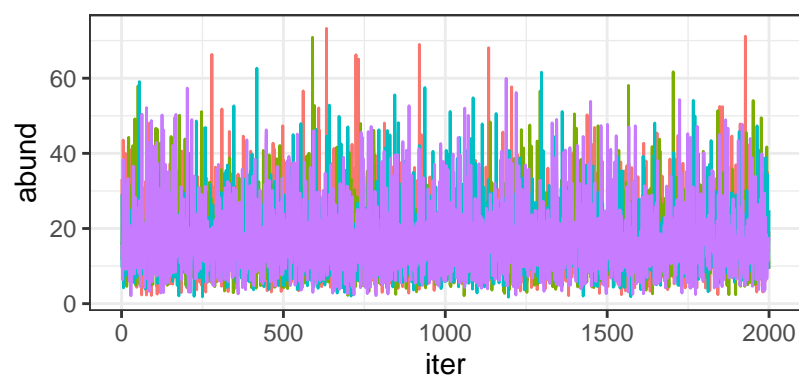
LTR_bb



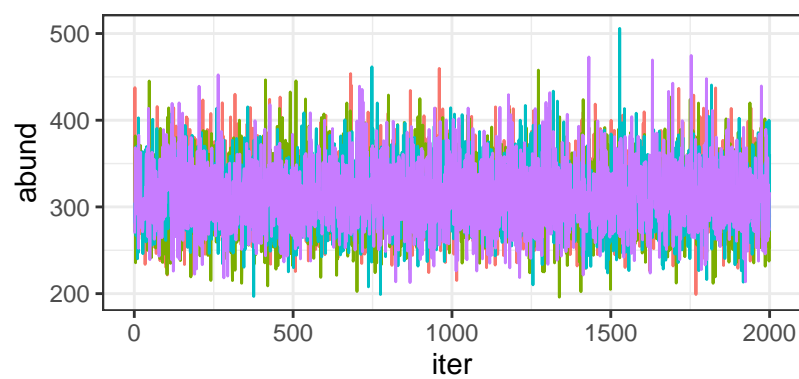
MAR



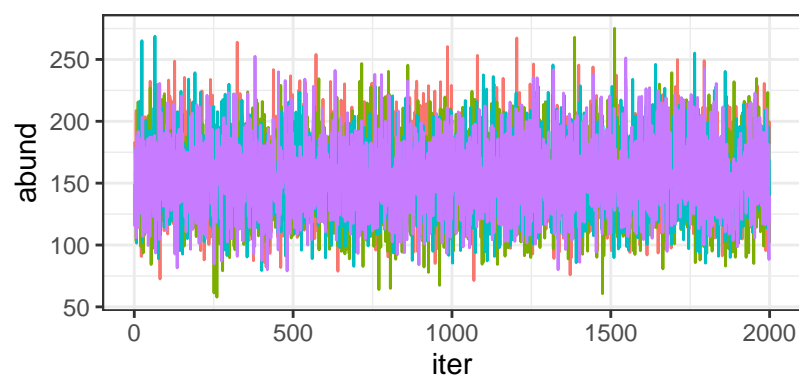
RFL



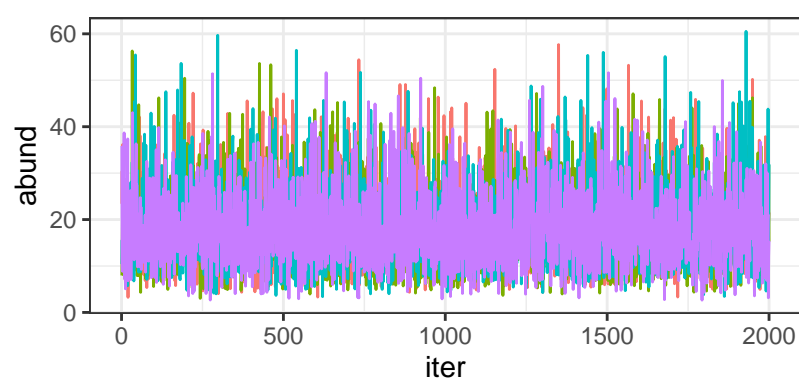
MR1



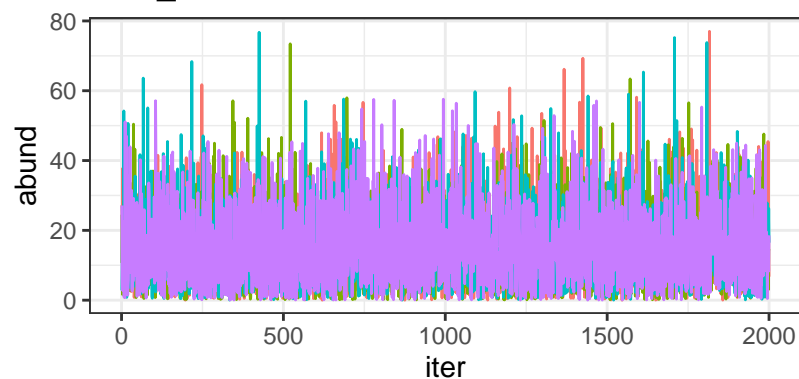
SC1



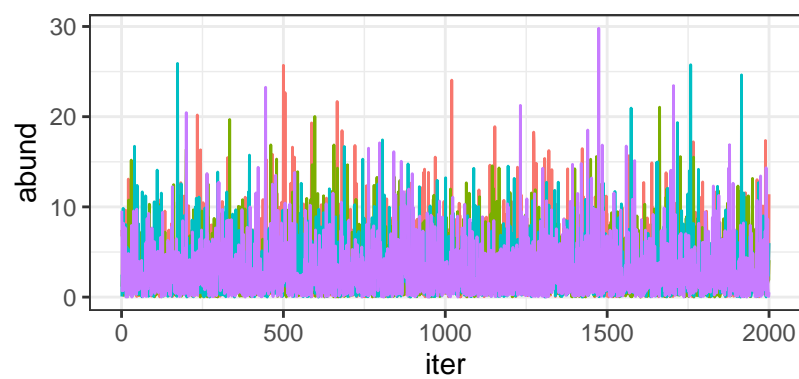
MTR



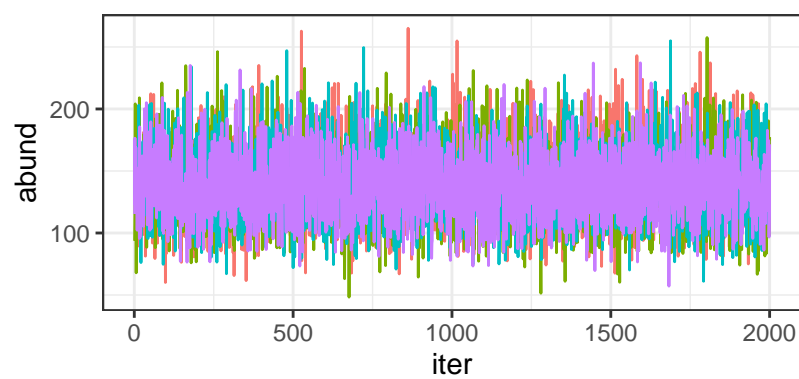
SC1_bb



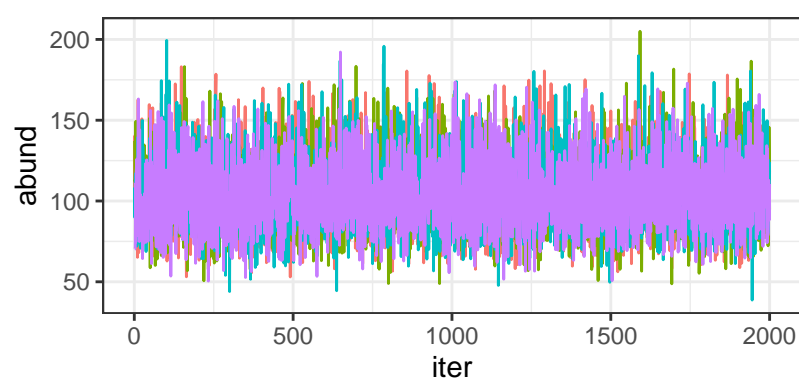
MTR_bb



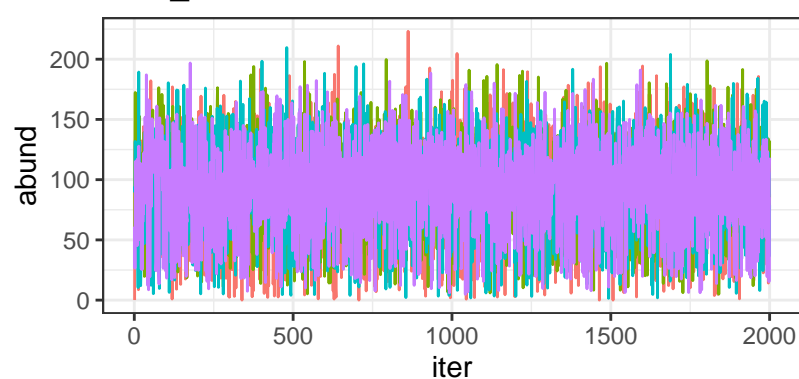
SC2



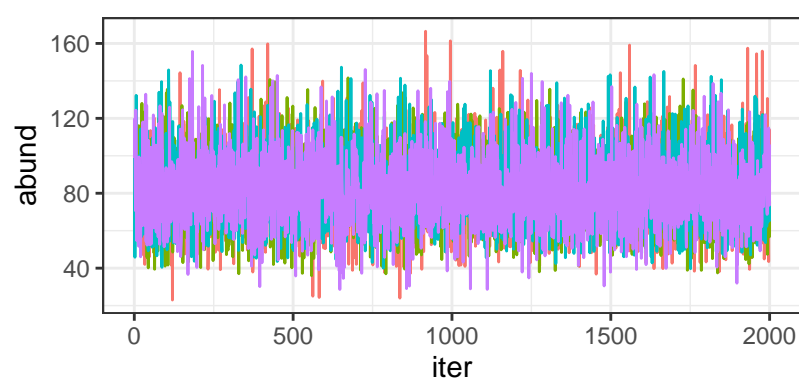
PAHH



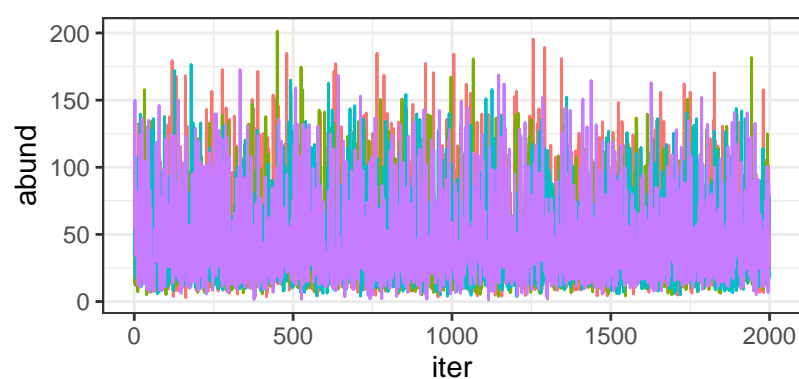
SC2_bb



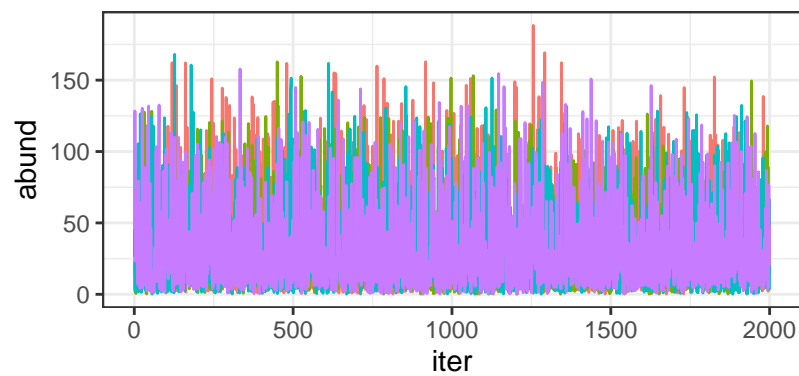
PCA



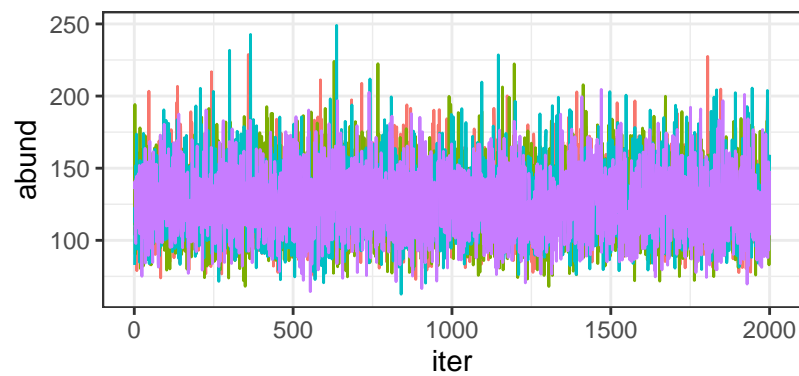
SC3



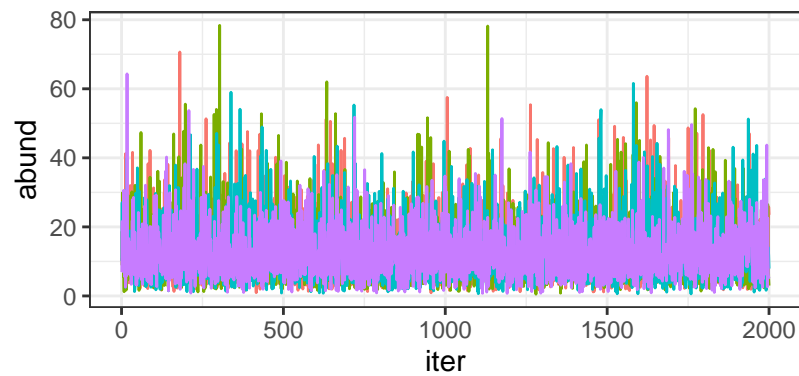
SC3_bb



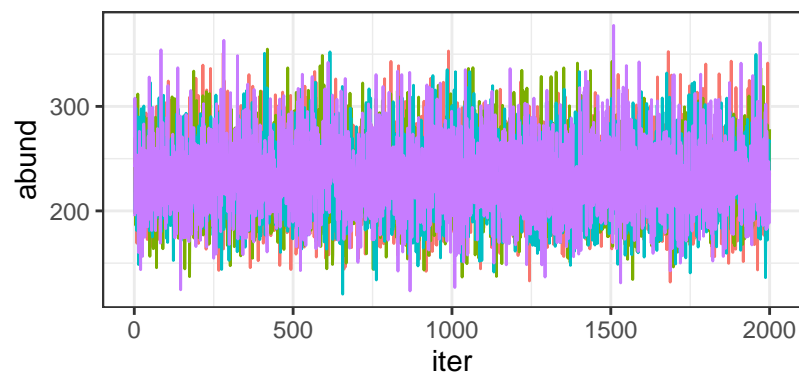
STR



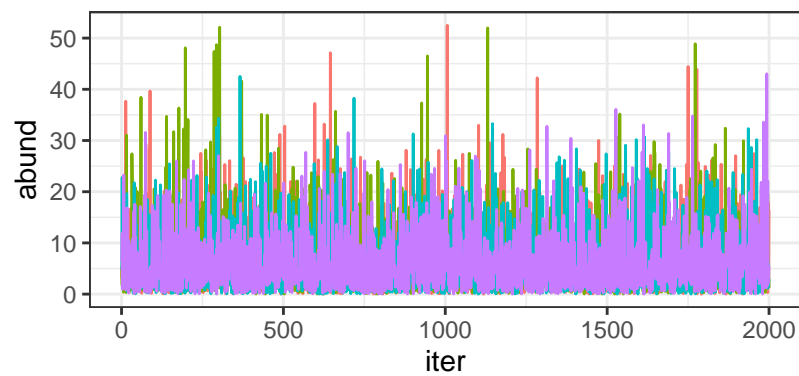
SC4



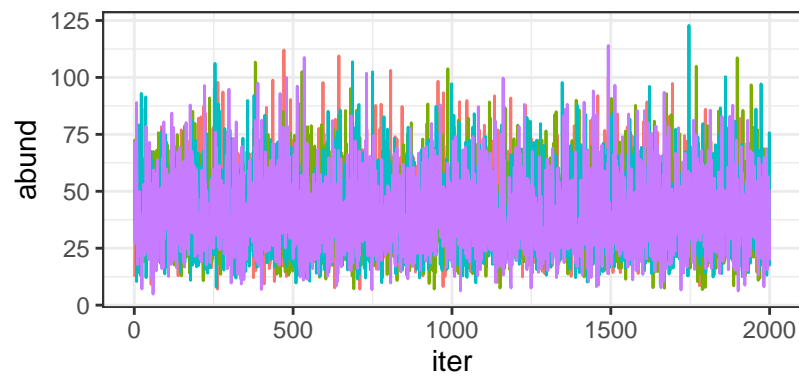
SW1



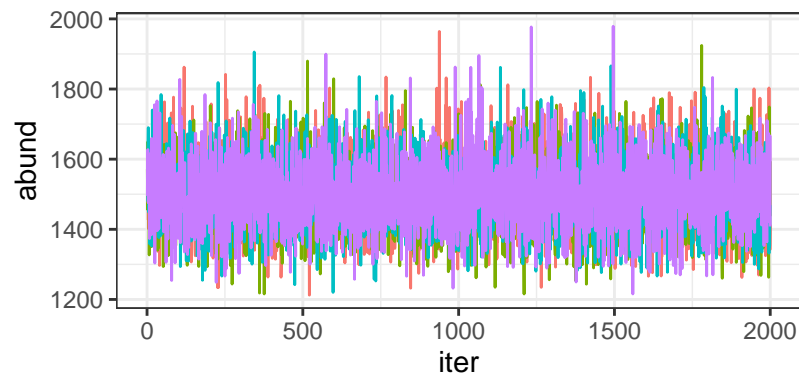
SC4_bb



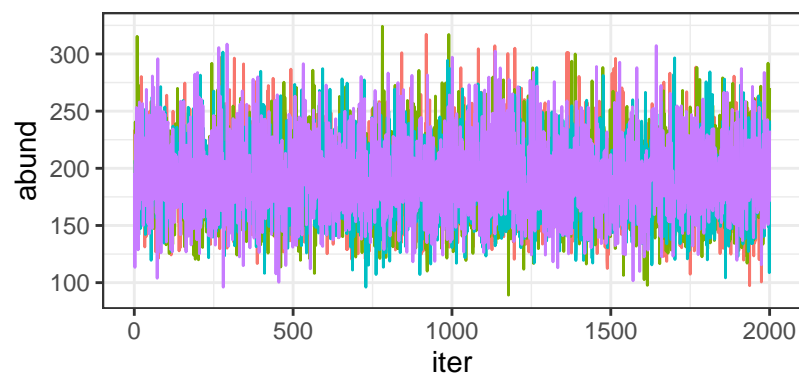
SW1_bb



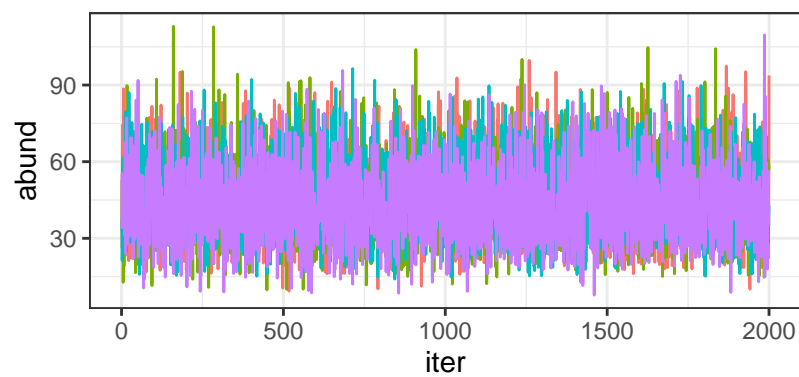
SFG



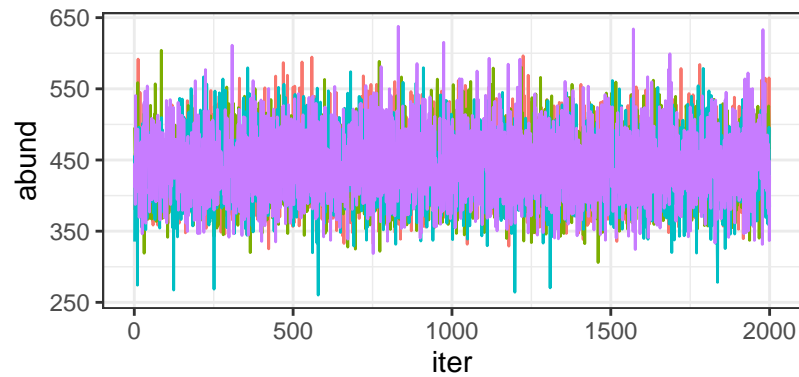
SW2



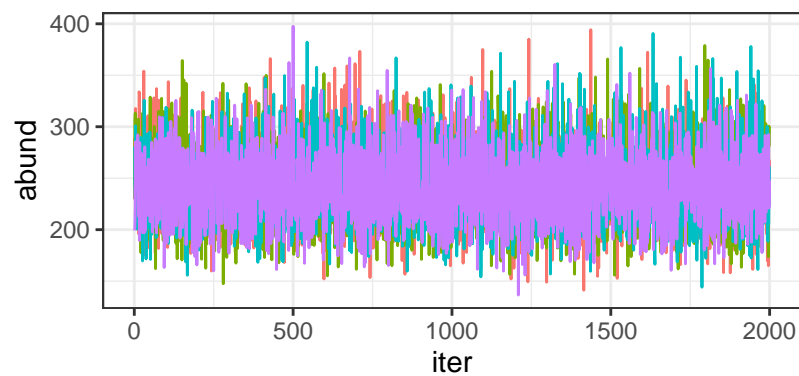
SFG_bb



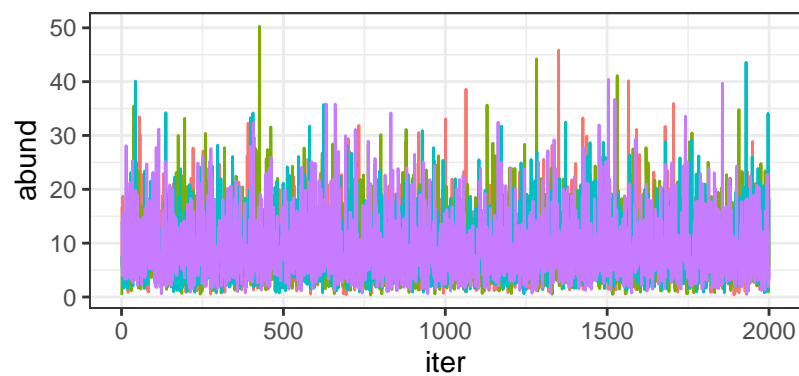
TAY



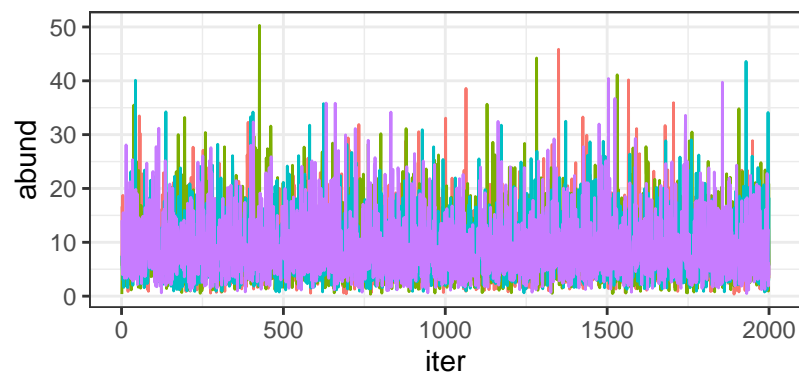
STL



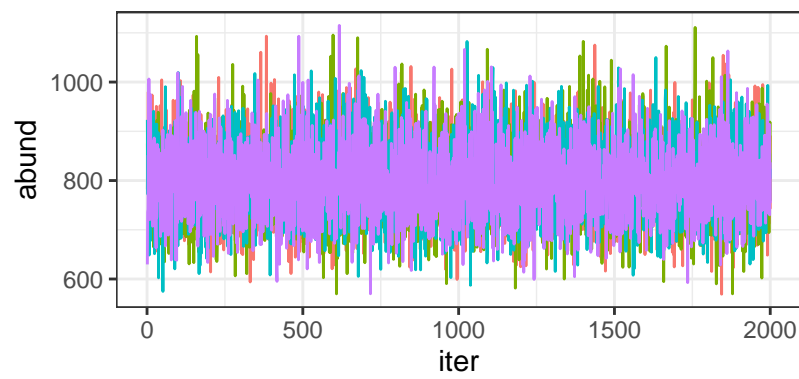
TFH



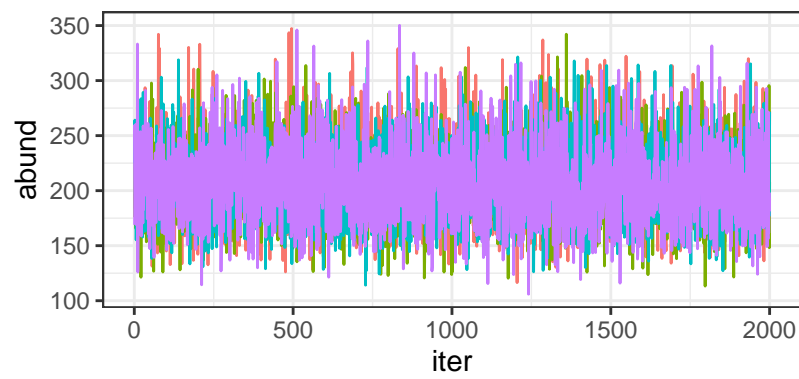
TFH_bb



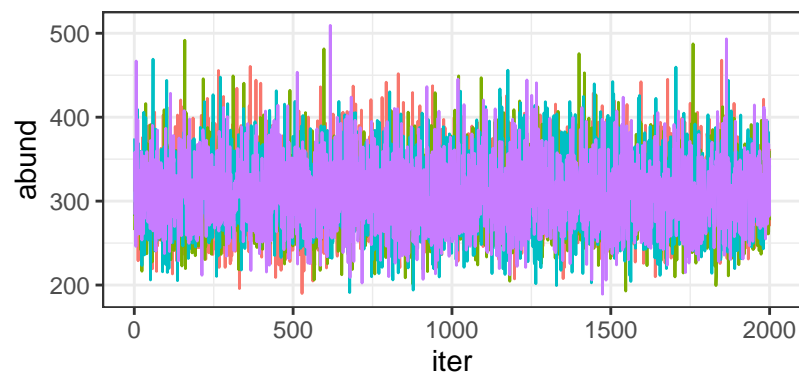
USI



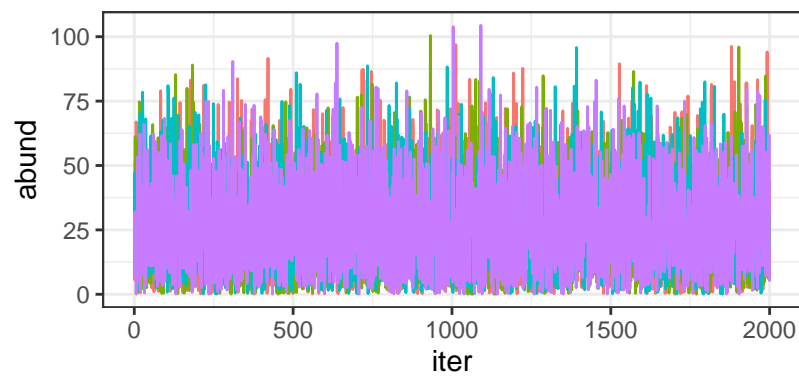
UGR



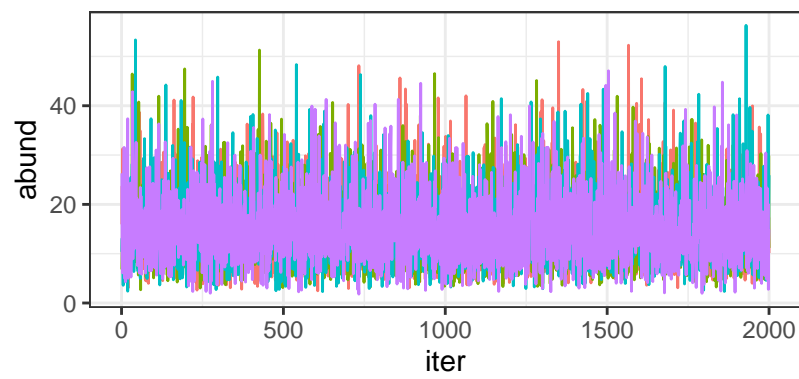
USI_bb



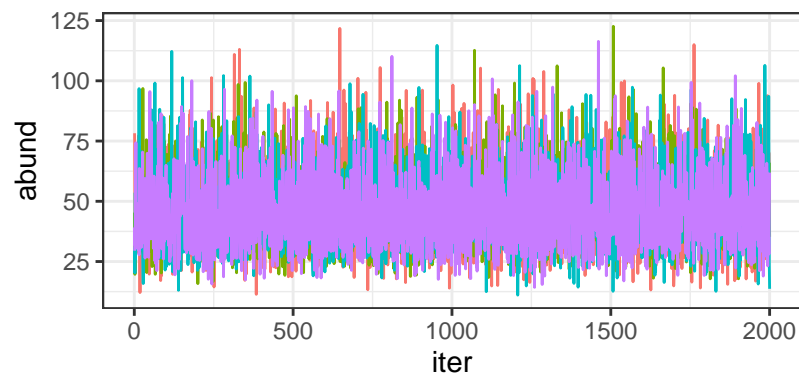
UGR_bb



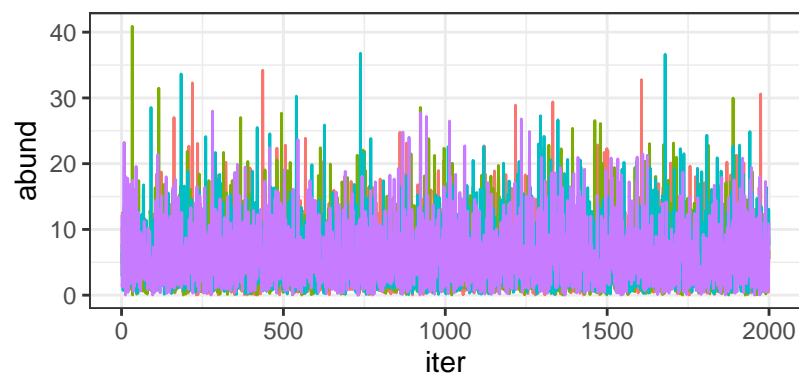
UTR



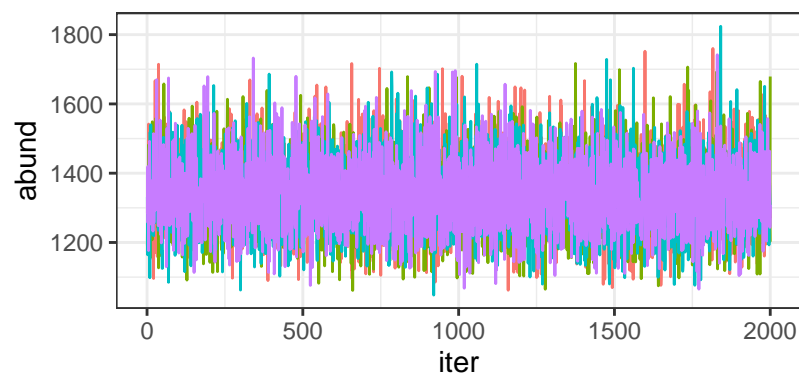
UGS



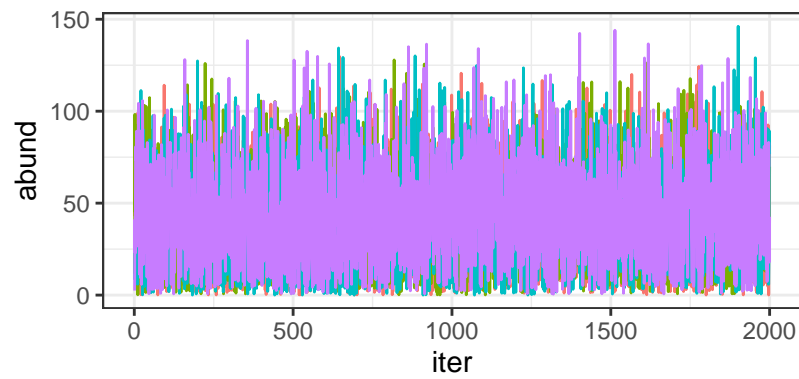
UTR_bb



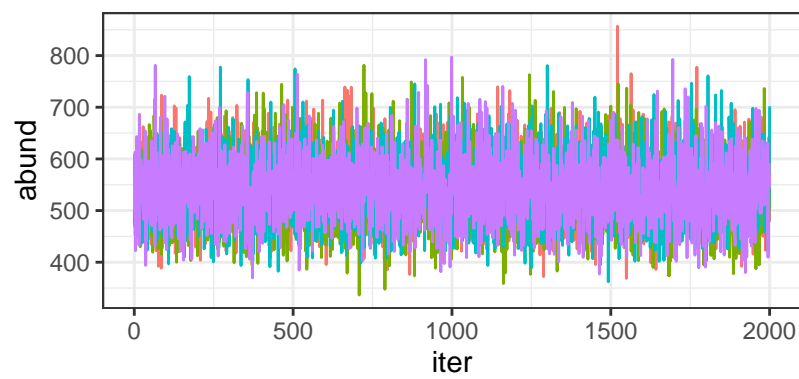
USE



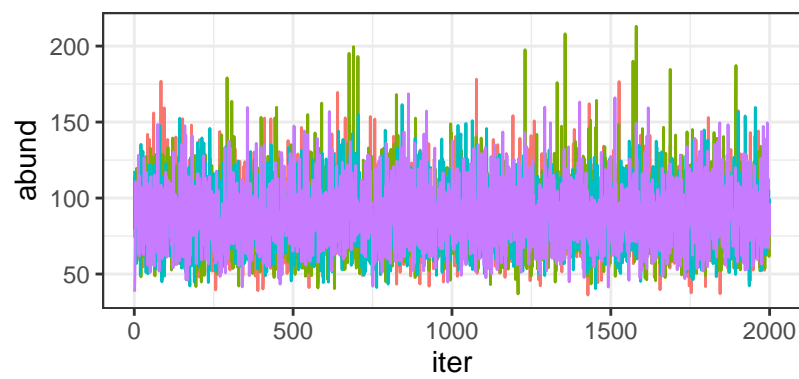
VC1



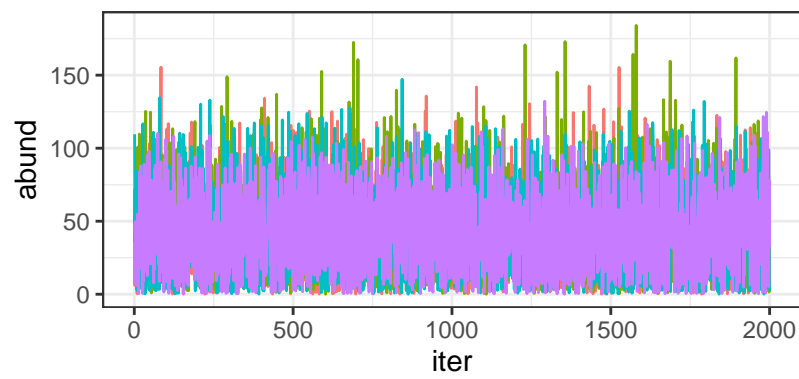
USE_bb



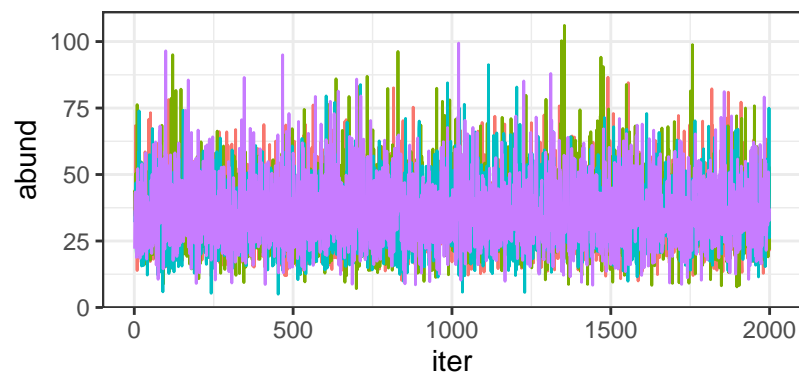
VC2



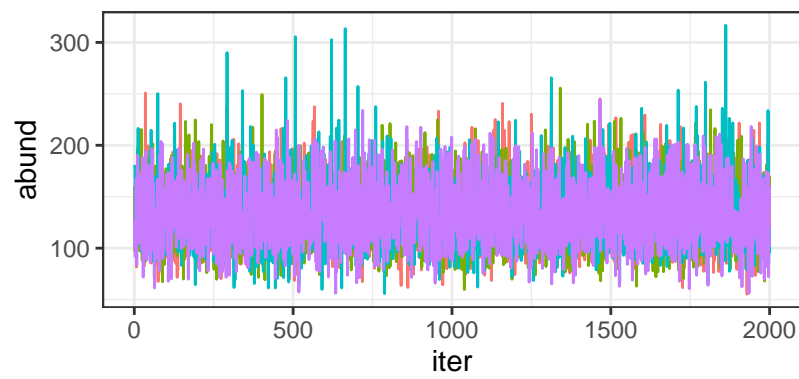
VC2_bb



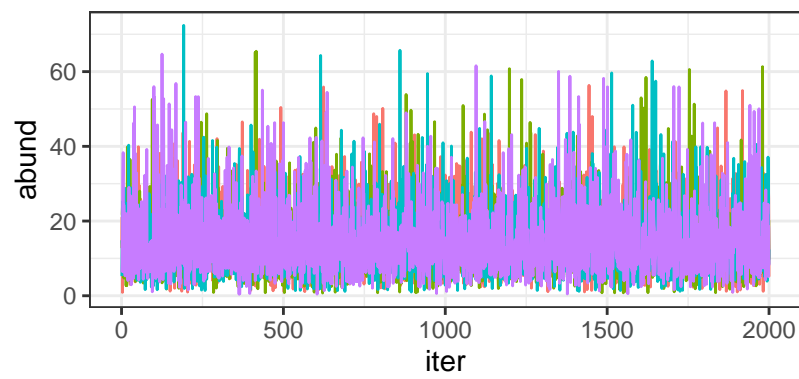
YFK



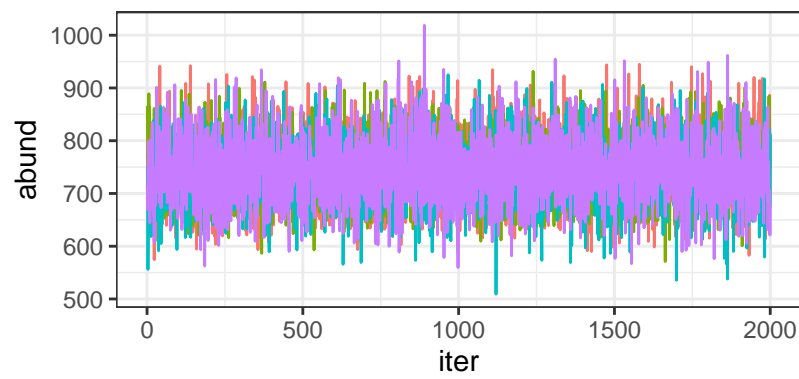
WEN



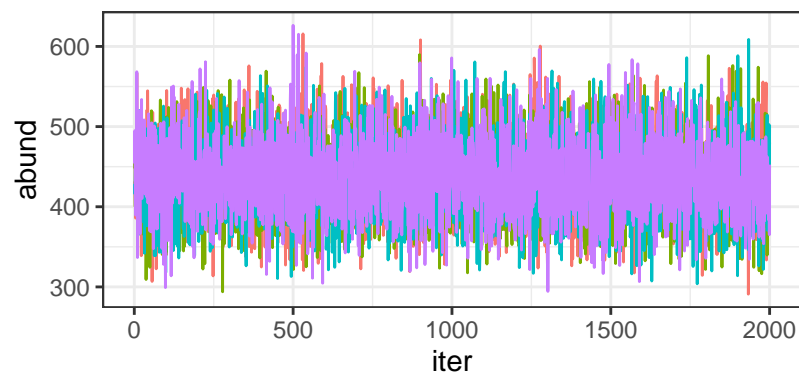
YPP



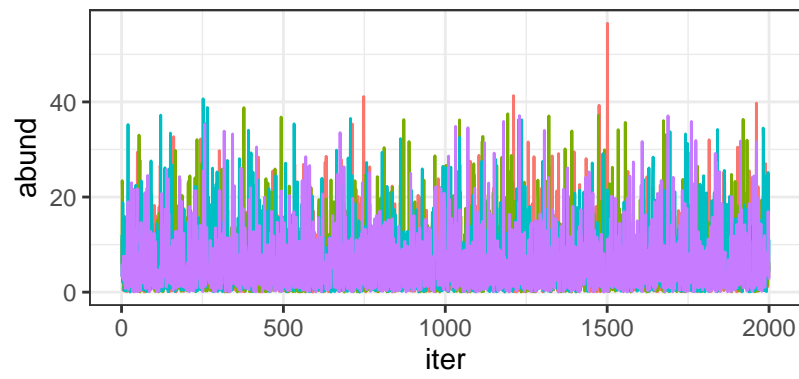
WR1



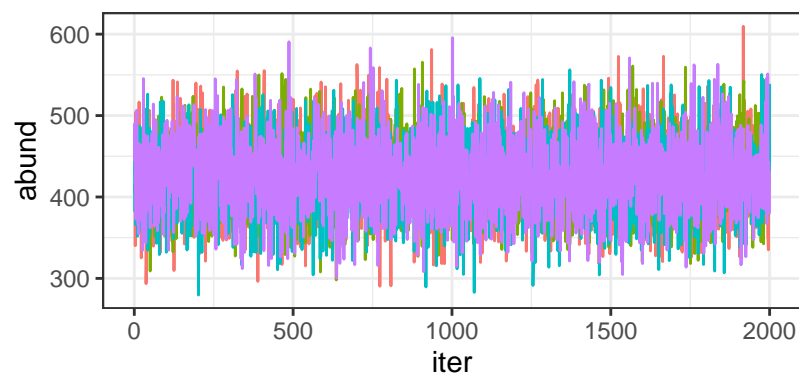
ZEN



WR1_bb



WR2



WR2_bb

