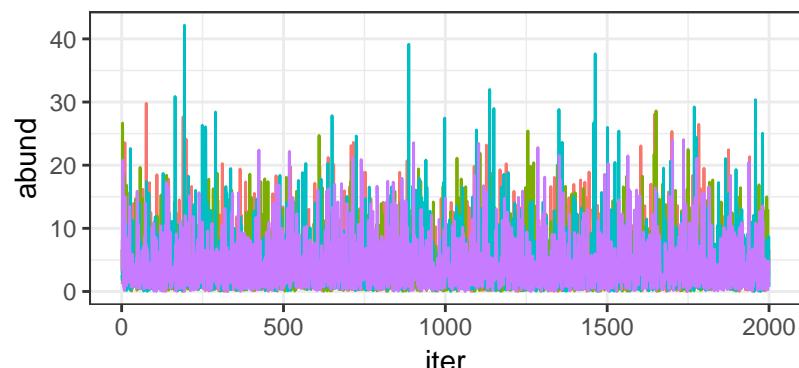
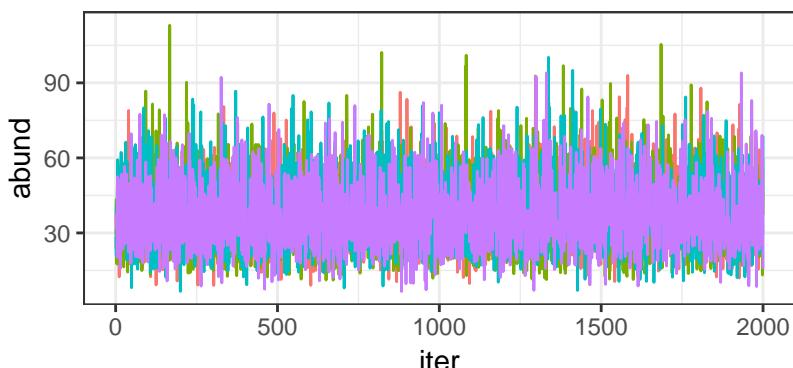


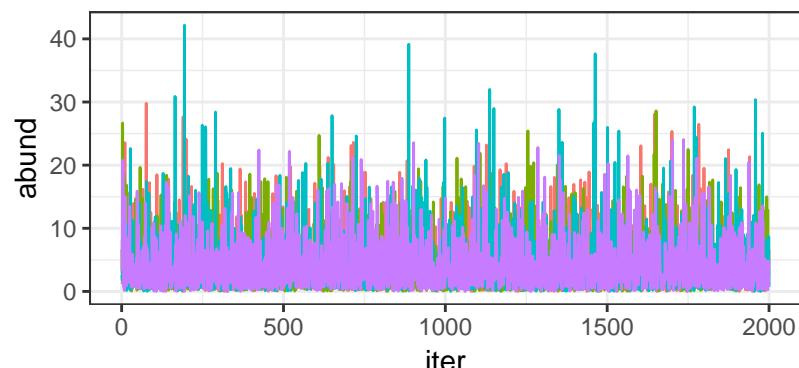
ACB



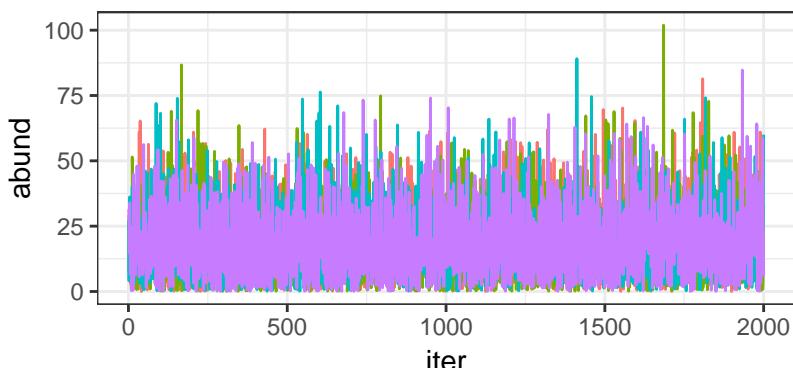
CC4



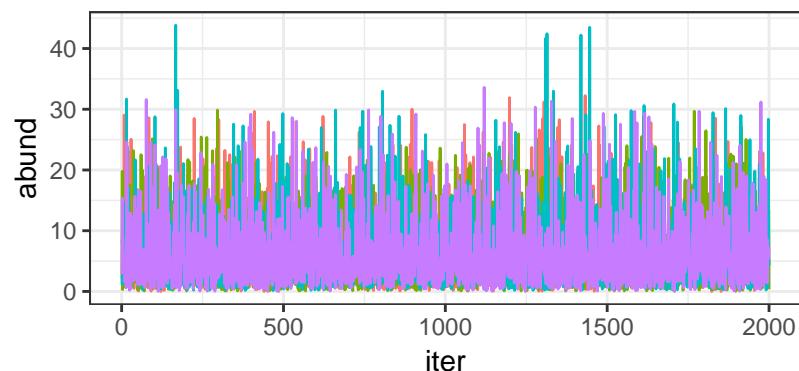
ACB\_bb



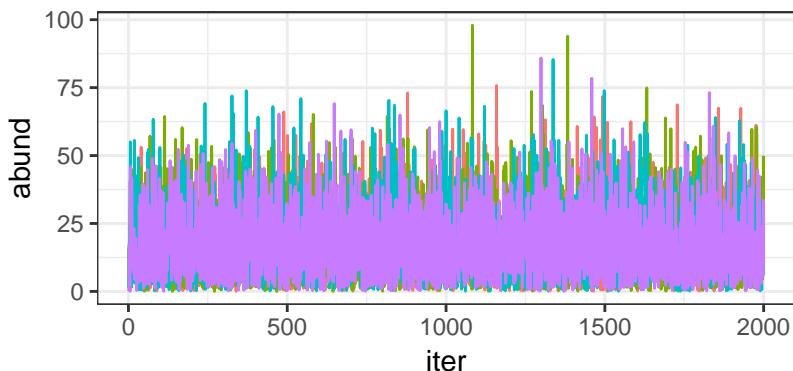
CC4\_bb



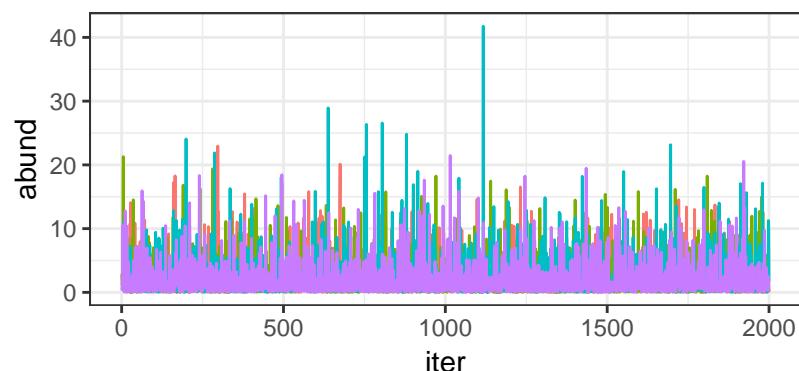
ACM



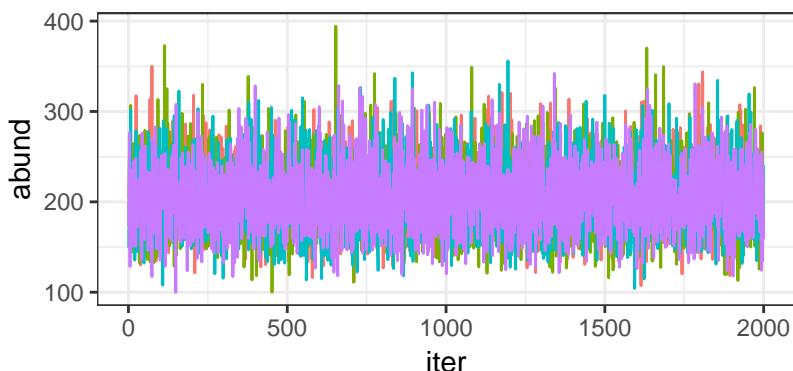
CC5



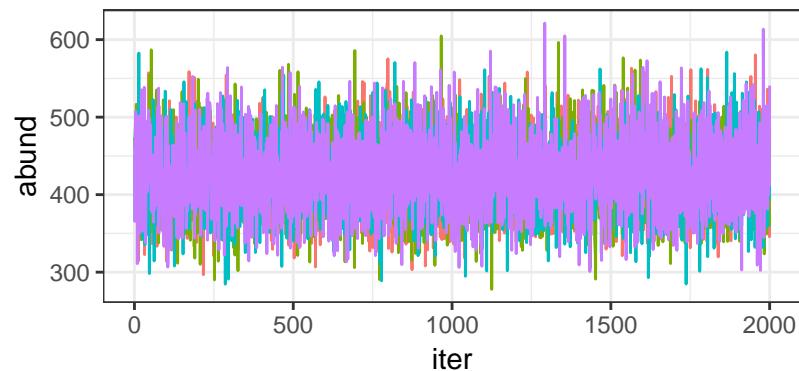
ACM\_bb



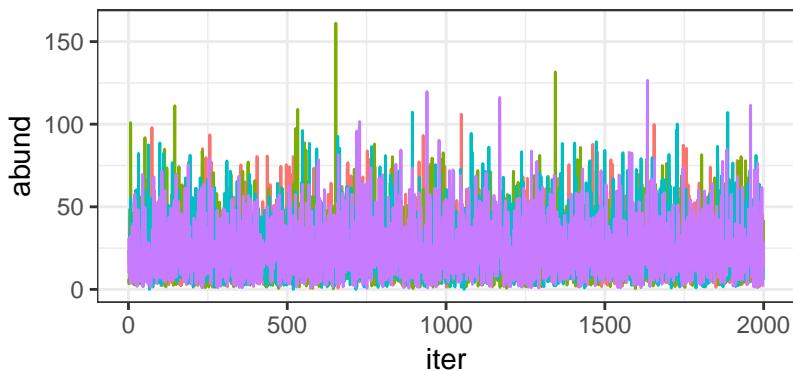
CCU



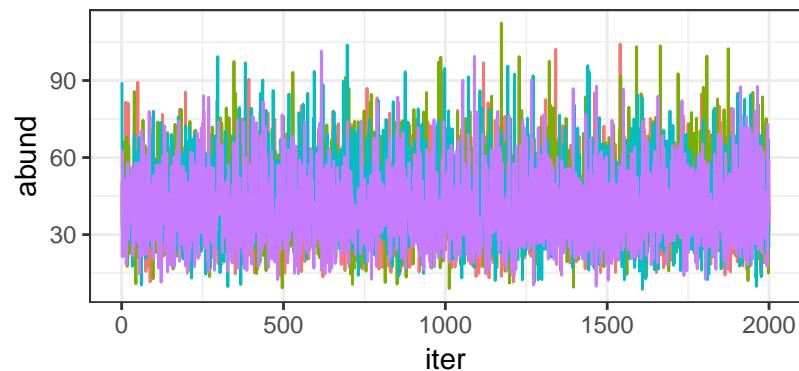
BRC



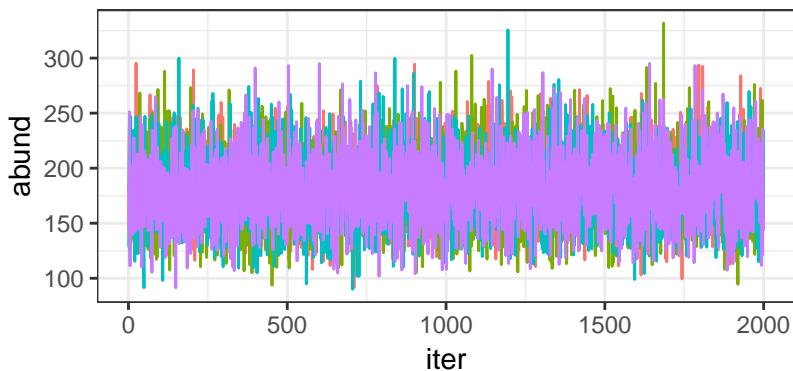
CCU\_bb



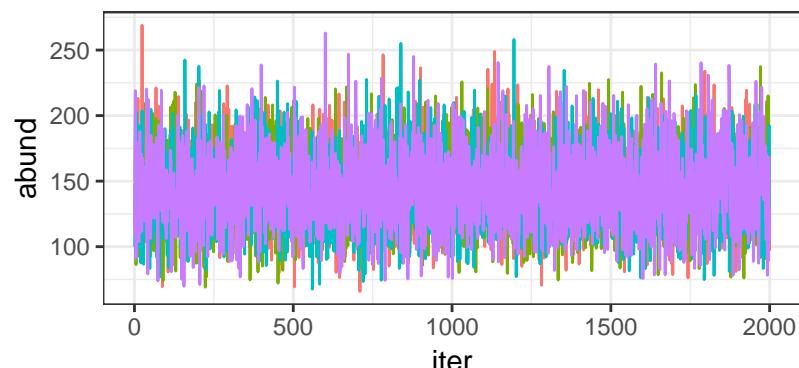
BSC



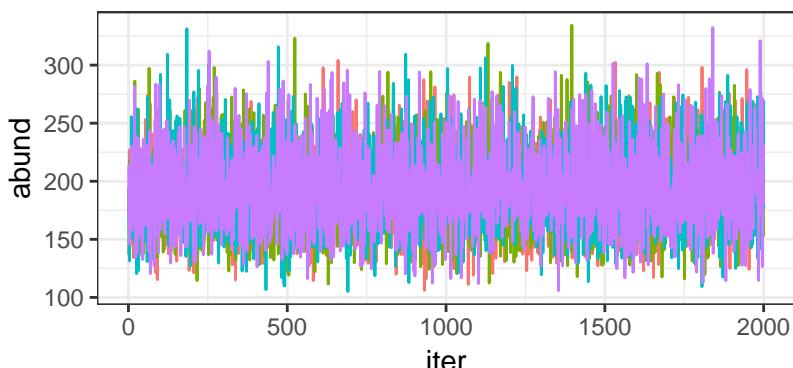
CCW



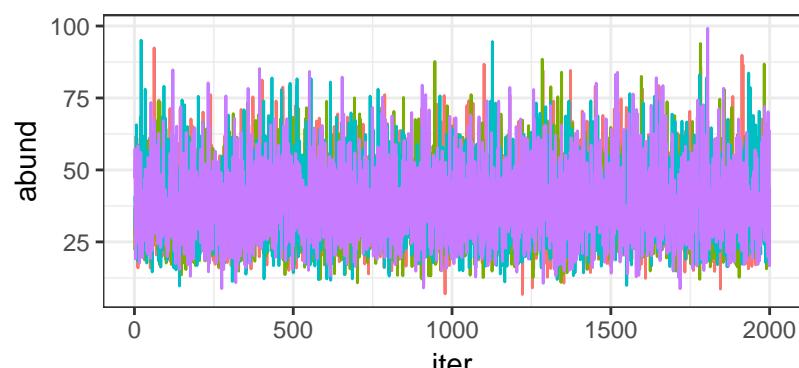
CCW\_bb



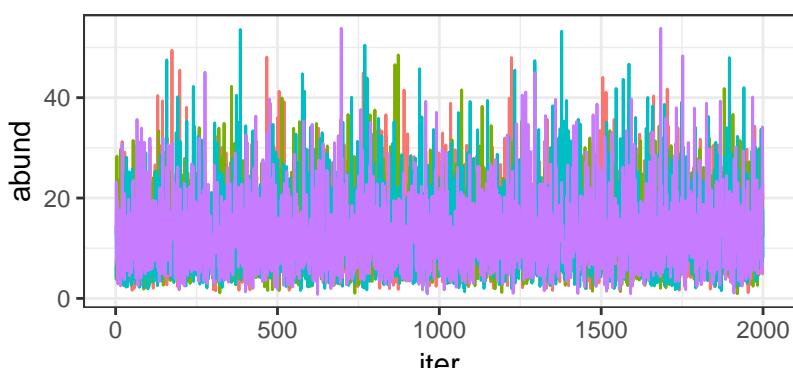
EVU



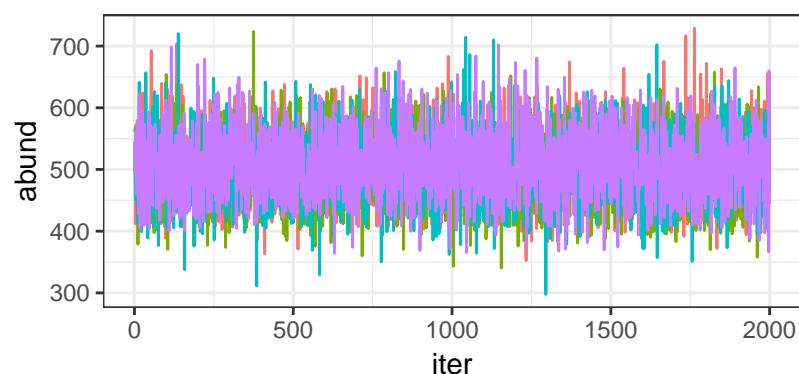
CRA



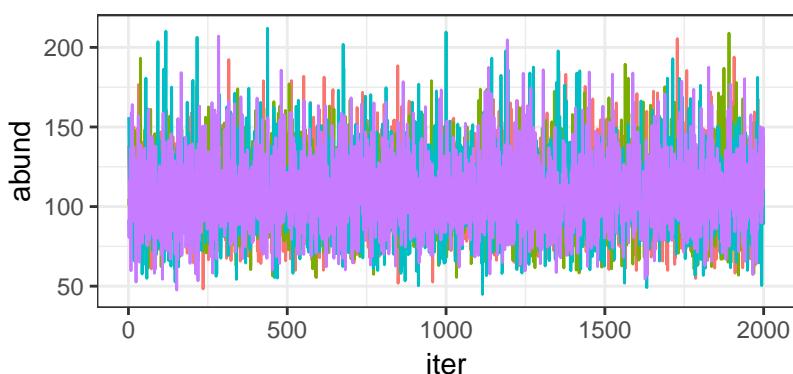
EVU\_bb



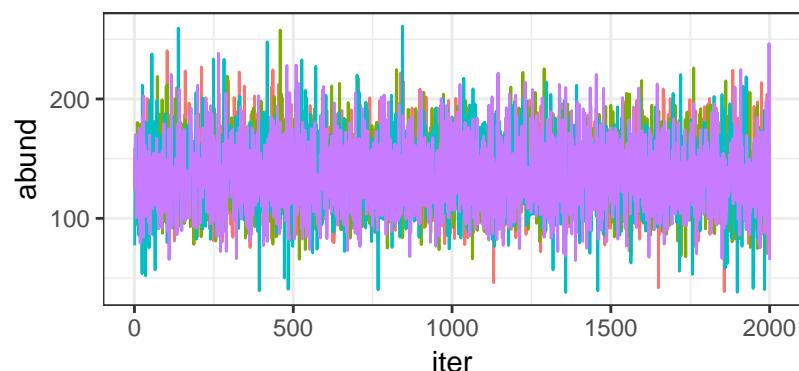
ESS



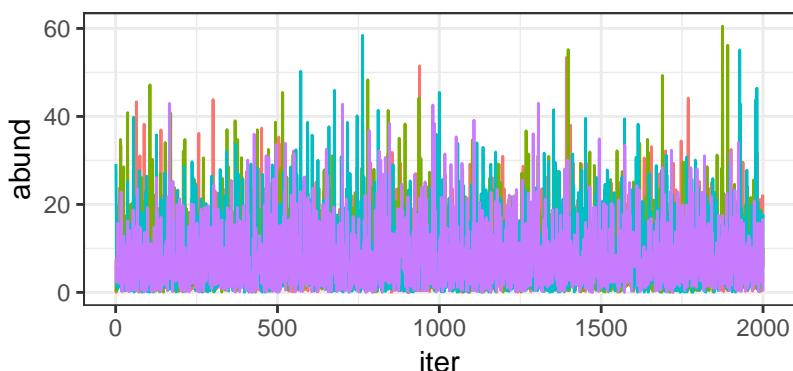
GOA



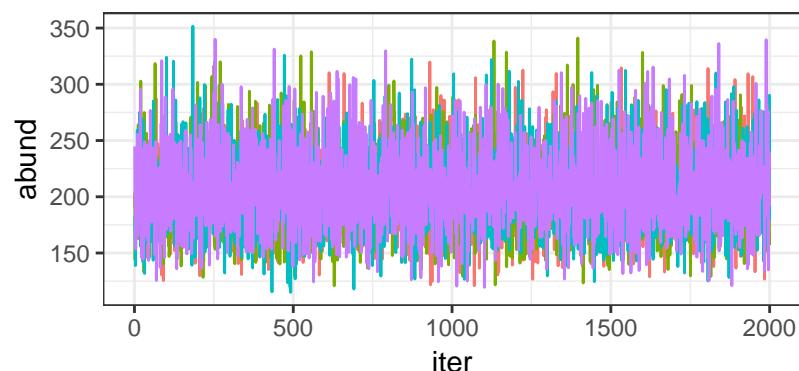
ESS\_bb



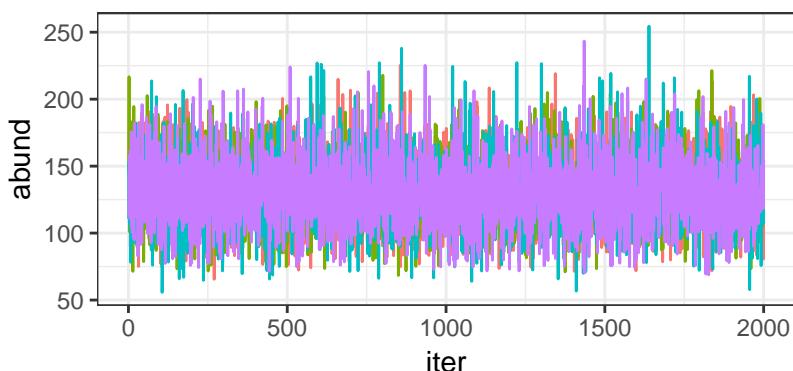
GOA\_bb



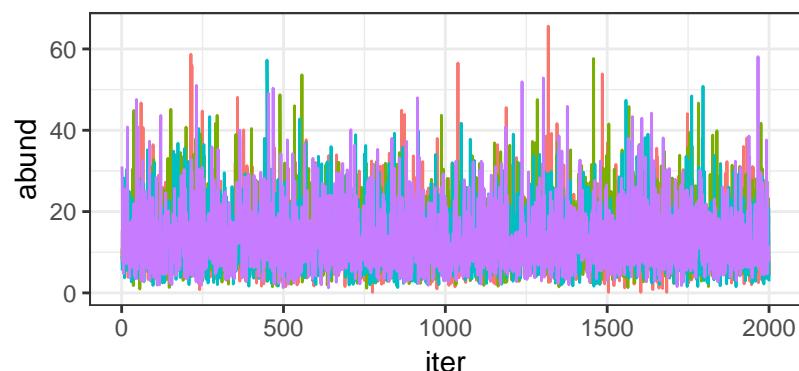
EVL



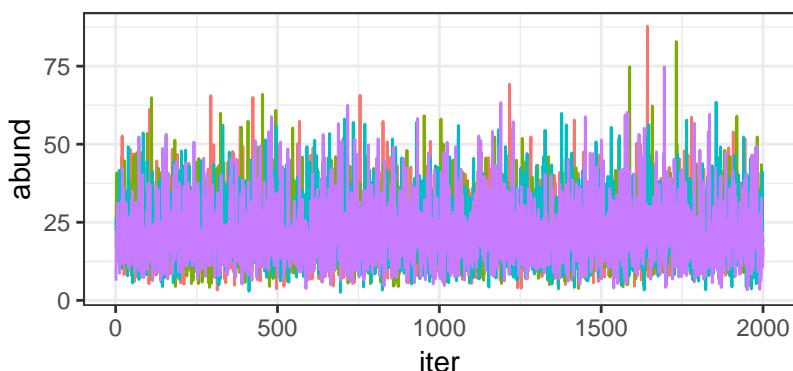
GRS



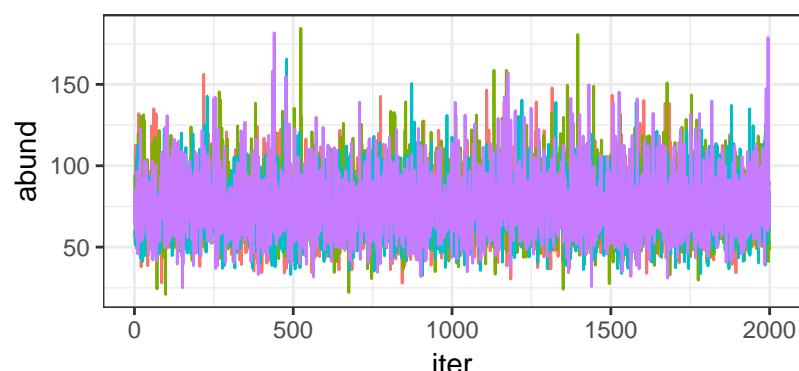
EVL\_bb



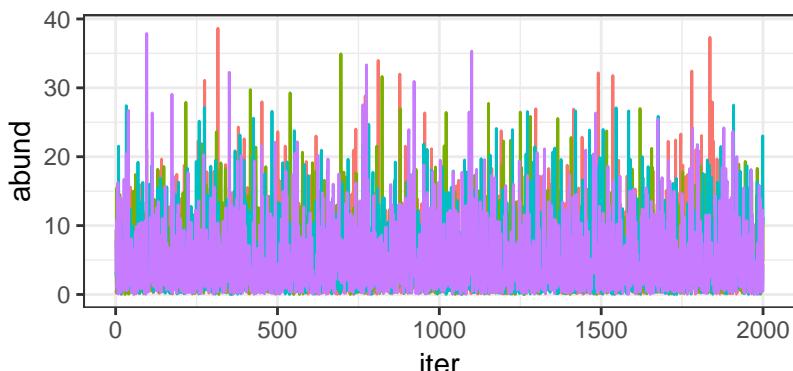
GRS\_bb



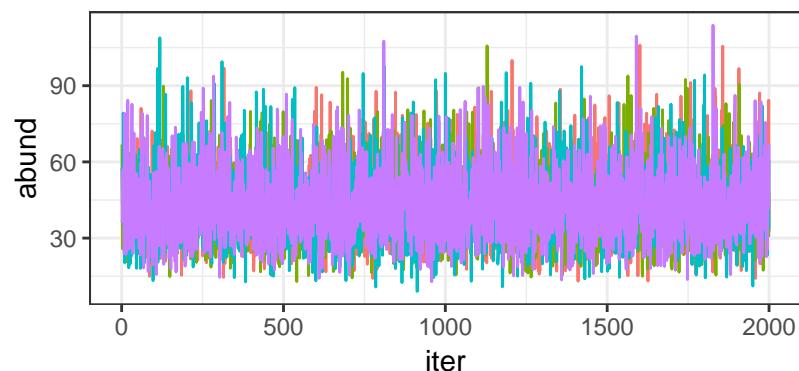
HYC



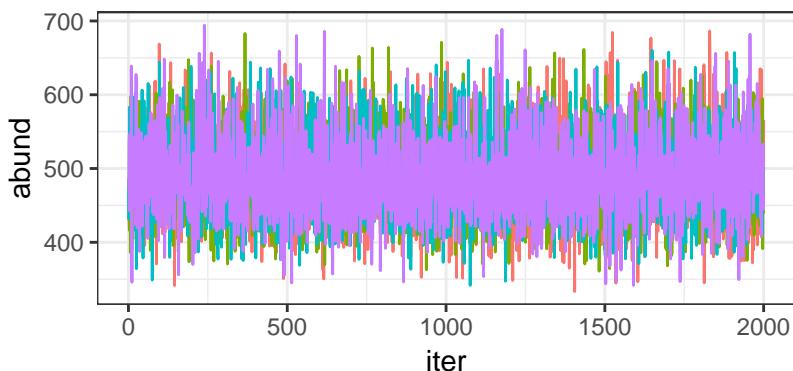
IR1\_bb



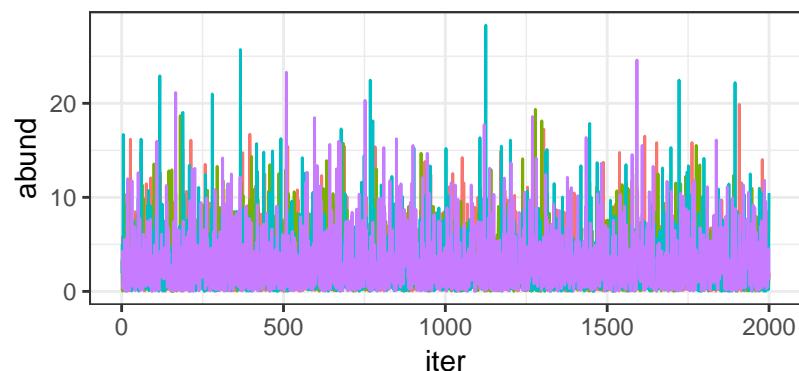
IHR



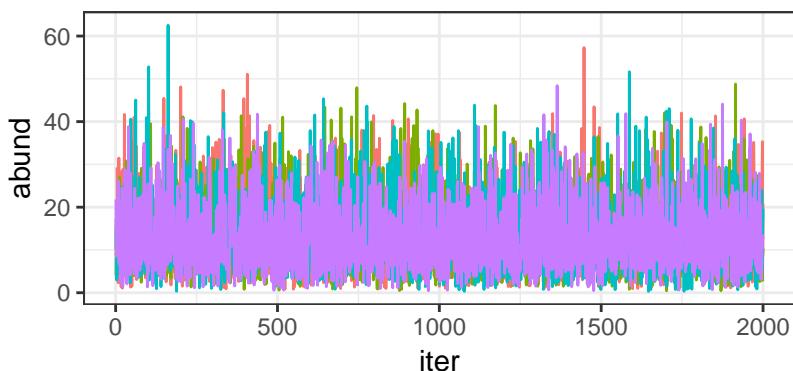
IR2



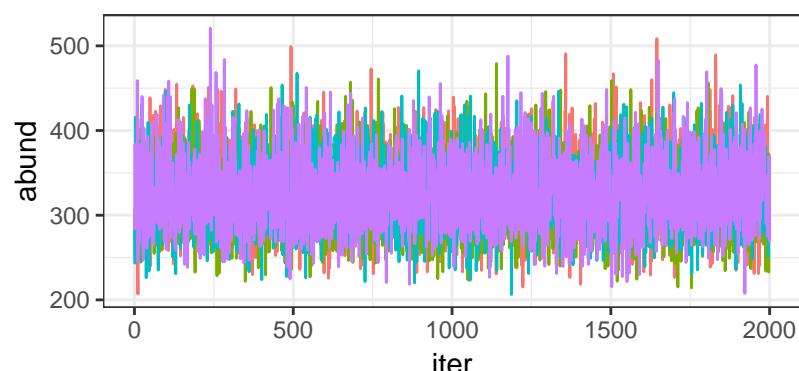
IHR\_bb



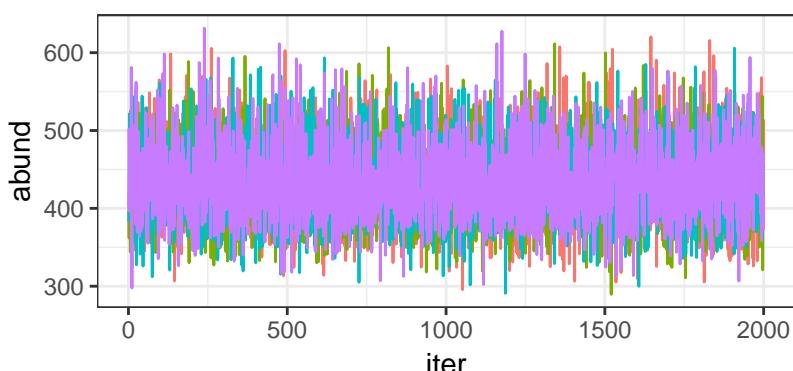
IR2\_bb



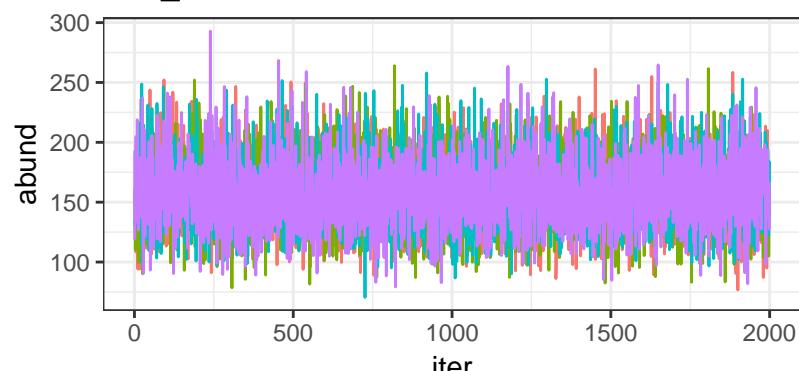
IML



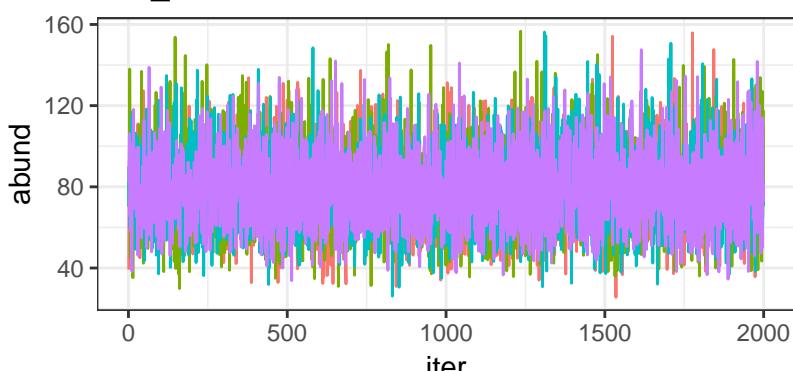
IR3



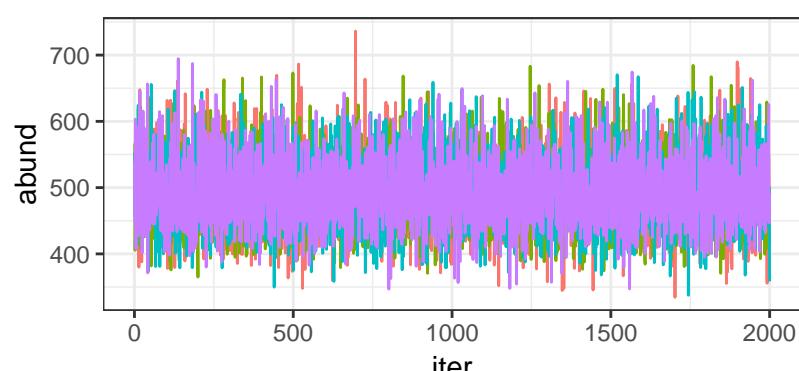
IML\_bb



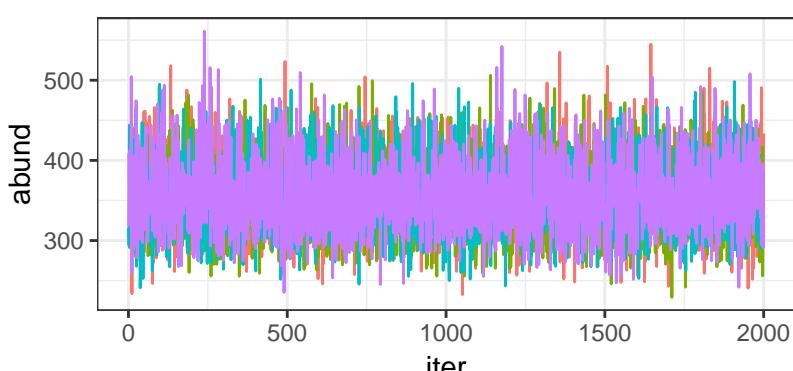
IR3\_bb

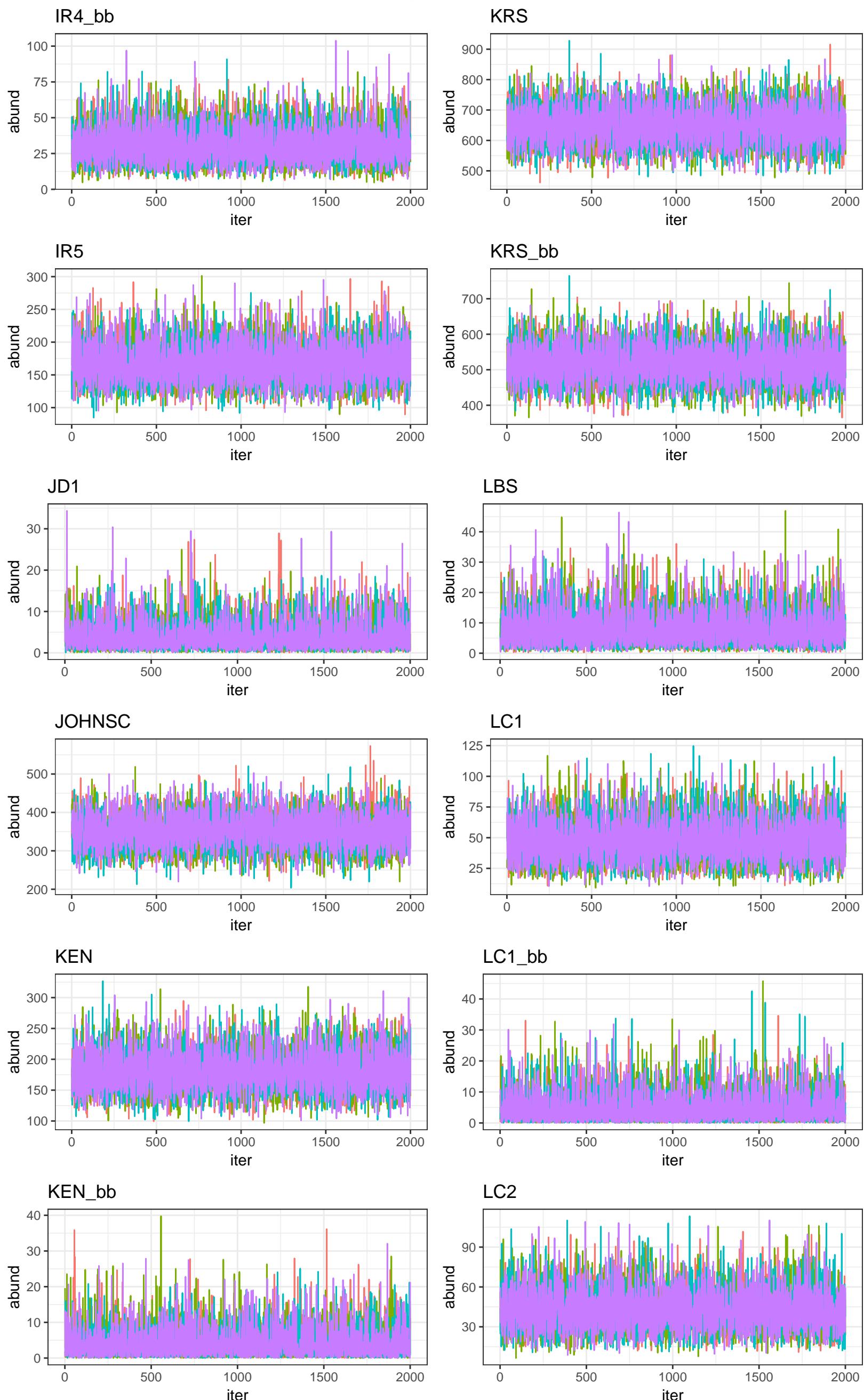


IR1

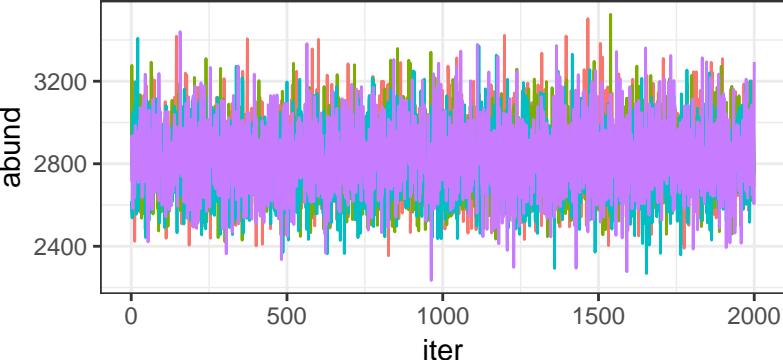


IR4

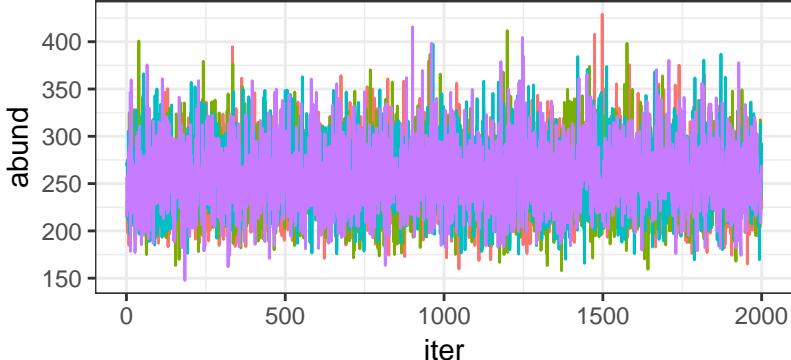




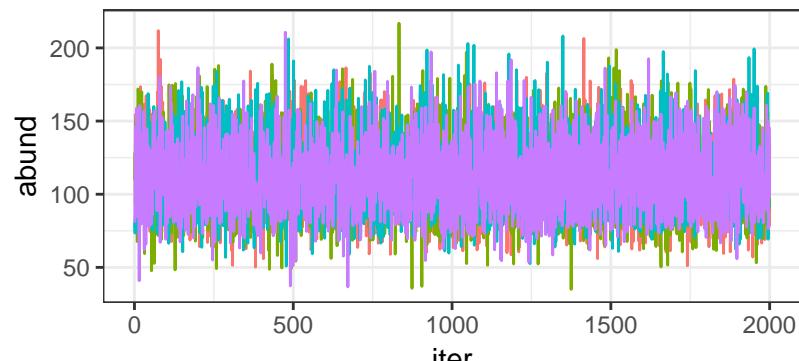
LGR\_bb



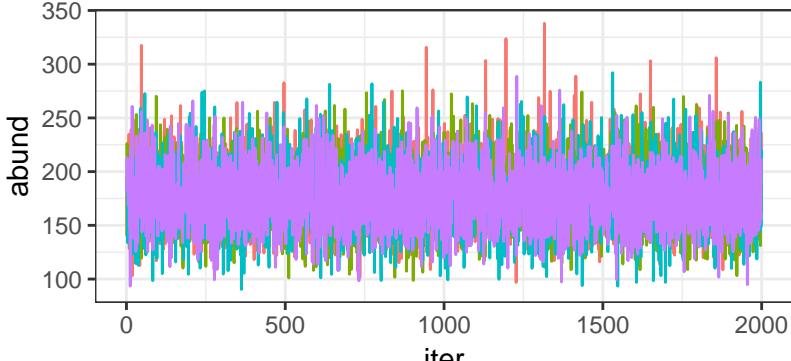
LOSTIW



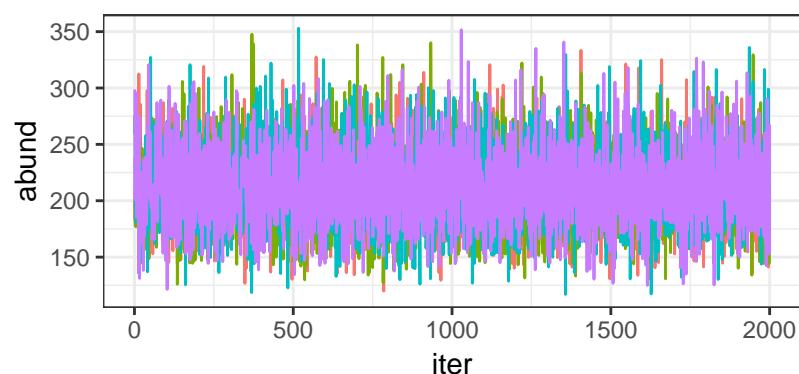
LGW



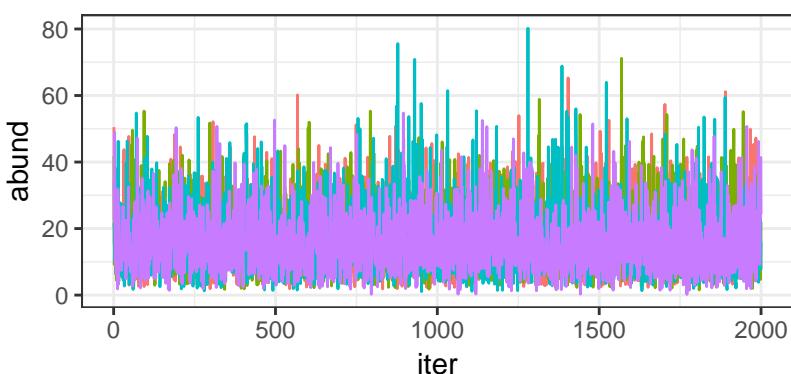
LRL



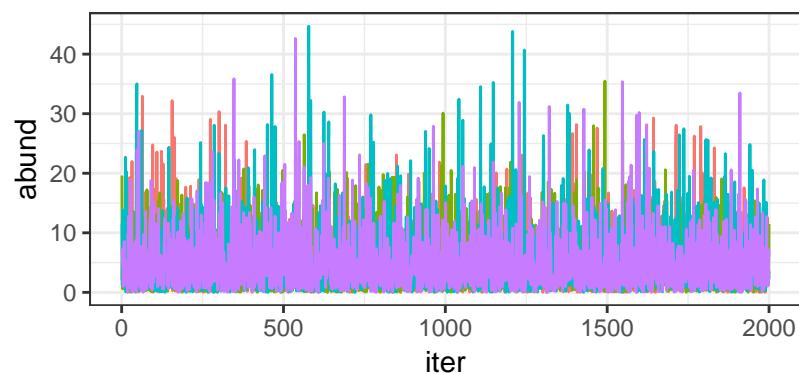
LLR



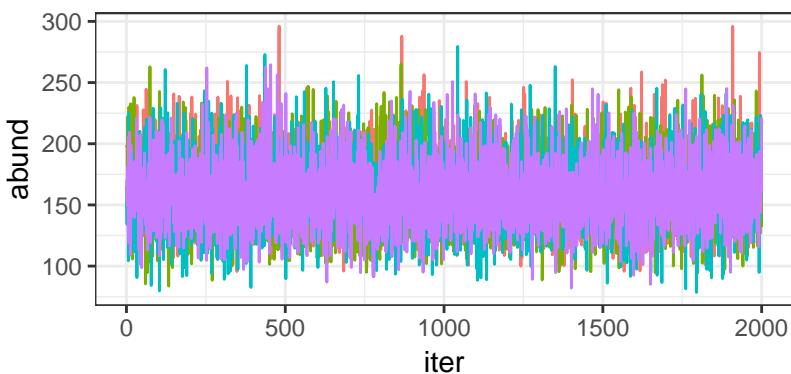
LRL\_bb



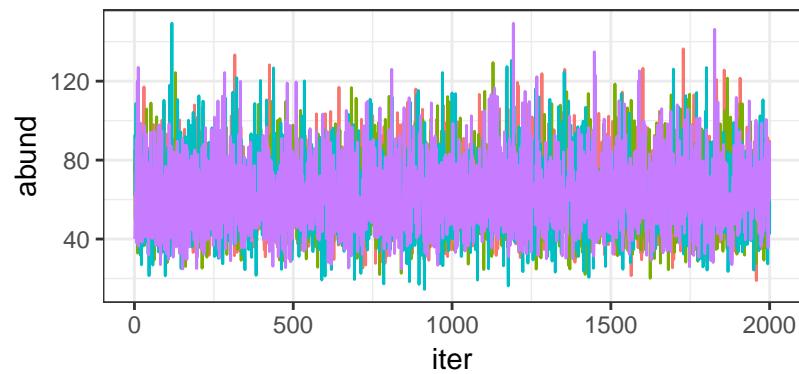
LLR\_bb



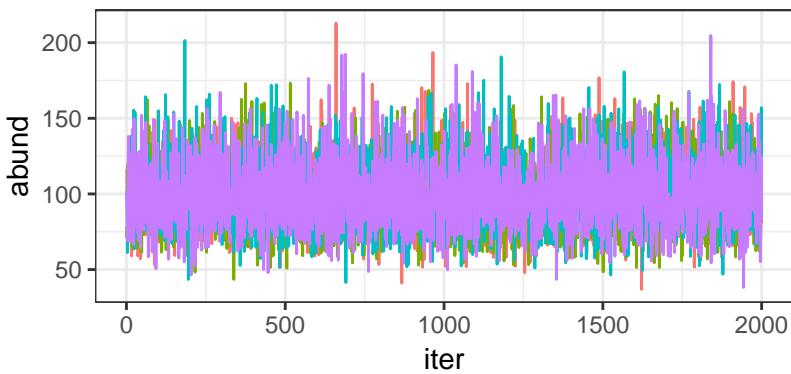
LRU



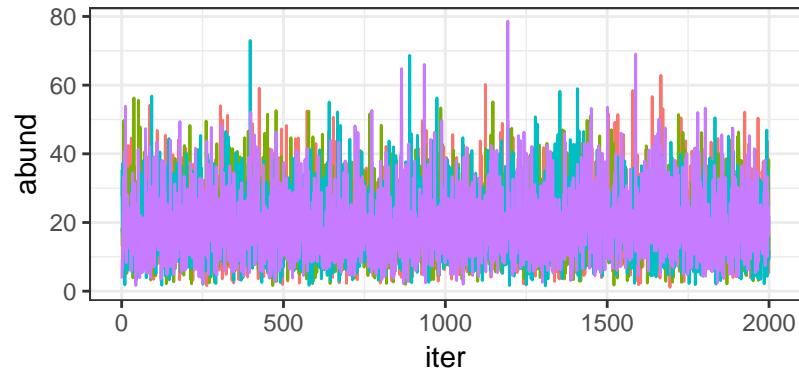
LMA



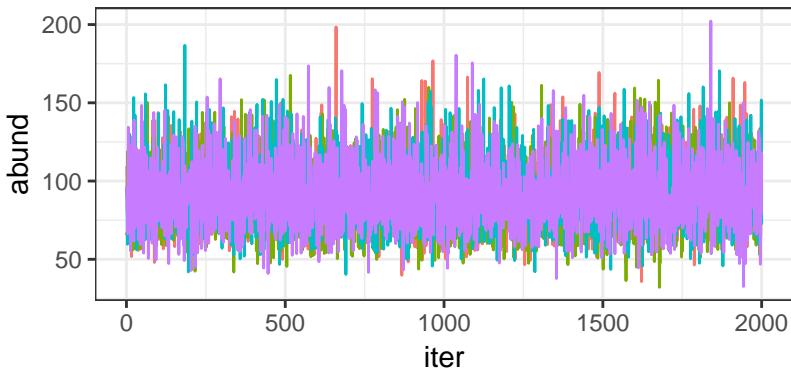
LRW

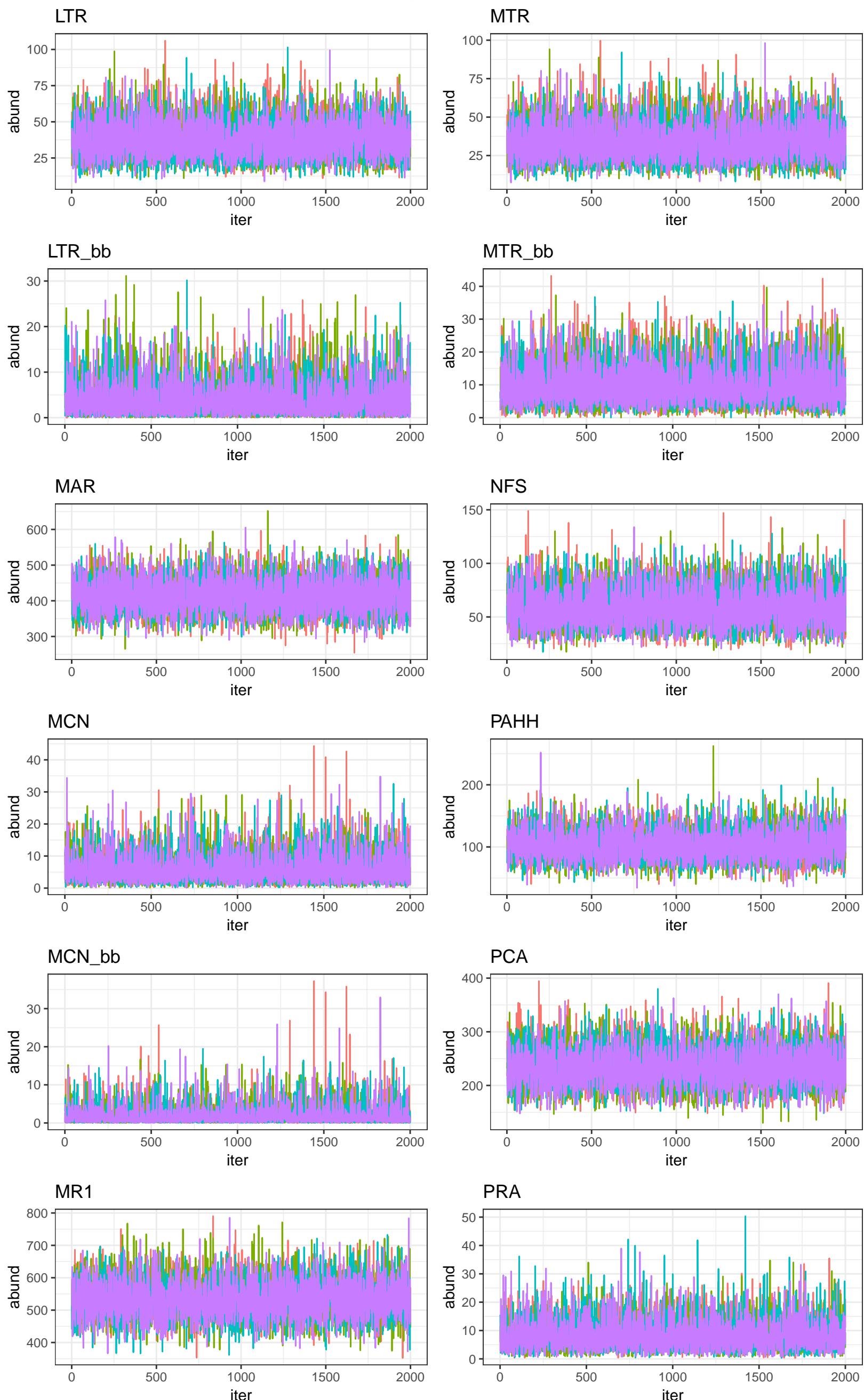


LMA\_bb

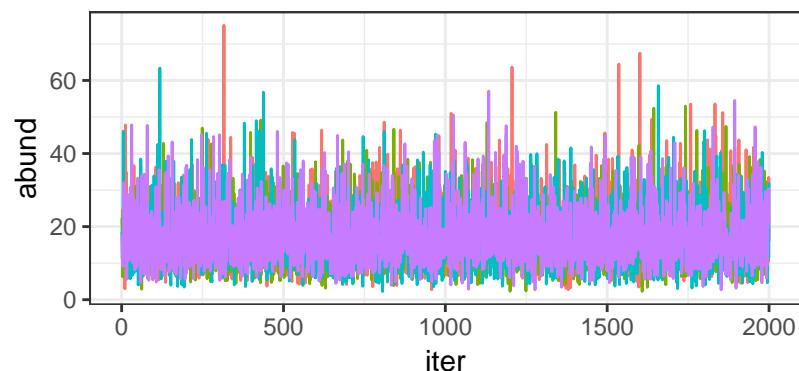


LRW\_bb

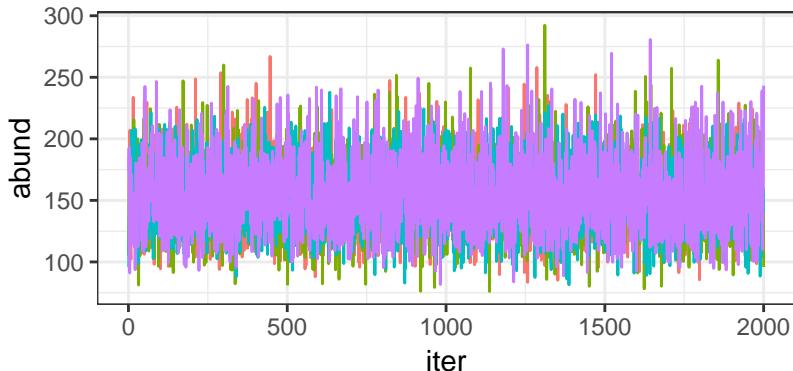




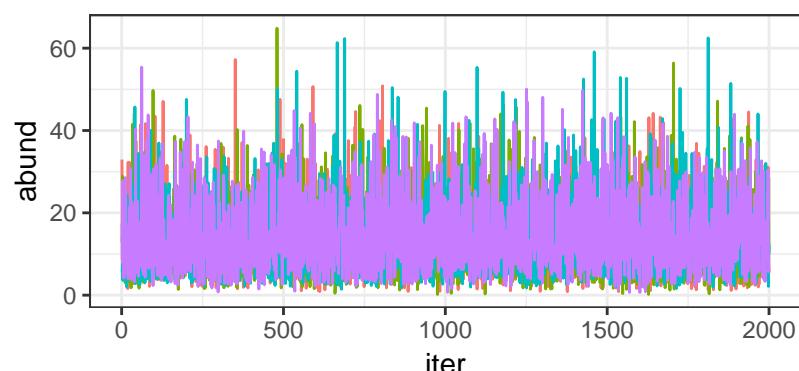
PRO



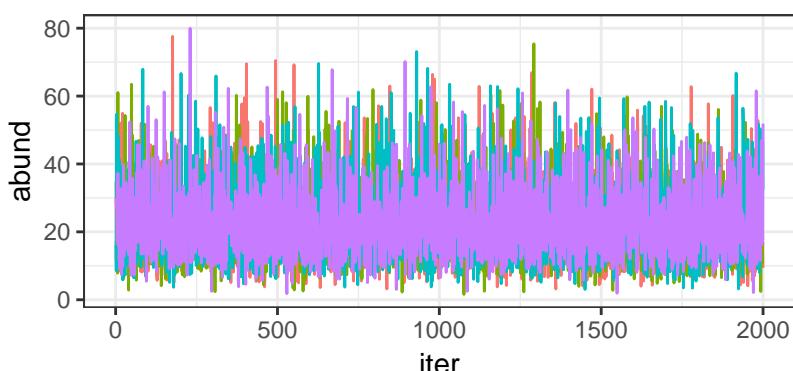
SC2



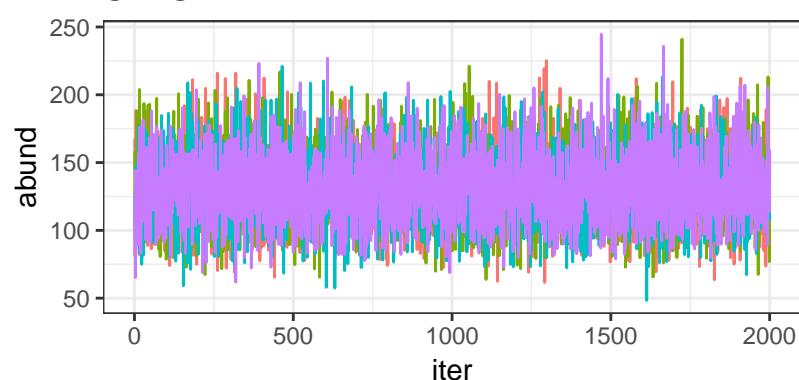
SALEFT



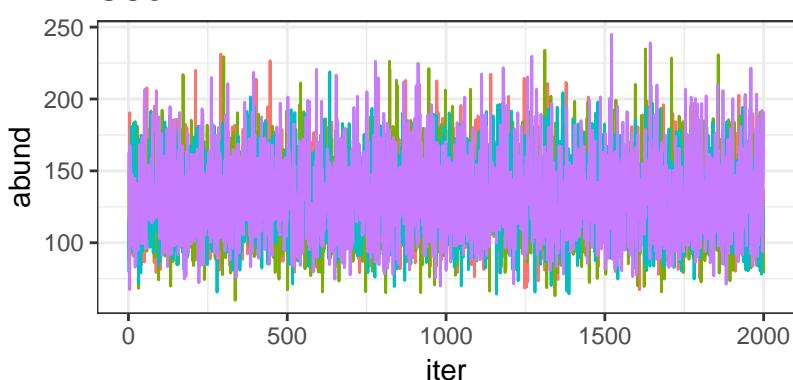
SC2\_bb



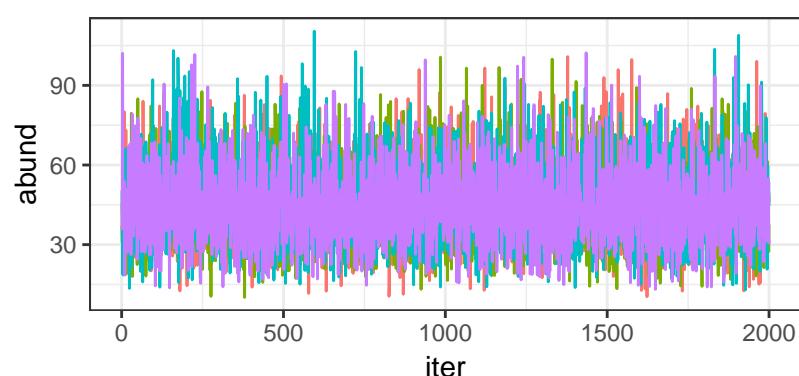
SALSFW



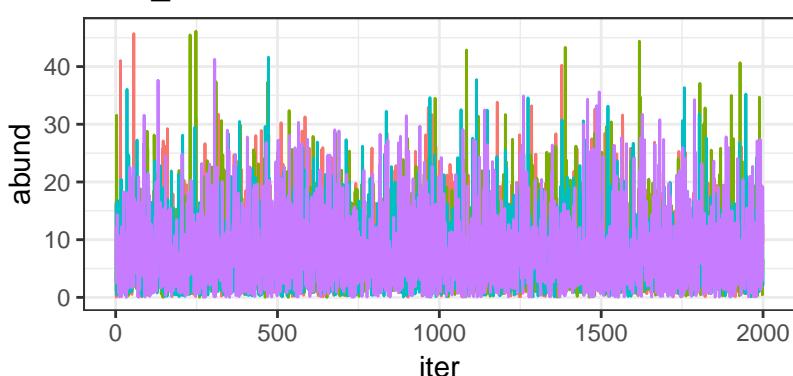
SC3



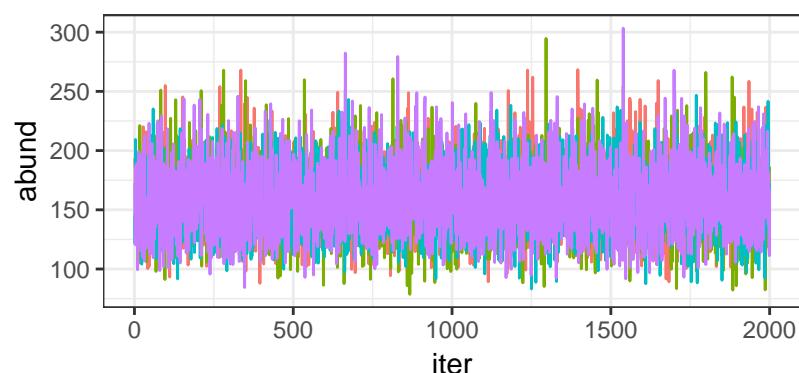
SAWT



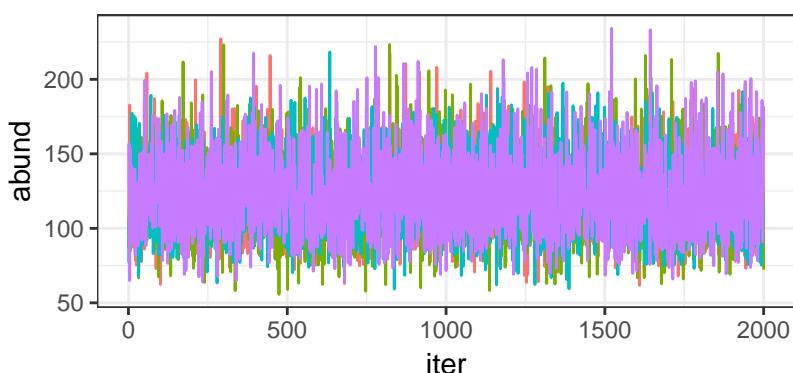
SC3\_bb



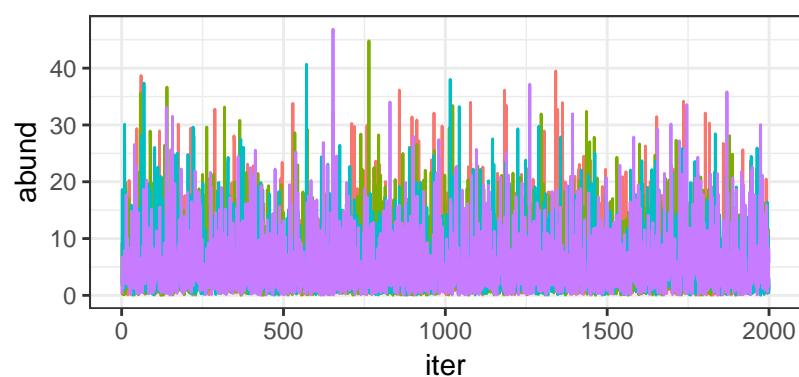
SC1



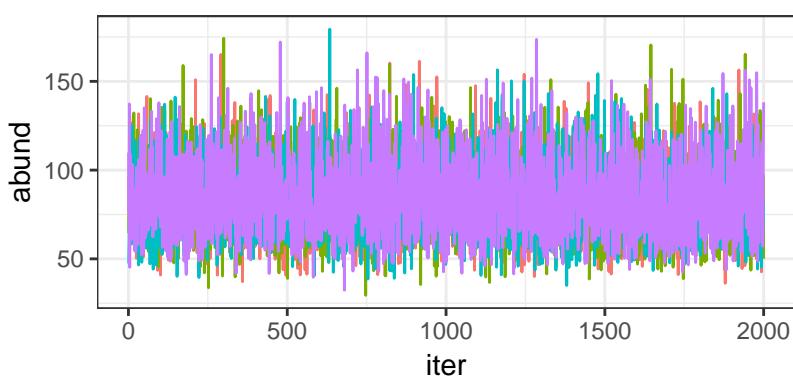
SC4



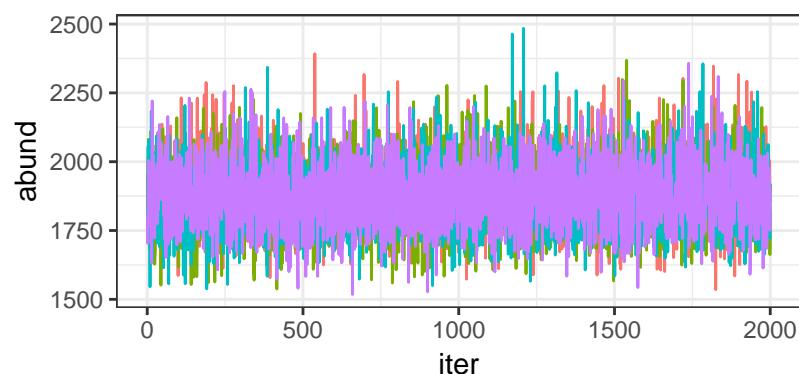
SC1\_bb



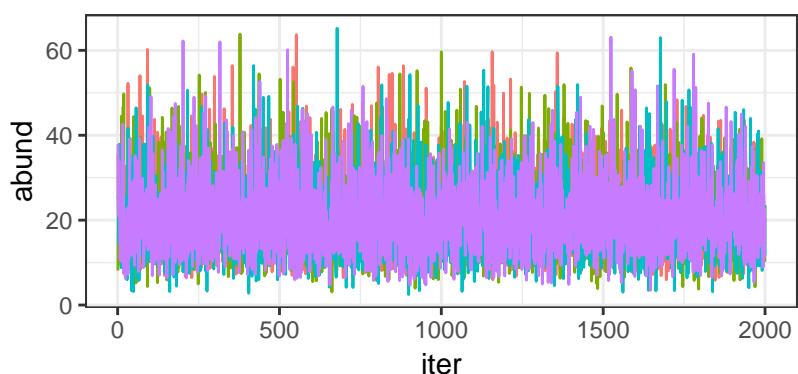
SC4\_bb



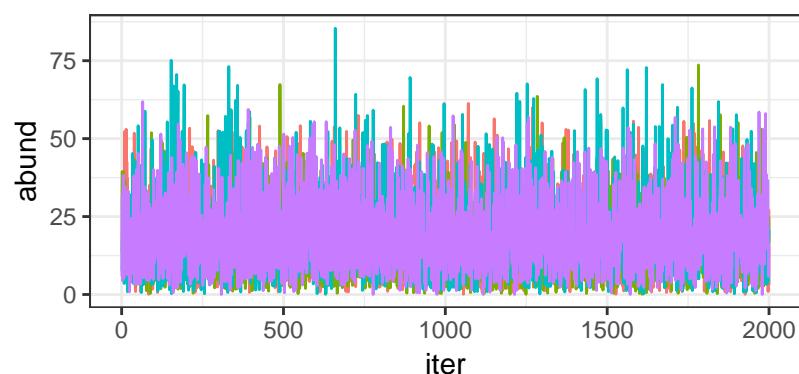
SFG



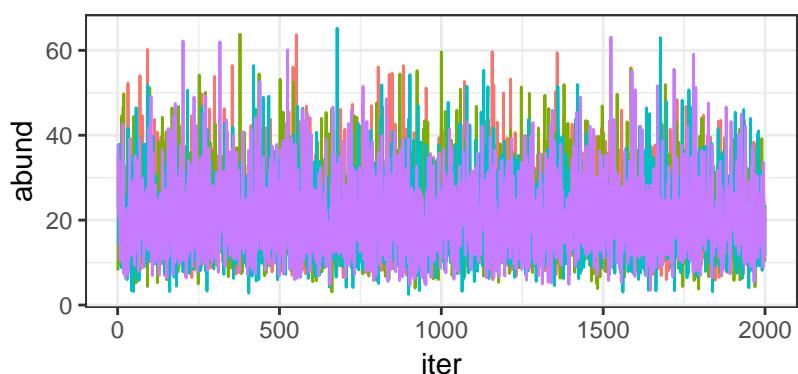
TFH



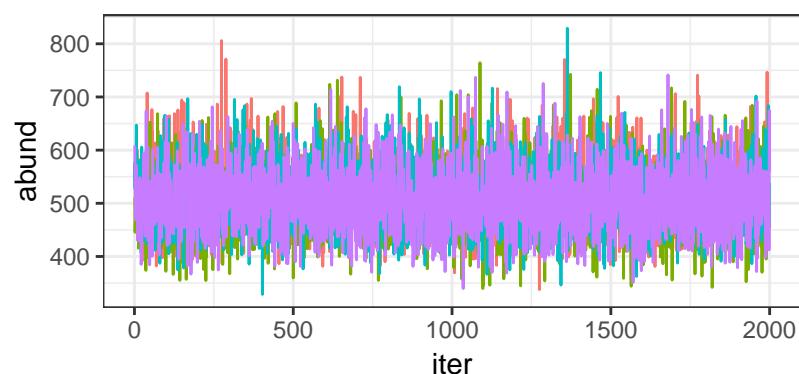
SFG\_bb



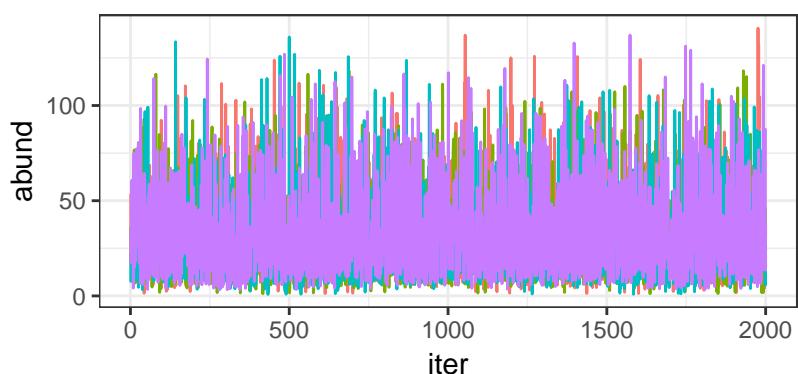
TFH\_bb



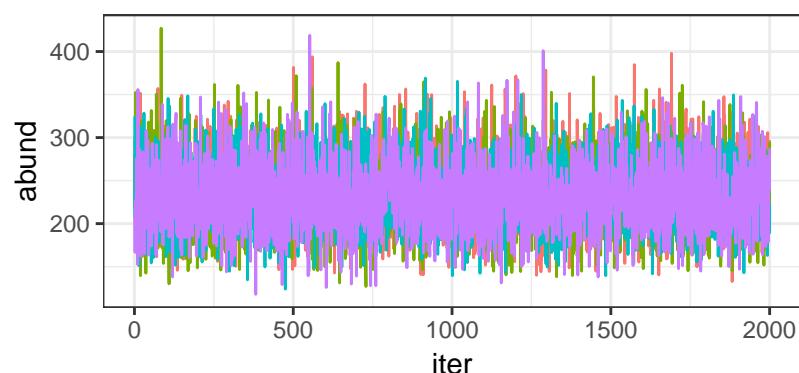
SW1



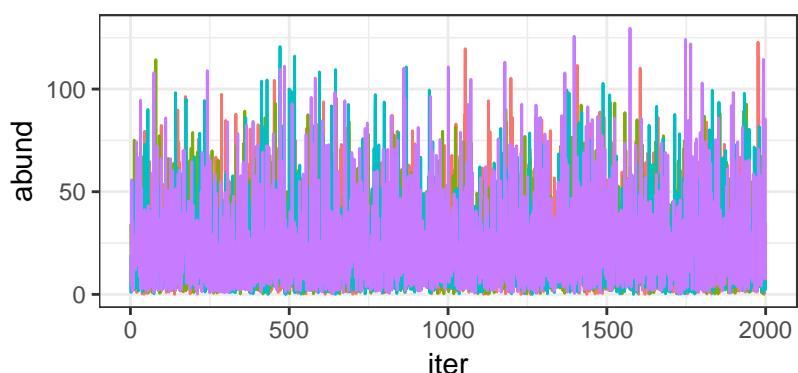
UG3



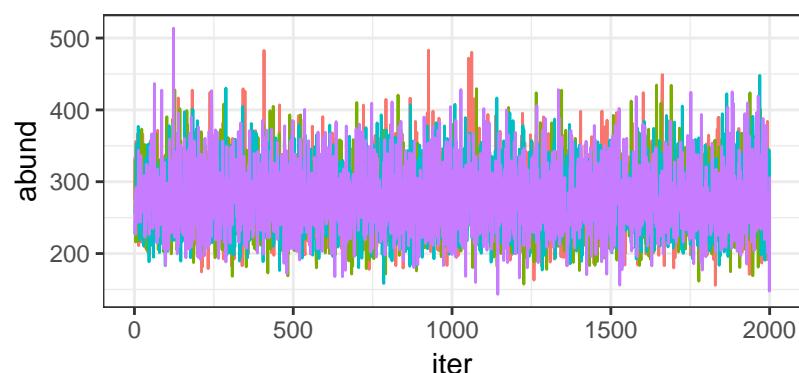
SW1\_bb



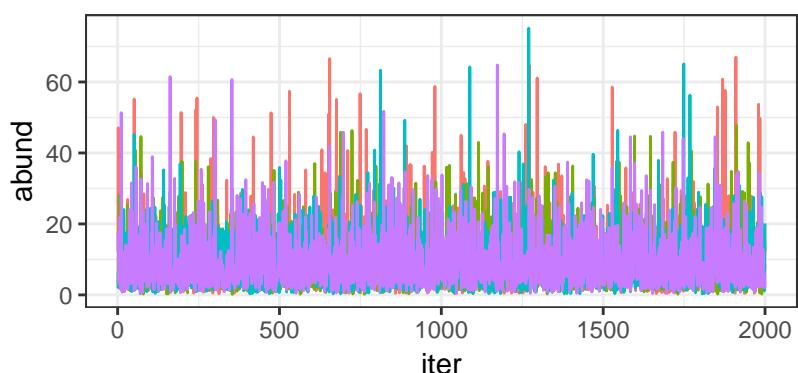
UG3\_bb



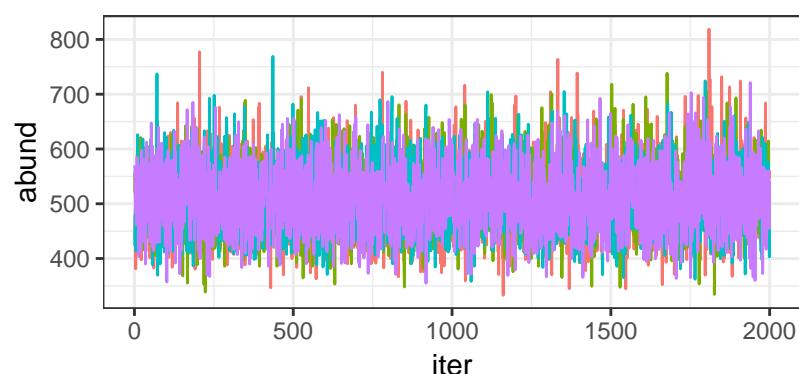
SW2



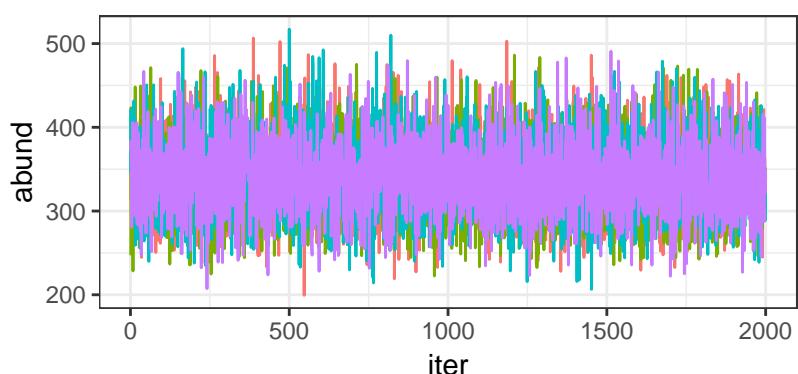
UG4

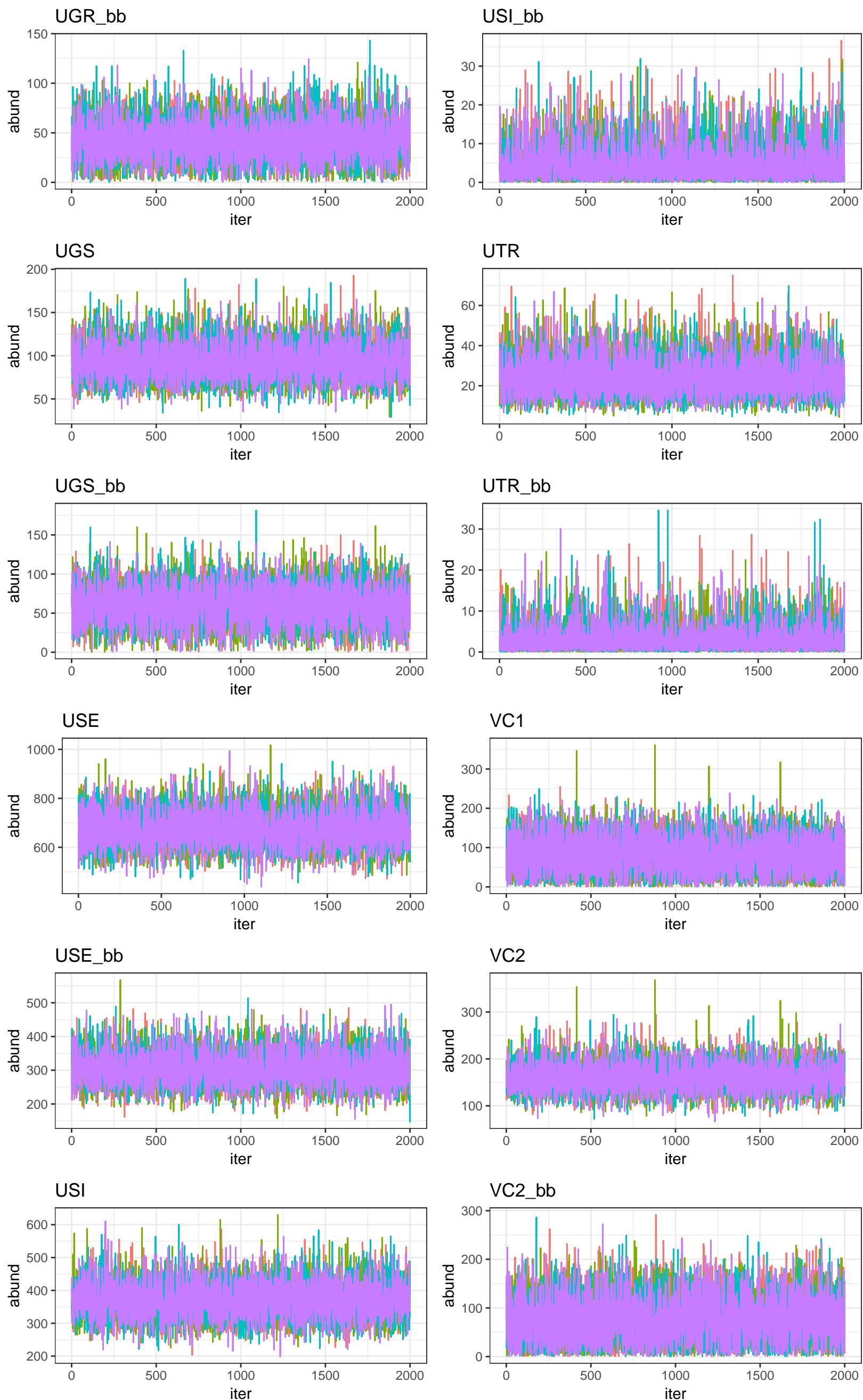


TAY

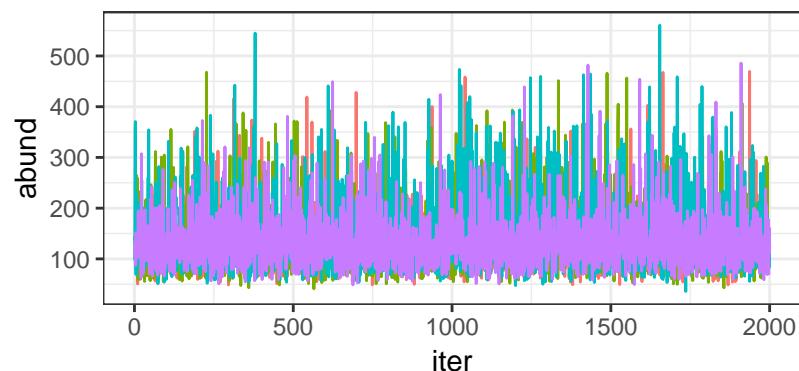


UGR

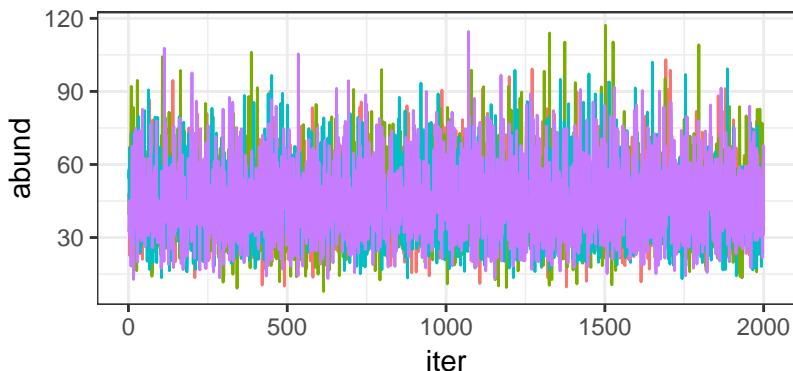




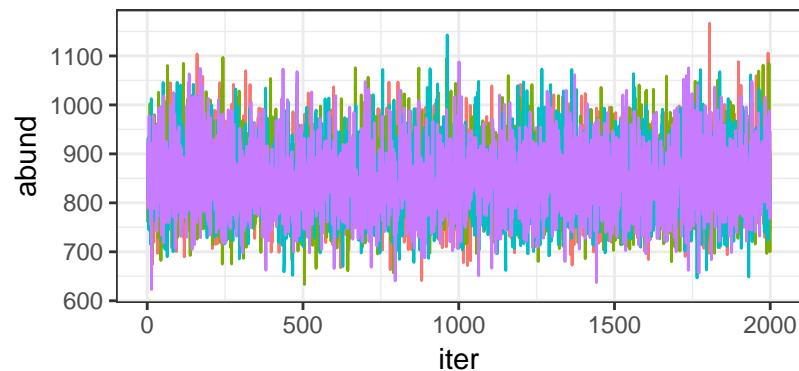
WEN



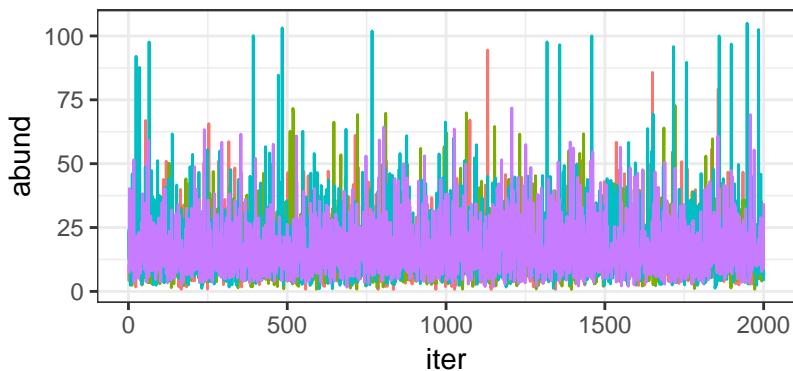
YFK



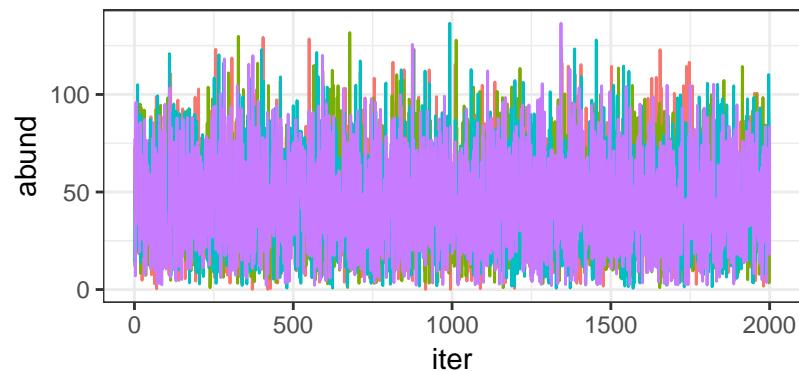
WR1



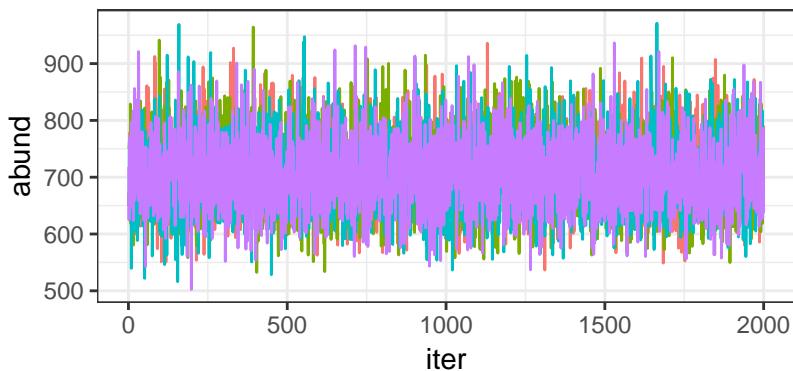
YPP



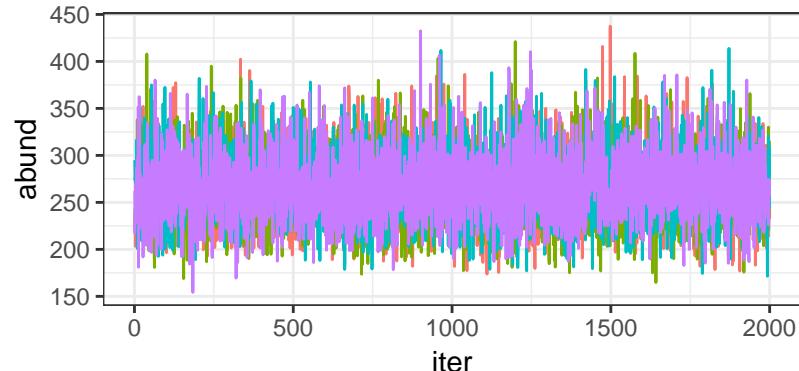
WR1\_bb



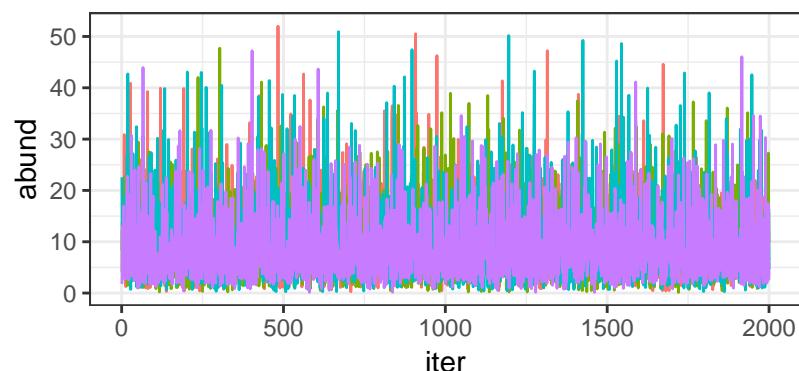
ZEN



WR2



WR2\_bb



WWB

