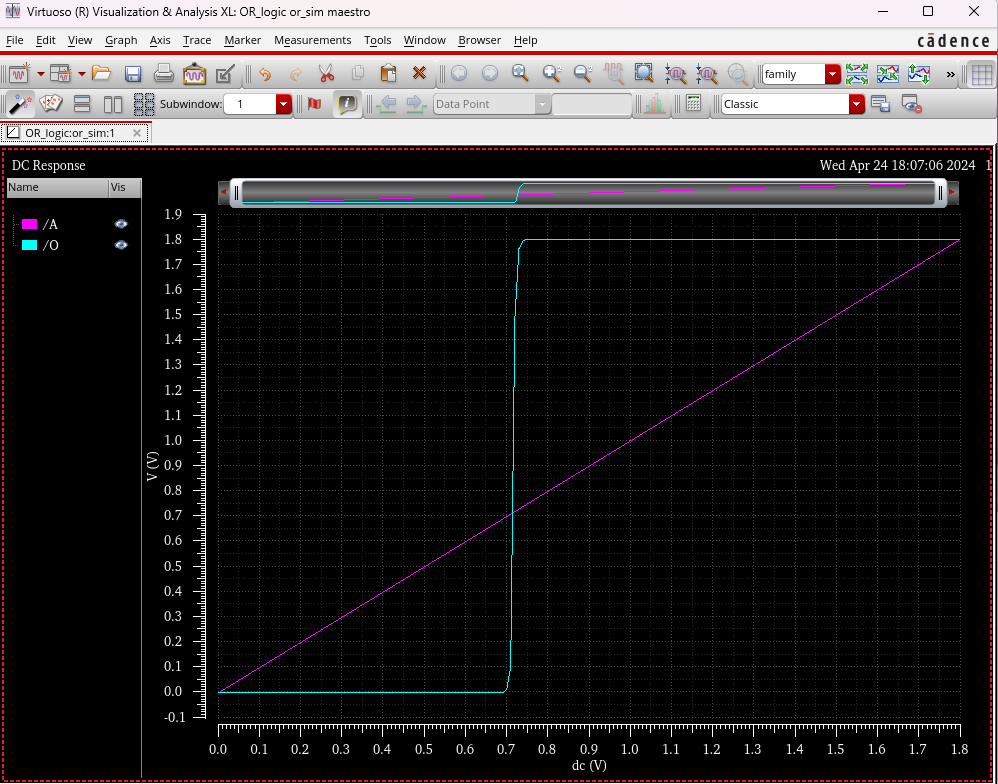
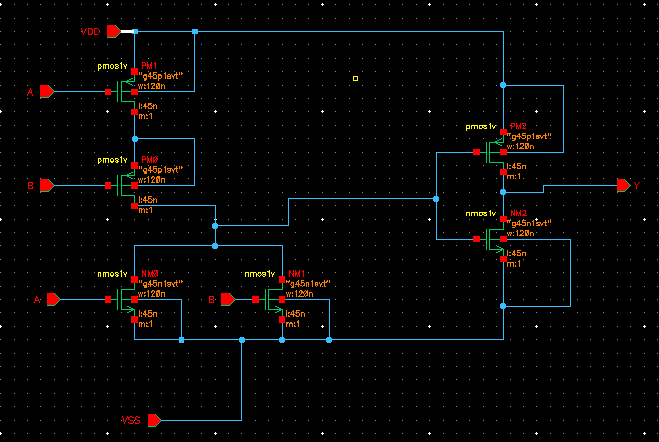
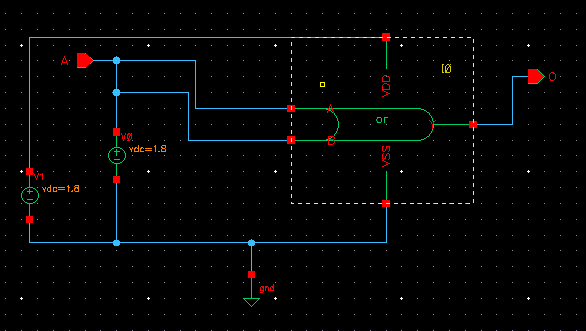


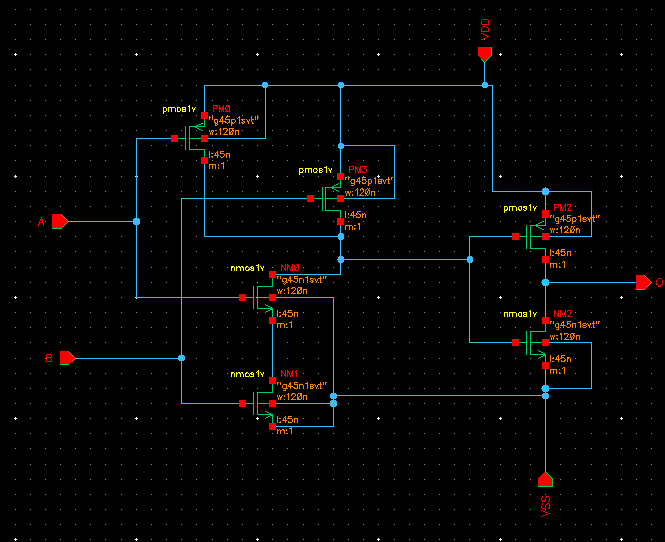
OR LOGIC SIMUALTION RESULT:

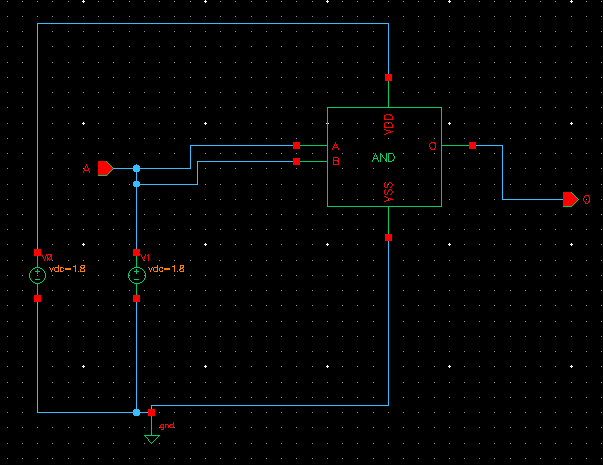


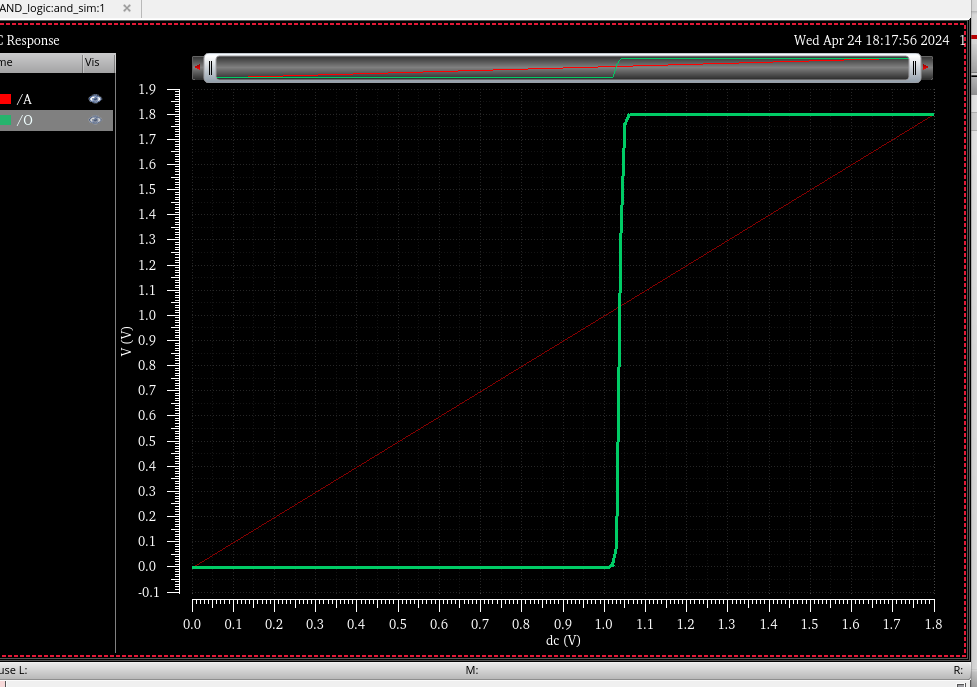




And logic:







Lab4 Continues: 