

```

===== Parallax PIC16C5x Assembler v1.7 =====

1      ;*
2      ;* Sutekh Avatar Version 2.0
3      ;*
4
5      ;*
6      ;* equates
7      ;*
8
9      =0052
10     =0032
11     =0012
12     =0042
13
14     =0008
15     =0009
16     =000A
17     =000B
18
19     =000C
20     =000D
21
22     =000E
23     =000F
24     =0010
25     =0011
26
27     =0012
28     =0013
29     =0014
30     =0015
31
32     =0016
33     =0017
34     =0018
35
36     =001E
37     =001F
38
39     =0006
40
41     =0005
42     =0025
43     =0045
44     =0065
45
46     =004C
47     =005F
48     =006F
49     =007A
50     =007F
51     =007A
52     =006F
53     =005F
54     =004C
55     =0039
56     =0029
57     =001E

;
; Sutekh Avatar Version 2.0
;
;
; equates
;
pass3  = 52H ; password = 52321242
pass2  = 32H
pass1  = 12H
pass0  = 42H
;
t1c_hi = 8 ; tone constant registers
t1c_lo = 9
t2c_hi = 10
t2c_lo = 11
;
dur_hi = 12 ; tone duration registers
dur_lo = 13
;
ph1_hi = 14 ; phase accumulation registers
ph1_lo = 15
ph2_hi = 16
ph2_lo = 17
;
pwreg0 = 18 ; circulating pw digit registers
pwreg1 = 19
pwreg2 = 20
pwreg3 = 21
;
pulse = 22 ; pulse counting register
secure = 23 ; holds security value
digit = 24 ; holds current digit
;
temp = 30 ; temporary registers
scratch = 31
;
dac = rb ; DAC is portb
;
buttona = ra.0 ; button constants
buttonb = ra.1
buttonc = ra.2
buttond = ra.3
;
sine0 = 76 ; sine table constants
sine1 = 95 ;
sine2 = 111 ; this sine table is calculated for use
;
sine3 = 122 ; with an emitter follower
;
sine4 = 127 ; the table includes a dc offset to insure
;
sine5 = 122 ; that there will be no clipping of the wave
;
sine6 = 111
sine7 = 95
sine8 = 76
sine9 = 57
sine10 = 41
sine11 = 30

58     =001A
59     =001E
60     =0029
61     =0039
62
63     =00A5
64     =0000
65
66     =0016
67     =00DB
68     =001D
69     =0094
70     =0005
71     =00EA
72     =001B
73     =0089
74     =0022
75     =006C
76     =0029
77     =004E
78
79     =0002
80     =001E
81     =0003
82     =0074
83     =0005
84     =00E8
85     =0026
86     =0027
87     =0095
88     =00C0
89
90
91
92
93
94     000-
95     000-
96     1FF- A89
97
98
99
100
101
102     000-
103     000- 003
104
105
106
107
108
109
110     001- 000
111     002- 000
112
113     003- 004
114
115     004- 209
116     005- 1EF

sine12 = 26
sine13 = 30
sine14 = 41
sine15 = 57
;
secval = 0A5H
nil = 0
;
red1_hi = 016H ; tone constants and durations
;
red1_lo = 0DBH
red2_hi = 01DH
red2_lo = 094H
ahi = 005H ; 440Hz
alo = 0EAH
chi = 01BH ; 2048Hz
clo = 089H
ehi = 022H ; 2560Hz
elo = 06CH
ghi = 029H ; 3072Hz
glo = 04EH
;
_15mshi = 002H ; +1 added
_15mslo = 01EH
_33mshi = 003H ; +1 added
_33mslo = 074H
_66mshi = 005H ; +1 added
_66mslo = 0E8H
_500msh = 026H ; +1 added
_500msl = 027H
_2sechi = 095H ; +1 added
_2seclo = 0C0H
;*****
;* chip info *
;*****
device PIC16C54, XT_OSC, WDT_ON, PROTECT
id 7777H
reset wake_up
;*****
;* start of code *
;*****
org 0
sleep
;*****
;* subroutines *
;*****
twotone nop ; generate two tones
nop ; at Fosc=3.57954
tt clrwdt ; clear wdt
movf t1c_lo, w ; advance phase f
addwf ph1_lo

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117	006- 208	movf	t1c_hi, w		173	037- 06A	clrf	t2c_hi	; only a single t	
118	007- 603	btfsc	c		one					
119	008- 288	incf	t1c_hi, w		174	038- 06B	clrf	t2c_lo		
120	009- 1EE	addwf	ph1_hi		175	039- 800	retlw	nil		
121					176					
122	00A- 20B	movf	t2c_lo, w	; advance phase	177	03A- C1B	su_C	movlw	chi	; set up for C
for tone2					178	03B- 028		movwf	t1c_hi	
123	00B- 1F1	addwf	ph2_lo		179	03C- C89		movlw	clo	
124	00C- 20A	movf	t2c_hi, w		180	03D- 029		movwf	t1c_lo	
125	00D- 603	btfsc	c		181	03E- 06A		clrf	t2c_hi	; only a single t
126	00E- 28A	incf	t2c_hi, w		one					
127	00F- 1F0	addwf	ph2_hi		182	03F- 06B		clrf	t2c_lo	
128					183	040- 800		retlw	nil	
129	010- 20E	movf	ph1_hi, w	; look up amplit	184					
udes					185	041- C22	su_E	movlw	ehi	; set up for E
130	011- 91C	call	ph2sin		186	042- 028		movwf	t1c_hi	
131	012- 03F	movwf	scratch		187	043- C6C		movlw	elo	
132	013- 210	movf	ph2_hi, w		188	044- 029		movwf	t1c_lo	
133	014- 91C	call	ph2sin		189	045- 06A		clrf	t2c_hi	; only a single t
134	015- 1DF	addwf	scratch, w	; sum waves	one					
135	016- 026	movwf	dac	; ship to DAC	190	046- 06B		clrf	t2c_lo	
136					191	047- 800		retlw	nil	
137	017- 2ED	decfsz	dur_lo	; decrement dela	192					
y values					193	048- C29	su_G	movlw	ghi	; set up for G
138	018- A01	goto	twotone		194	049- 028		movwf	t1c_hi	
139	019- 2EC	decfsz	dur_hi		195	04A- C4E		movlw	glo	
140	01A- A03	goto	tt	; jump short to	196	04B- 029		movwf	t1c_lo	
make up for lost cycles					197	04C- 06A		clrf	t2c_hi	; only a single t
141					one					
142	01B- 800	retlw	nil		198	04D- 06B		clrf	t2c_lo	
143					199	04E- 800		retlw	nil	
144	01C- 03E	ph2sin	movwf	temp ; routine to con	200					
vert from phase angle to					201	04F- C1B	su_3rd	movlw	chi	; set up to play
145	01D- 39E	swapf	temp, w	; waveform magni	a major third interval					
tude value					202	050- 028		movwf	t1c_hi	
146	01E- EOF	andlw	0FH		203	051- C89		movlw	clo	
147	01F- 03E	movwf	temp		204	052- 029		movwf	t1c_lo	
148	020- C23	movlw	table		205	053- C22		movlw	ehi	
149	021- 1DE	addwf	temp, w		206	054- 02A		movwf	t2c_hi	
150	022- 022	movwf	pc	; computed goto	207	055- C6C		movlw	elo	
151					208	056- 02B		movwf	t2c_lo	
152	023- 84C	table	retlw	sine0 ; return lookup	209	057- 800		retlw	nil	
table					210					
153	024- 85F	retlw	sine1		211	058- C1B	su_5th	movlw	chi	; set up a fifth
154	025- 86F	retlw	sine2		interval					
155	026- 87A	retlw	sine3		212	059- 028		movwf	t1c_hi	
156	027- 87F	retlw	sine4		213	05A- C89		movlw	clo	
157	028- 87A	retlw	sine5		214	05B- 029		movwf	t1c_lo	
158	029- 86F	retlw	sine6		215	05C- C29		movlw	ghi	
159	02A- 85F	retlw	sine7		216	05D- 02A		movwf	t2c_hi	
160	02B- 84C	retlw	sine8		217	05E- C4E		movlw	glo	
161	02C- 839	retlw	sine9		218	05F- 02B		movwf	t2c_lo	
162	02D- 829	retlw	sine10		219	060- 800		retlw	nil	
163	02E- 81E	retlw	sine11		220					
164	02F- 81A	retlw	sine12		221	061- 003		sleep		; prevent fritzin
165	030- 81E	retlw	sine13		g cpu from getting past					
166	031- 829	retlw	sine14		222					
167	032- 839	retlw	sine15		223	062- C16	su_red	movlw	red1_hi	; set up red box
168					tones					
169	033- C05	su_A	movlw	ahi ; set up for A s	224	063- 028		movwf	t1c_hi	
tandard pitch					225	064- CDB		movlw	red1_lo	
170	034- 028		movwf	t1c_hi	226	065- 029		movwf	t1c_lo	
171	035- CEA		movlw	alo	227	066- C1D		movlw	red2_hi	
172	036- 029		movwf	t1c_lo	228	067- 02A		movwf	t2c_hi	

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229      06B- C94      movlw   red2_lo
230      069- 02B      movwf   t2c_lo
231      06A- 800      retlw    nil
232
233      06B- 068
ant register      su_wait clrf   t1c_hi      ; zero out const
234      06C- 069      clrf     t1c_lo      ; no tones e.g.
freq = 0
235      06D- 06A      clrf     t2c_hi
236      06E- 06B      clrf     t2c_lo
237      06F- 800      retlw    nil
238
239      070- C02      do_15  movlw   _15mshi
240      071- 02C      movwf   dur_hi
241      072- C1E      movlw   _15mslo
242      073- 02D      movwf   dur_lo
243      074- A01      goto     twotone
244
245      075- C03      do_33  movlw   _33mshi
246      076- 02C      movwf   dur_hi
247      077- C74      movlw   _33mslo
248      078- 02D      movwf   dur_lo
249      079- A01      goto     twotone
250
251      07A- C05      do_66  movlw   _66mshi
252      07B- 02C      movwf   dur_hi
253      07C- CE8      movlw   _66mslo
254      07D- 02D      movwf   dur_lo
255      07E- A01      goto     twotone
256
257      07F- C26      do_500 movlw   _500msh
258      080- 02C      movwf   dur_hi
259      081- C27      movlw   _500msl
260      082- 02D      movwf   dur_lo
261      083- A01      goto     twotone
262
263      084- C95      do_2sec movlw   _2sechi
264      085- 02C      movwf   dur_hi
265      086- CC0      movlw   _2seclo
266      087- 02D      movwf   dur_lo
267      088- A01      goto     twotone
268
269
270      ;*****
271      ;*
272      ;* wake up *
273      ;*
274      ;*****
275
276      089- 040      wake_up clrw
277      08A- 002      option
278
279      08B- 645      btfsc   buttonc
280      08C- AE1      goto    test1
281      08D- 665      btfsc   buttond
282      08E- AE6      goto    test2
283
284      08F- 217      movf     secure, w
285      090- FA5      xorlw   secval
286      091- 743      btfss   z
287      092- ACF      goto    armed
288
289      093- 605      unarmed btfsc   buttona
290      094- A98      goto    bump

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291	095- 625		btfsc	buttonb	
292	096- A9C		goto	enter	
293	097- 003		sleep		
294					
295	098- 2B8				
296	099- 94F	bump	incf	digit	; increment
297	09A- 970		call	su_3rd	
298	09B- 003		call	do_15	
299			sleep		
300	09C- C04				
r 4 clicks		enter	movlw	4	; rotate pw buffe
301	09D- 03E				
302	09E- 372		movwf	temp	
303	09F- 373	rl	rlf	pwreg0	
304	0A0- 374		rlf	pwreg1	
305	0A1- 375		rlf	pwreg2	
306	0A2- 2FE		rlf	pwreg3	
307	0A3- A9E		decfsz	temp	
308			goto	rl	
309	0A4- 212				
and digit			movf	pwreg0,w	; push in right h
310	0A5- EF0				
311	0A6- 178		andlw	0F0H	
312	0A7- 078		andwf	digit	
313	0A8- 800		clrf	digit	
314			retlw	nil	
315	0A9- C04				
risons		chkpw	movlw	4	; make four compa
316	0AA- 03E				
317			movwf	temp	
318	0AB- 212				
319	0AC- F42		movf	pwreg0,w	
320	0AD- 643		xorlw	pass0	
321	0AE- 0FE		btfsc	z	
322	0AF- 213		decf	temp	
323	0B0- F12		movf	pwreg1,w	
324	0B1- 643		xorlw	pass1	
325	0B2- 0FE		btfsc	z	
326	0B3- 214		decf	temp	
327	0B4- F32		movf	pwreg2,w	
328	0B5- 643		xorlw	pass2	
329	0B6- 0FE		btfsc	z	
330	0B7- 215		decf	temp	
331	0B8- F52		movf	pwreg3,w	
332	0B9- 643		xorlw	pass3	
333	0BA- 0FE		btfsc	z	
334	0BB- 743		decf	temp	
335	0BC- ABE		btfss	z	
336	0BD- AC2		goto	noway	; no
337			goto	arm	; yes
338	0BE- 077				
register		noway	clrf	secure	; stomp security
339	0BF- 958				
340	0C0- 970		call	su_5th	
341	0C1- 003		call	do_15	
342			sleep		
343	0C2- CA5				
344	0C3- 037	arm	movlw	secval	
345	0C4- 93A		movwf	secure	
346	0C5- 97A		call	su_C	
347	0C6- 96B		call	do_66	
348	0C7- 97A		call	su_wait	
349	0C8- 941		call	do_66	
350	0C9- 97A		call	su_E	
			call	do_66	

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```
351      OCA- 96B      call    su_wait
352      OCB- 97A      call    do_66
353      OCC- 948      call    su_G
354      OCD- 97A      call    do_66
355      OCE- 003      sleep
356
357      OCF- 205      armed  movf    ra,w
358      ODO- F03      xorlw   00000011b      ; both buttons d
own?
359      OD1- 643      btfsz   z
360      OD2- AD8      goto    disarm
361
362      OD3- 605      btfsz   buttona
363      OD4- AF6      goto    quarter
364      OD5- 625      btfsz   buttonb
365      OD6- B02      goto    dime
366      OD7- 003      sleep
367
368      OD8- 077      disarm  clrf    secure
369      OD9- 072      clrf    pwreg0
370      ODA- 073      clrf    pwreg1
371      ODB- 074      clrf    pwreg2
372      ODC- 075      clrf    pwreg3
373      ODD- 078      clrf    digit
374      ODE- 933      call    su_A
375      ODF- 97A      call    do_66
376      OEO- 003      sleep
377
378      OE1- 040      test1   clrw      ; output a 440Hz
sine wave
379      OE2- 006      tris     dac
380      OE3- 933      call    su_A
381      OE4- 984      t1      call    do_2sec
382      OE5- AE4      goto    t1
383
384      OE6- 004      test2   clrwdt
385      OE7- 040      clrw      ; sweep DAC to t
est range, linearity, and
386      OE8- 006      tris     dac      ; amplifier clip
ping points
387      OE9- 2BE      incf     temp
388      OEA- 21E      movf     temp, w
389      OEB- 026      movwf    dac
390      OEC- 000      nop
391      OED- 000      nop
392      OEE- 000      nop
393      OEF- 000      nop
394      OF0- 000      nop
395      OF1- 000      nop
396      OF2- 000      nop
397      OF3- 000      nop
398      OF4- AE6      goto    test2
399
400      OF5- 003      ;
sleep      ; don't want fri
tzed processor
401      ;
next routines
402      OF6- 040      quarter clrw
403      OF7- 006      tris     dac
404      OF8- C05      movlw   5
405      OF9- 036      movwf    pulse
406      OFA- 962      gloop   call    su_red
407      OFB- 975      call    do_33
408      OFC- 96B      call    su_wait
409      OFD- 975      call    do_33
410      OFE- 2F6      decfsz  pulse
411      OFF- AFA      goto    gloop
412      100- 97F      call    do_500
413      101- 003      sleep
414
415      102- 040      dime    clrw
416      103- 006      tris     dac
417      104- C02      movlw   2
418      105- 036      movwf    pulse
419      106- 962      dloop   call    su_red
420      107- 97A      call    do_66
421      108- 96B      call    su_wait
422      109- 97A      call    do_66
423      10A- 2F6      decfsz  pulse
424      10B- B06      goto    dloop
425      10C- 97F      call    do_500
426      10D- 003      sleep
427
428      ;fin
429

===== Errors: 0 =====
```