Nicolas Rampelotto and Jason Pritchet:

Modifications Made:

1) Removed extra semicolon from line 6

2) Removed is_prime from last line and replaced with equations.

3) Added semicolon to ending the entity

4) Added parenthesis surrounding the NOT a AND b AND d

Error Messages From Adding 0 and 1 Minterms to Logic Description:

 Warning: Circuit failed for abcd=0000

 Warning: Circuit failed for abcd=0001

SS For Working Prime Number Circuit: