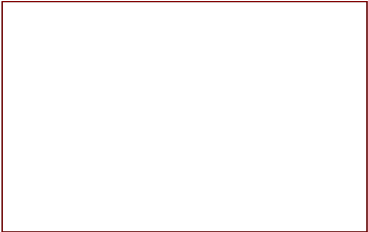Sheetname: Power Subsystem

File: Power Subsystem.kicad_sch

Sheetname: Central Subsystem

File: Central Subsystem.kicad_sch

Micro USB
J8
VBUS 1
D+ 3
D- 2
ID 4
Shield
GND

D2
SS54

Solar Panel
J7
+ 1
- 2

D1
SS54

C23
10 uF

C24
4.7 uF

C27
470 nF

VDC

Q2
SI2301CDS-T1-GE3

D3
SS54

D4
SS54

L8
100uH

R6
1.02

C28
10 uF

C29
4.7 uF

D5
SS54

D6
1N4148

R7
10K

Q3
SI2301CDS-T1-GE3

U5
HT7333-1
VIN 2 VOUT 3
GND 1

C30
100 nF

C31
10 uF

C32
10 uF

C34
100 nF

+3.3V
To Central Subsystem

U4
S1 1   G1 6
D1/D2   D1/D2
S2 3   G2
FS8205A

R8
1K

U6
OD 1   GND 6
VM 2   VCC 5
OC 3   TD 4
DW01A

C33
100 nF

R9
100

J9
- 2
+ 1
Battery

J5 and J8 are
0.1 inch spaced holes where
pins can be soldered.

VDC

C26
100 nF

U3
VG 1   DRV 10
GND 2   VCC 9
CHRG 3   CSP 8
DONE 4   BAT 7
COM 5   MPPT 6
CN3791

VDC

To Central Subsystem
CHRG
DONE

R3
120

C25
220 nF

VDC

R4
10K

R5
10K

To Central Subsystem

NSE Labs
Sheet: /Sheetname: Power Subsystem/
File: Power Subsystem.kicad_sch
Title: Power Subsystem
Size: USLetter   Date: 2023-09-21   Rev: A.1
KiCad E.D.A. kicad 7.0.2   Id: 3/3