

- clock
- reset
- led[1:3]
- lvds\_dout\_p
- lvds\_dout\_n
- lvds\_sout\_p
- lvds\_sout\_n
- lvds\_din\_p
- lvds\_din\_n
- lvds\_sin\_p
- lvds\_sin\_n
- clk
- mreset
- linkstart0
- linkdisable0
- autostart0
- tx\_write0
- tx\_data0[8:0]
- tick\_in0
- time\_in0[7:0]
- din0
- sin0
- buffer\_ready0
- dout0
- sout0
- tx\_ready0
- buffer\_write0
- rx\_data0[8:0]
- tick\_out0
- time\_out0[7:0]
- estadointerno0[9:0]
- linkstart1
- linkdisable1
- autostart1



