

Uh clock 🖺 reset led[1:3]

🆺 clk **¼** mreset U linkstart0 linkdisable0 👪 autostart0 1 tx_write0

U tick_in0

場 din0 ₩ sin0

U dout0 🖺 sout0 1 tx_ready0

U tick_out0

linkstart1 U linkdisable1 👪 autostart1

lvds_dout_p lvds_dout_n lvds_sout_p lvds_sout_n lvds_din_p lvds_din_n lvds_sin_p lvds_sin_n



tx_write1 **tx_data1**[8:0]

tick_in1 **time_in1**[7:0]

U buffer_ready1

谒 din1 🆺 sin1

場 dout1 🖺 sout1 🖔 tx_ready1 U buffer_write1 **x_data1**[8:0]

1 tick_out1 **time_out1**[7:0]

👪 somador

🖺 contador

U estado_codec

The reset_doubleclk