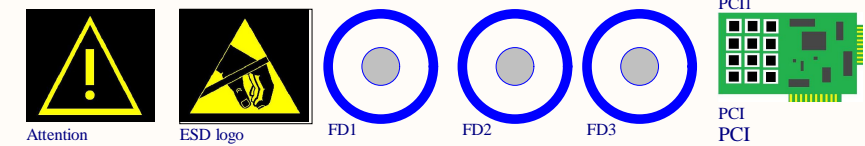
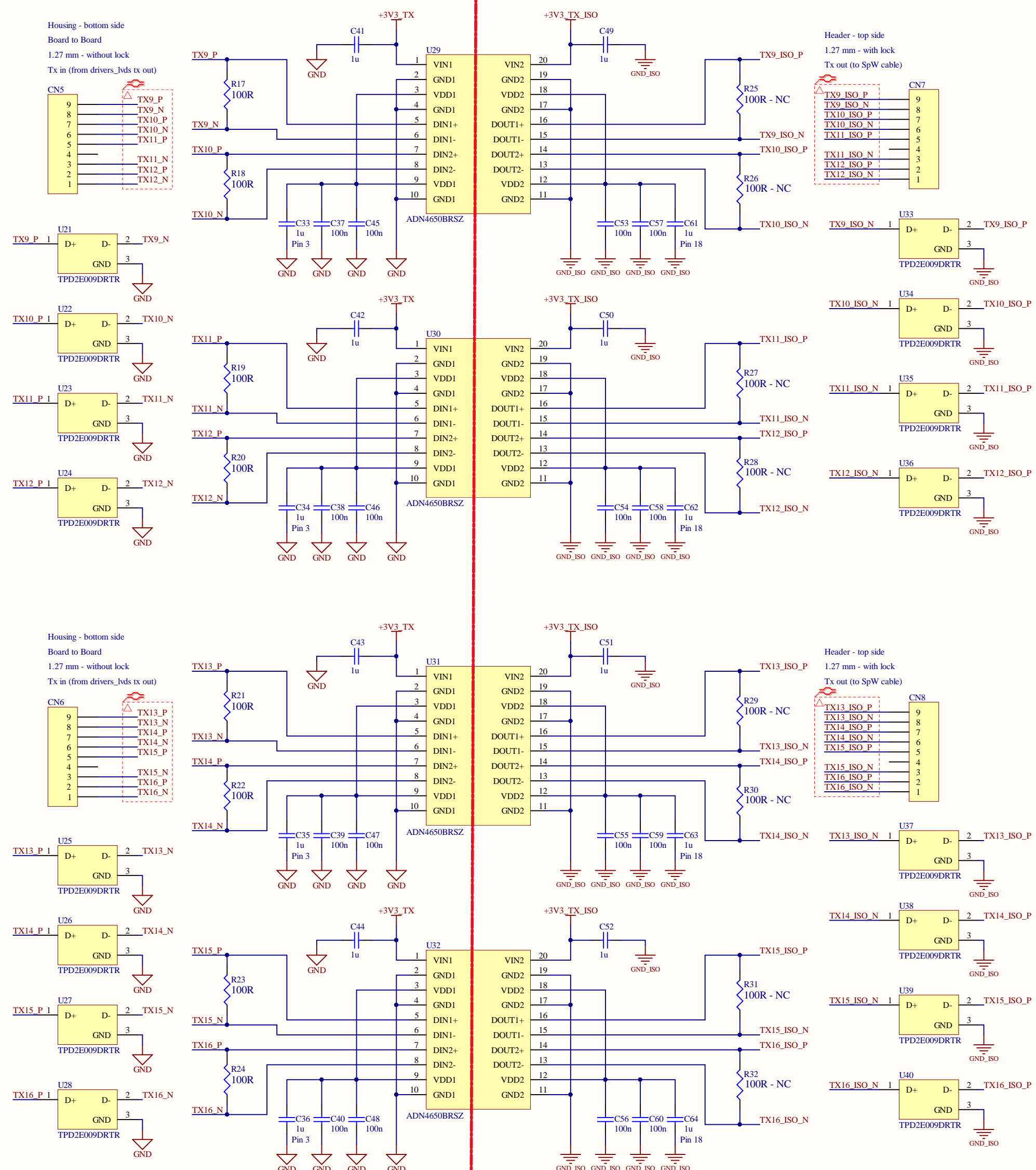
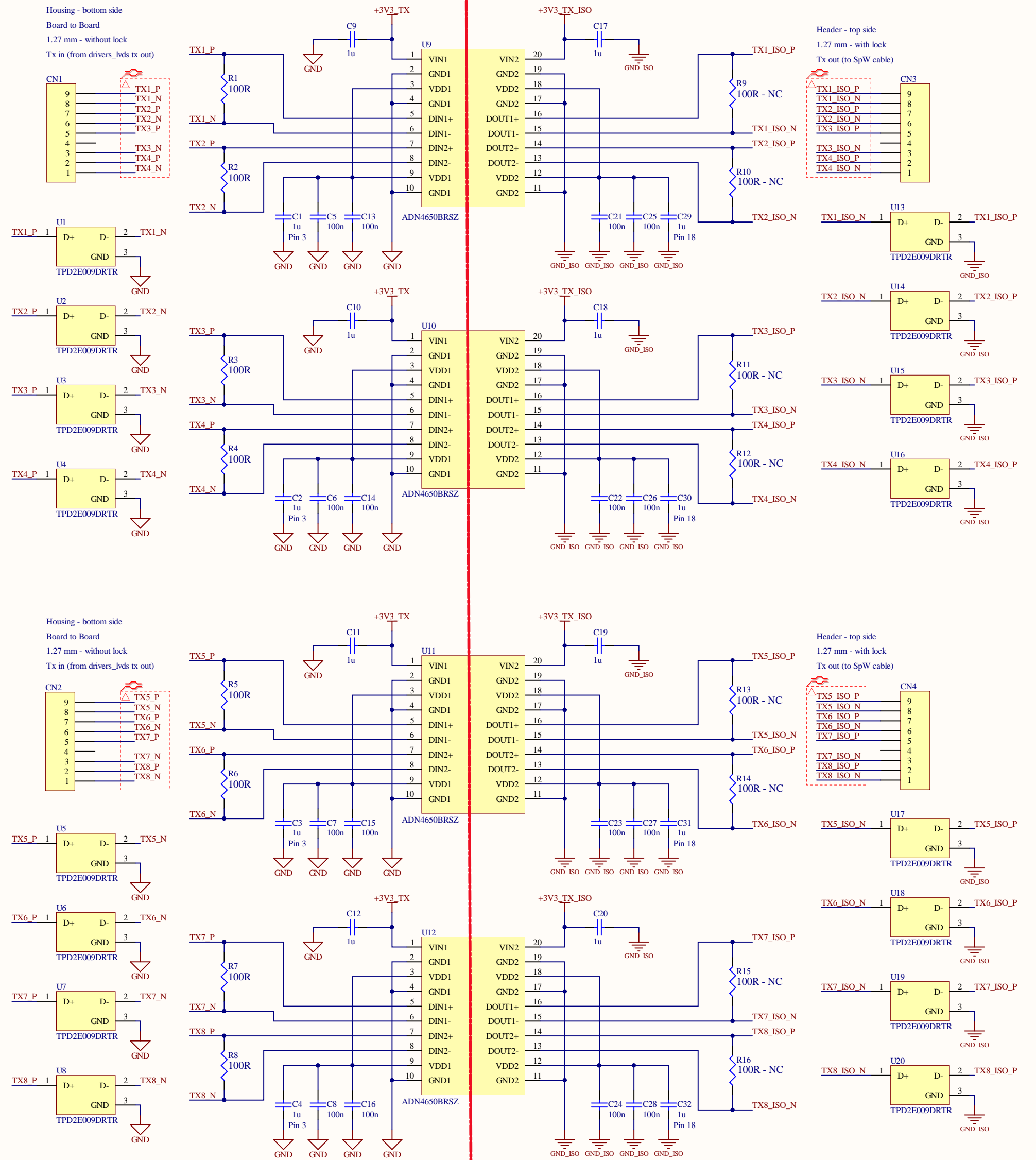


3750 Vrms - 600 Mbps
ISOLATED EXTERNAL SpW LVDS TX DRIVERS

Default: resistors: 0402 - 5%
Default: caps: cer. 0402 - X5R/X7R - 16V/25V
10 uF caps: cer. 0603 - X5R/X7R - 25V



3750 Vrms - 600 Mbps ISOLATED EXTERNAL SpW LVDS TX DRIVERS



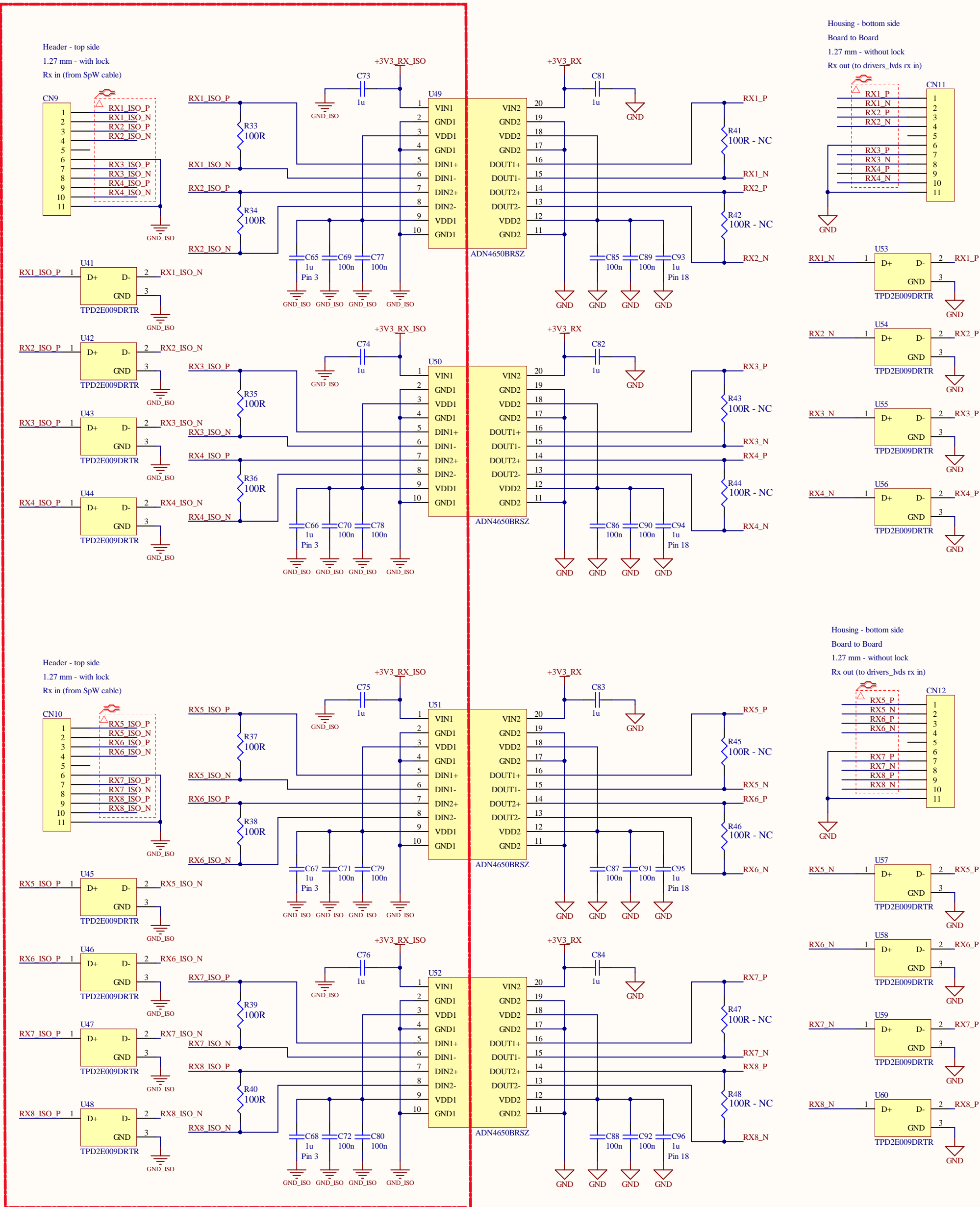
All TX Drivers can be enabled / disabled by controlling their iso side power supply (+3V3_TX_ISO)

All LVDS lines protected with Dual-Matching TVS Diodes TPD2E009

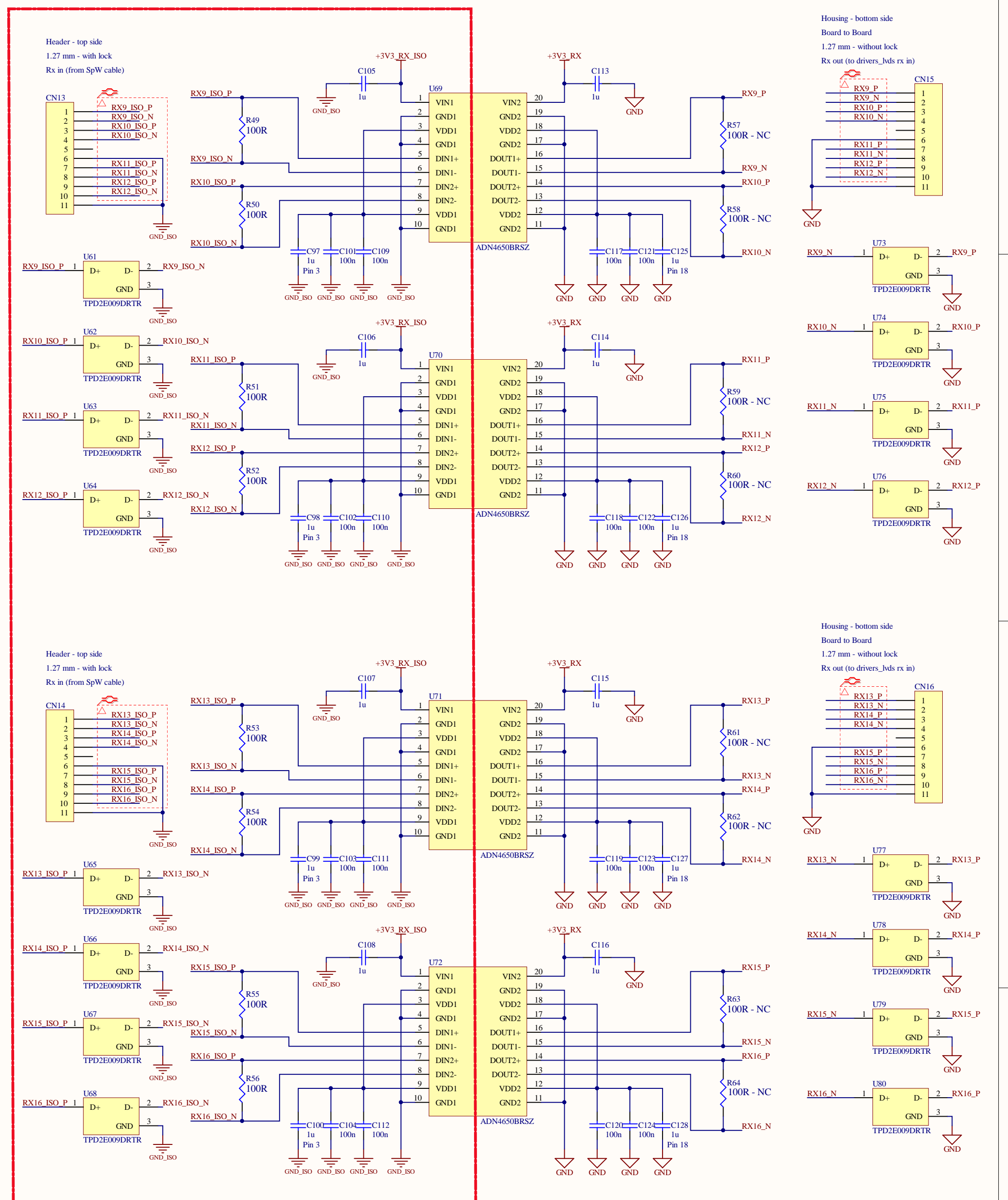
Rev 1_00 -> 1_01: all output lvds drivers 100R resistors must be NC

Title Tx iso drivers		
Size A2	Number iso_simucam.PrjPCB	Revision 1_01
Date: 31/08/2018	Sheet 1 of 4	
File: C:\projeto\iso_lds_tx.SchDoc	Drawn By: CCB	

3750 Vrms - 600 Mbps
ISOLATED EXTERNAL SpW LVDS RX DRIVERS

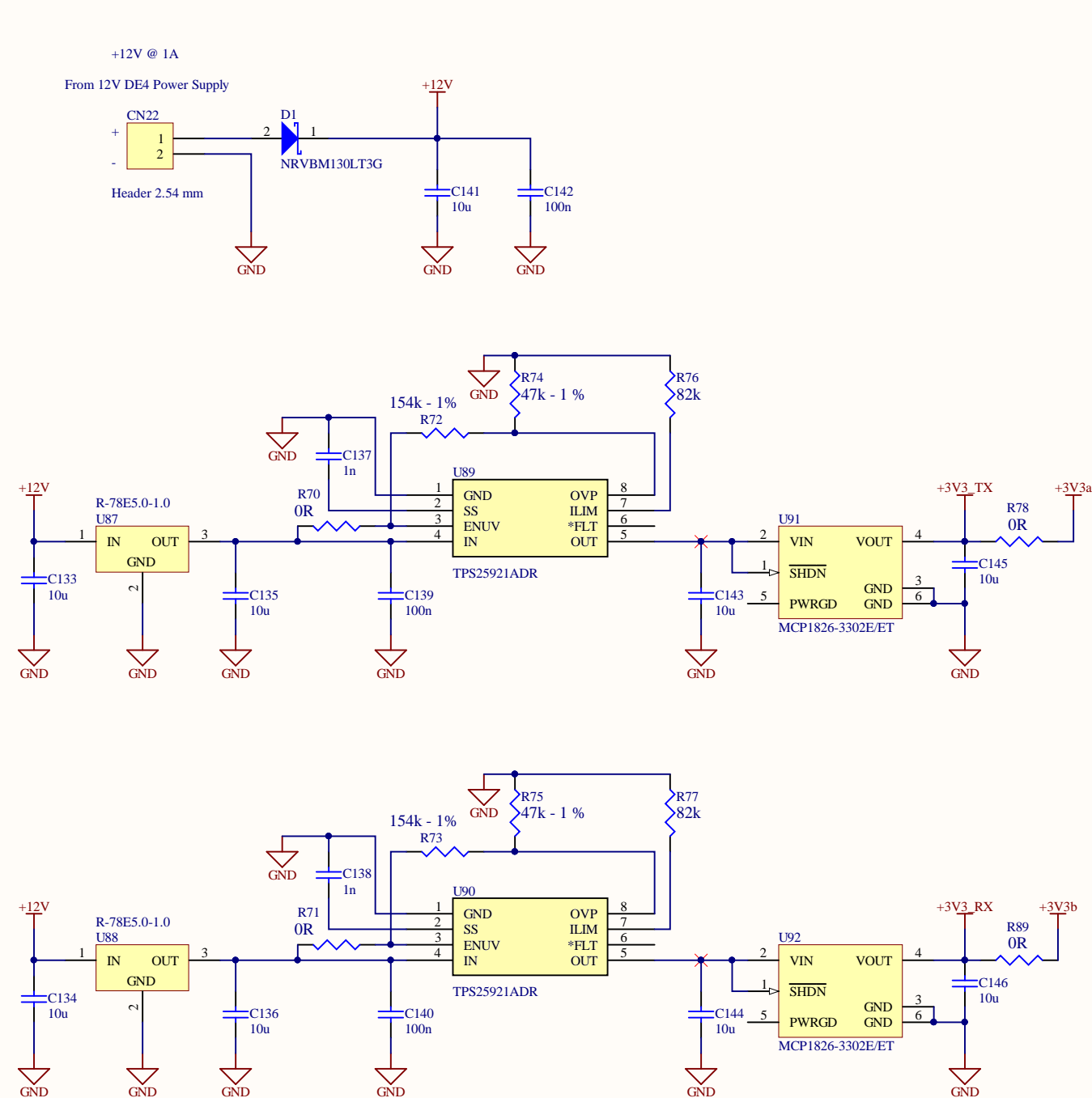


3750 Vrms - 600 Mbps
ISOLATED EXTERNAL SpW LVDS RX DRIVERS



All LVDS lines protected with Dual-Matching TVS Diodes TPD2E009

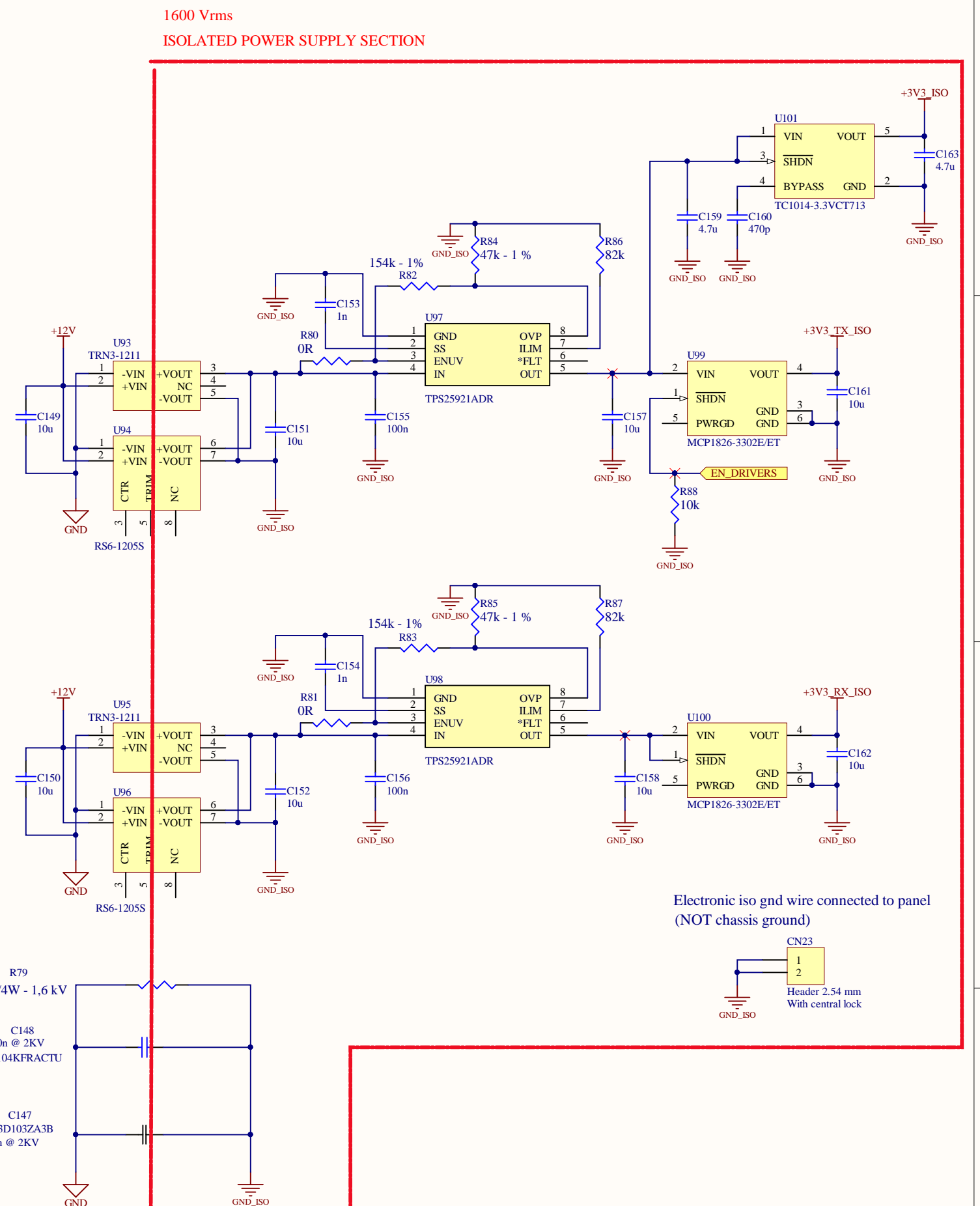
Title Rx iso drivers		
Size A2	Number iso_simucam.PrjPCB	Revision 1_01
Date: 31/08/2018	Sheet2 of 4	
File: C:\projeto\iso_hds_rx.SchDoc	Drawn By: CCB	



R78E5.0-1.0 -> Buck regulators: to +5V @ 1A

TRN3-1211 / RS6-1205S -> Iso buck regulators: to +5V @ 600 mA -> only one must be assembled

All +3V3 lines protected with an eFuse @ 860 mA and 6V max input OVP (TPS25921A)



Title Iso Power			
Size A3	Number iso_simucam.PrjPCB		Revision 1_01
Date:	31/08/2018	Sheet 4 of	4
File:	C:\projetos\...\iso_power.SchDoc	Drawn By:	CCB