

TC_SCAMxx_FEE_HK_UPDATE_VALUE (PLC00096)					Description
	Default	Min	Max	Size	
TYPE	250	-	-	-	PUS command Type
SUB_TYPE	58	-	-	-	PUS command Sub-Type
FEE_HK_UPDATE_VALUE_FEESIM_INSTANCE	1	1	8	UINT8	FEESIM instance to receive the command [NFEESIM-UR-647]
FEE_HK_UPDATE_VALUE_ID_VARIABLE	0	0	255	UINT8	RMAP HK Register ID [defined in this document]
FEE_HK_UPDATE_VALUE	0	0	4294967295	UINT32	Raw value of the RMAP HK Register
RMAP HK Register	RMAP HK ID	Min	Max		
TOU_SENSE_1[15:0]	0	0	65535		
TOU_SENSE_2[15:0]	1	0	65535		
TOU_SENSE_3[15:0]	2	0	65535		
TOU_SENSE_4[15:0]	3	0	65535		
TOU_SENSE_5[15:0]	4	0	65535		
TOU_SENSE_6[15:0]	5	0	65535		
CCD1_TS[15:0]	6	0	65535		
CCD2_TS[15:0]	7	0	65535		
CCD3_TS[15:0]	8	0	65535		
CCD4_TS[15:0]	9	0	65535		
PRT1[15:0]	10	0	65535		
PRT2[15:0]	11	0	65535		
PRT3[15:0]	12	0	65535		
PRT4[15:0]	13	0	65535		
PRT5[15:0]	14	0	65535		
ZERO_DIFF_AMP[15:0]	15	0	65535		
CCD1_VOD_MON[15:0]	16	0	65535		
CCD1_VOG_MON[15:0]	17	0	65535		
CCD1_VRD_MON_E[15:0]	18	0	65535		
CCD2_VOD_MON[15:0]	19	0	65535		
CCD2_VOG_MON[15:0]	20	0	65535		
CCD2_VRD_MON_E[15:0]	21	0	65535		
CCD3_VOD_MON[15:0]	22	0	65535		
CCD3_VOG_MON[15:0]	23	0	65535		
CCD3_VRD_MON_E[15:0]	24	0	65535		
CCD4_VOD_MON[15:0]	25	0	65535		
CCD4_VOG_MON[15:0]	26	0	65535		
CCD4_VRD_MON_E[15:0]	27	0	65535		
VCCD[15:0]	28	0	65535		
VRCLK_MON[15:0]	29	0	65535		
VICLK[15:0]	30	0	65535		
VRCLK_LOW[15:0]	31	0	65535		
5VB_POS_MON[15:0]	32	0	65535		
5VB_NEG_MON[15:0]	33	0	65535		
3V3B_MON[15:0]	34	0	65535		
2V5A_MON[15:0]	35	0	65535		
3V3D_MON[15:0]	36	0	65535		

2V5D_MON[15:0]	37	0	65535
1V5D_MON[15:0]	38	0	65535
5VREF_MON[15:0]	39	0	65535
VCCD_POS_RAW[15:0]	40	0	65535
VCLK_POS_RAW[15:0]	41	0	65535
VAN1_POS_RAW[15:0]	42	0	65535
VAN3_NEG_MON[15:0]	43	0	65535
VAN2_POS_RAW[15:0]	44	0	65535
VDIG_RAW[15:0]	45	0	65535
VDIG_RAW_2[15:0]	46	0	65535
VICLK_LOW[15:0]	47	0	65535
CCD1_VRD_MON_F[15:0]	48	0	65535
CCD1_VDD_MON[15:0]	49	0	65535
CCD1_VGD_MON[15:0]	50	0	65535
CCD2_VRD_MON_F[15:0]	51	0	65535
CCD2_VDD_MON[15:0]	52	0	65535
CCD2_VGD_MON[15:0]	53	0	65535
CCD3_VRD_MON_F[15:0]	54	0	65535
CCD3_VDD_MON[15:0]	55	0	65535
CCD3_VGD_MON[15:0]	56	0	65535
CCD4_VRD_MON_F[15:0]	57	0	65535
CCD4_VDD_MON[15:0]	58	0	65535
CCD4_VGD_MON[15:0]	59	0	65535
IG_HI_MON[15:0]	60	0	65535
IG_LO_MON[15:0]	61	0	65535
TSENSE_A[15:0]	62	0	65535
TSENSE_B[15:0]	63	0	65535
FPGA_MINOR_VERSION[7:0]	64	0	255
FPGA_MAJOR_VERSION[3:0]	65	0	15
BOARD_ID[8:0]	66	0	511