NAYMAN LEUNG

3109 Schubert Drive, Silver Spring, MD 20904
301-233-9552
naymanl2@illinois.edu
https://www.linkedin.com/in/nayman-leung/
https://github.com/NSLeung

WORK EXPERIENCE

[CANCELLED DUE TO COVID-19 PANDEMIC]

EMBEDDED SOFTWARE ENGINEERING INTERN / Viasat, Germantown, MD

[JUNE 2019 – AUGUST 2019]

SOFTWARE ENGINEERING INTERN / Collins Aerospace, Cedar Rapids, IA

- Executed Robot Framework system test suites on remote rigs to verify integrity of software loaded onto Secure Server Routers embedded in Airbus A320/A330 aircraft
- Communicated daily goals/progress with Agile Scrum team; assigned and reviewed code on Jira

[JUNE 2018 – AUGUST 2018]

RESEARCH INTERN / NIST for Neutron Research (NCNR), Gaithersburg, MD

- Designed Frontend JavaScript-based Sequence Editor (JSSSE) GUI in HTML5/JavaScript/NodeJS for interacting with Protein DataBank (PDB) fields
- Integrated zoomable granularity with DragSelectJS to emulate a protein sequence selector

PROJECT EXPERIENCE

DormDash - Web Application for queueing up students in the Dining Hall safely (CS 411 Databases)

- Designed Frontend user interface and admin interface for CRUD using MaterialUI and ReactJS
- Connected Frontend to Backend REST endpoints on Serverless framework with fetch API calls
- Implemented a recommendation feature based on the user's current location in MySQL
- Backfilled database and displayed weekly queue capacity history on D3.js barchart

RISC-V Processor (ECE 411 Computer Organization)

- Wrote foundational datapath, control module, and supporting microarchitecture for RISC-V ISA
- Optimized memory access latency with a 2-way set associative write-back cache
- Verified DUT in ModelSim by feeding RISC-V Assembly programs into SystemVerilog testbenches

Linux-based Operating System (ECE 391 Systems Programming)

- Developed simple OS in C and inline x86 Assembly that supports Interrupts/Exceptions/System Calls, Read-only Filesystem, terminal with history
- Unit-tested program execution on terminal with given binaries

EDUCATION

[GRADUATED MAY 2020 | ACCEPTED UIUC MASTERS OF ENGINEERING IN ECE FALL 2020]

B.S in Computer Engineering / University of Illinois Urbana-Champaign

3.40/4.00 GPA | James Scholar Program

SKILLS

Languages: C, C++, Verilog, Python, Bash, ReactJS, Java, HTML5/CSS/JavaScript, Haskell

Web Stacks: ReactJS|MaterialUI|D3.js|Serverless(TS)|AWS Aurora MySQL; PugJS Template|MaterializeCSS & Bootstrap| NodeJS | Mongoose

FPGA Software Tools: Intel Quartus Prime FPGA Design, ModelSim Altera, Xilinx Vivado

ACTIVITIES

IEEE Circuits Technical Advancement Group (TAG)

Co-led series of technical lectures on Analog/Digital Circuit basics and hands-on projects.

Laboratory for Advanced Space Systems at Illinois (LASSI)

Developed and tested Linux 'at' daemon utility for Flight Software going onboard Cooling, Annealing, Pointing Satellite (CAPSat) on a small undergraduate team.