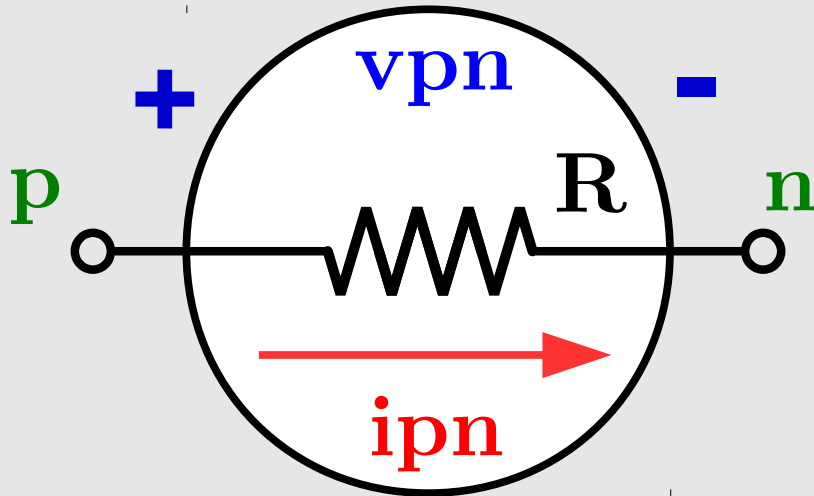


# Resistor



name: myR

2 terminals: p, n

branch: pn

I/Os: ipn, vpn

model equation:  $ipn = \frac{vpn}{R}$

explicit output: ipn

$$ipn = \frac{d}{dt} \left( \underset{\substack{\nearrow \\ qe}}{0} \right) + \underset{\substack{\nearrow \\ fe}}{\frac{vpn}{R}}$$

parameter: R

# Resistor

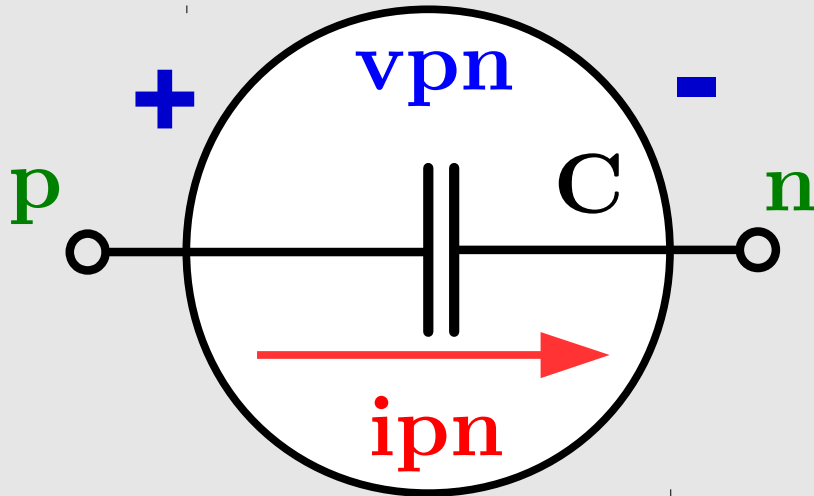
1 define parameter

```
1 function MOD = myR()
2     MOD = ee_model();
3     MOD = add_to_ee_model(MOD, 'name', 'myR');
4     MOD = add_to_ee_model(MOD, 'terminals', {'p', 'n'});
5     MOD = add_to_ee_model(MOD, 'explicit_outs', {'ipn'});
6
7     MOD = add_to_ee_model(MOD, 'parms', {'R', 1000.0});
8
9     MOD = add_to_ee_model(MOD, 'fe', @fe);
10    MOD = add_to_ee_model(MOD, 'qe', @qe);
11
12    MOD = finish_ee_model(MOD);
13 end % myR
14
15 function out = fe(S)
16     v2struct(S);
17
18    ipn_fe = vpn/R;
19
20    out(1, 1) = ipn_fe;
21 end % fe
22
23 function out = qe(S)
24     v2struct(S);
25
26    ipn_qe = 0;
27
28    out(1, 1) = ipn_qe;
29 end % qe
```

2 fill in functions

$$ipn = \frac{d}{dt} \underset{qe}{(0)} + \underset{fe}{\frac{vpn}{R}}$$

# Capacitor



name: **myC**

2 terminals: **p, n**

branch: **pn**

I/Os: **ipn, vpn**

model equation: **ipn** =  $\frac{d}{dt} (C \cdot \text{vpn})$

explicit output: **ipn**

parameter: **C**

$$\text{ipn} = \frac{d}{dt} (C \cdot \text{vpn}) + 0$$

↑
↑  
 qe                      fe

# Capacitor

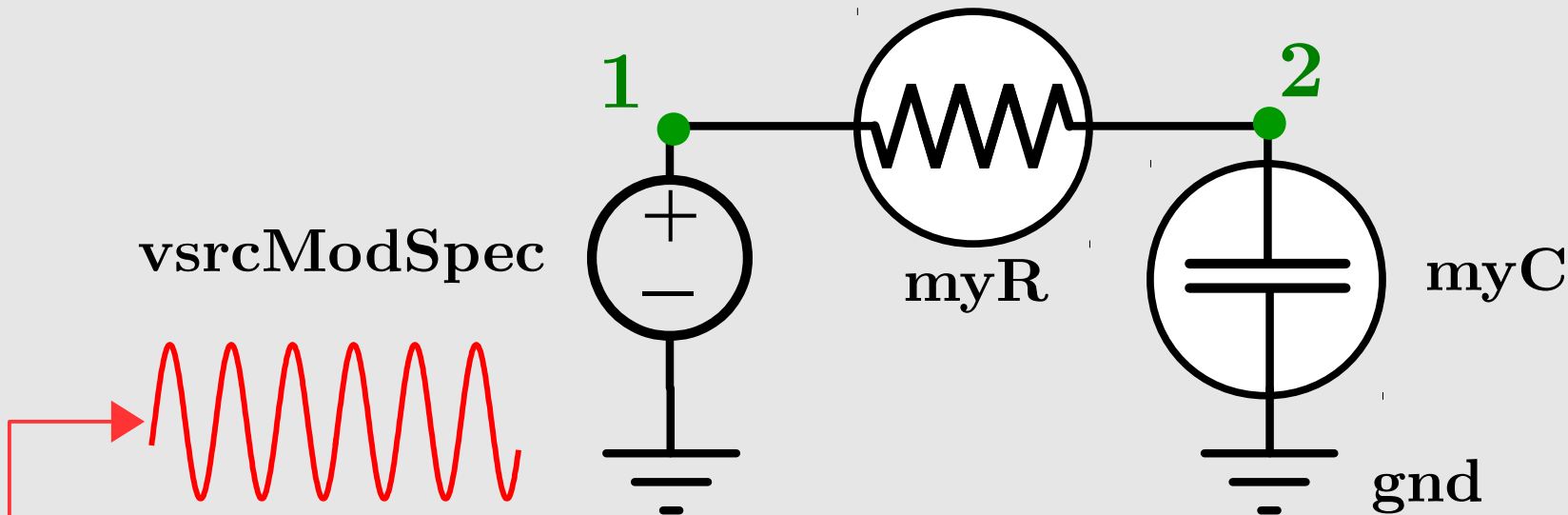
1 define parameter

```
1 function MOD = myC()
2     MOD = ee_model();
3     MOD = add_to_ee_model(MOD, 'name', 'myC');
4     MOD = add_to_ee_model(MOD, 'terminals', {'p', 'n'});
5     MOD = add_to_ee_model(MOD, 'explicit_outs', {'ipn'});
6
7     MOD = add_to_ee_model(MOD, 'parms', {'C', 1e-6});
8
9     MOD = add_to_ee_model(MOD, 'fe', @fe);
10    MOD = add_to_ee_model(MOD, 'qe', @qe);
11
12    MOD = finish_ee_model(MOD);
13 end % myC
14
15 function out = fe(S)
16     v2struct(S);
17
18     ipn_fe = 0;
19
20     out(1, 1) = ipn_fe;
21 end % fe
22
23 function out = qe(S)
24     v2struct(S);
25
26     ipn_qe = C*vpn;
27
28     out(1, 1) = ipn_qe;
29 end % qe
```

2 fill in functions

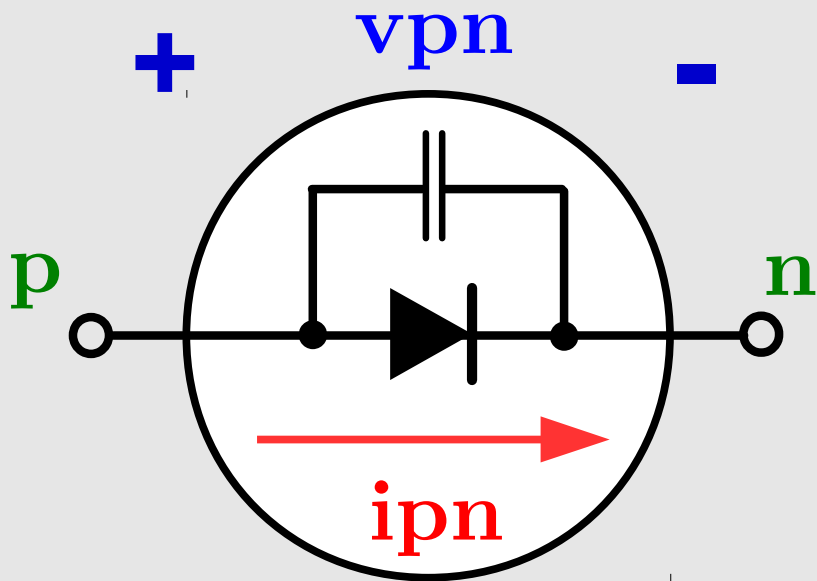
$$\text{ipn} = \frac{d}{dt} (\underset{\text{qe}}{C} \cdot \underset{\text{fe}}{\text{vpn}}) + 0$$

# RC circuit



```
1 function cktnetlist = myRC_ckt()
2     cktnetlist.cktname = 'myRC_ckt';
3     cktnetlist.nodenames = {'1', '2'}; % non-ground nodes
4     cktnetlist.groundnodename = 'gnd';
5
6     cktnetlist = add_element(cktnetlist, myR(), 'R1', {'1', '2'}, {{'R', 1000}});
7     cktnetlist = add_element(cktnetlist, myC(), 'C1', {'2', 'gnd'}, 1e-6);
8
9     mysinfunc = @(t, args) sin(2*pi*1000*t);
10
11     cktnetlist = add_element(cktnetlist, vsrcModSpec(), 'V1', ...
12         {'1', 'gnd'}, {}, {{'DC', 1}, {'AC', 1}, {'TRAN', mysinfunc, []}});
13 end % myRC_ckt
```

# Diode



name: diodeC

2 terminals: **p**, **n**

branch: **pn**

I/Os: **ipn**, **vpn**

model equation: **ipn** =  $\frac{d}{dt} (C \cdot \mathbf{vpn}) + I_S \cdot \left( e^{\frac{\mathbf{vpn}}{V_T}} - 1 \right)$

explicit output: **ipn**

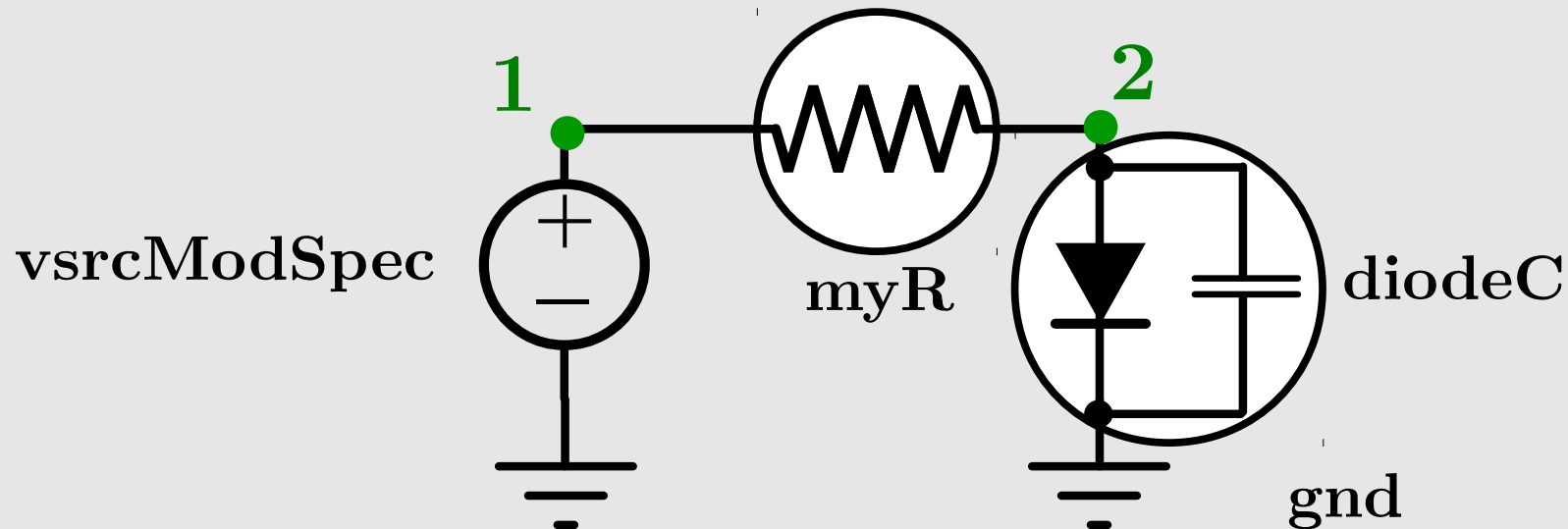
parameters: **C**, **I<sub>S</sub>**, **V<sub>T</sub>**

$$\mathbf{ipn} = \frac{d}{dt} (qe) + fe$$

$$qe = C \cdot \mathbf{vpn}$$

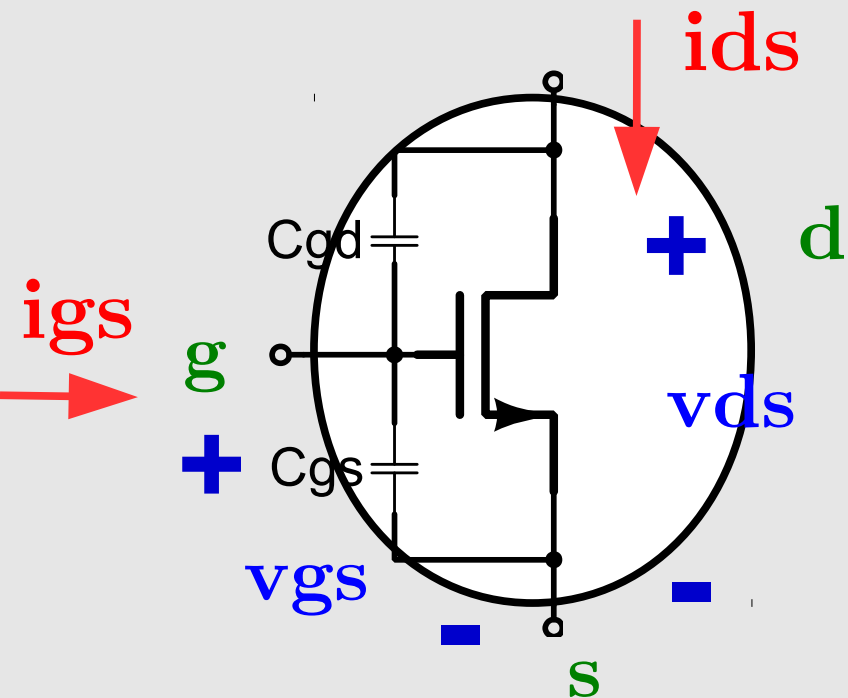
$$fe = I_S \cdot \left( e^{\frac{\mathbf{vpn}}{V_T}} - 1 \right)$$

# Vsrc-R-diode circuit



```
1 function cktnetlist = myR_diodeC_ckt()
2     cktnetlist.cktname = 'myR_diodeC_ckt';
3     cktnetlist.nodenames = {'1', '2'}; % non-ground nodes
4     cktnetlist.groundnodename = 'gnd';
5
6     cktnetlist = add_element(cktnetlist, vsrcModSpec(), 'V1', ...
7         {'1', 'gnd'}, {}, {'DC', 1});
8     cktnetlist = add_element(cktnetlist, myR(), 'R1', {'1', '2'}, {'R', 1});
9     cktnetlist = add_element(cktnetlist, diodeC(), 'D1', {'2', 'gnd'});
10 end % myR_diodeC_ckt
```

# Schichman-Hodges MOS Model



name: myNMOS

3 terminals: d, g, s

branches: ds, gs

model equation:

$$\begin{aligned} \text{ids\_fe} &= \begin{cases} 0 & \text{if } \text{vgs} \leq V_{th} \\ \beta \left[ (\text{vgs} - V_{th}) - \frac{\text{vds}}{2} \right] \text{vds}, & \text{if } \text{vgs} > V_{th} \text{ and } \text{vgs} < \text{vds} + V_{th} \\ \frac{1}{2} \cdot \beta \cdot (\text{vgs} - V_{th})^2, & \text{if } \text{vds} \geq \text{vgs} - V_{th} \end{cases} \\ \text{igs\_fe} &= 0 \end{aligned}$$

explicit output: [ids; igs]

parameters:  $V_{th}$ ,  $\beta$ , Cgs, Cgd