

Technical University of Cluj-NapocaAutomation and Computer Science

Year IV, Semester I

Distributed Control Systems Project

Traffic Intersection Controller

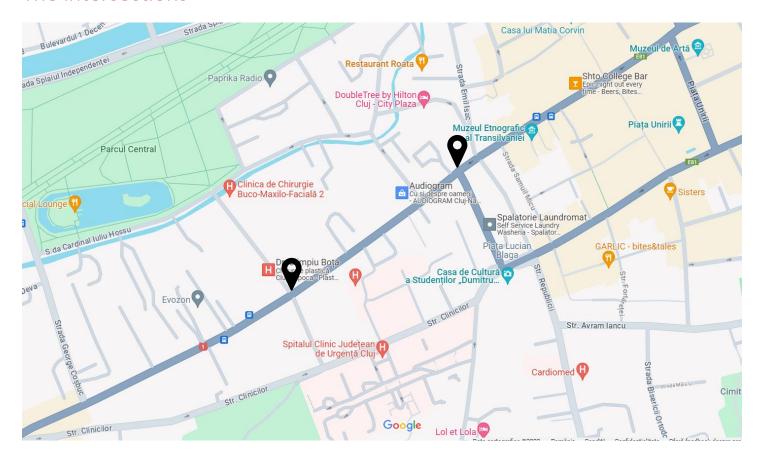
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Group: 30343

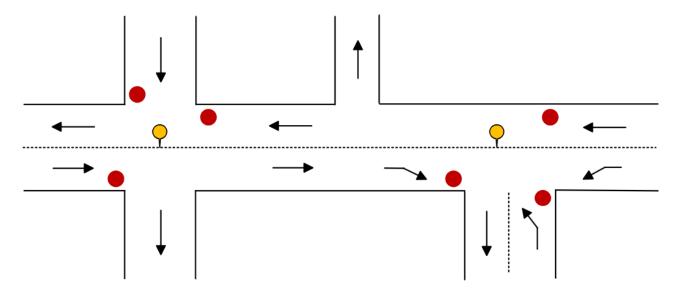
Specifications

According to the map given to each team, develop a controller for each intersection (plant), that controller is a closed-loop one (with the $in_{(1..n)}$ input channels that is connected to its intersection's output channels op_(1..n) and an Intersections (with the OPs output channels). The controller must have dynamic delays feature to extend the time of the green light in case of a traffic jam.

The intersections

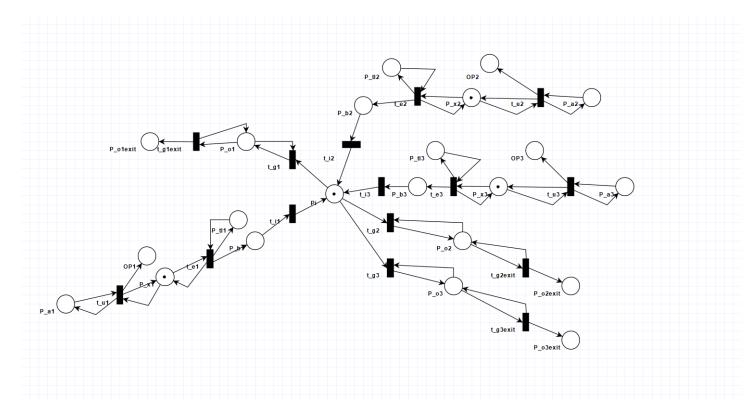


Simplified Model

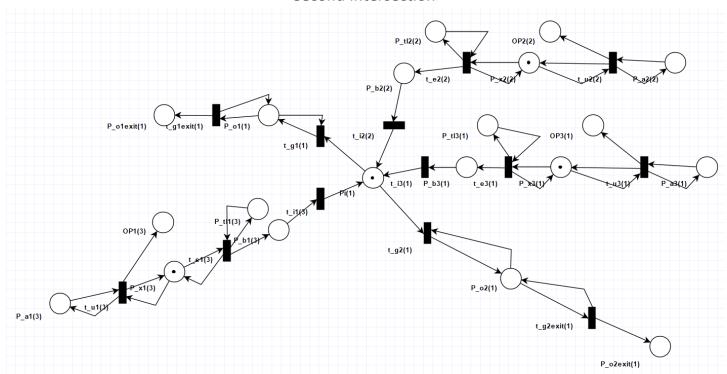


OETPN Model for Plant

First Intersection



Second Intersection



Guards & Mappings for Plant

First Intersection – for the second intersection is the same but with one more input lane.

```
Juput lanes: P-ax; P-bx; P-az; P-bz; P-as,
P-bs; -> Data Car

P-x1; P-x2; P-x3-> Data Conquere

> P-tex; P-tex; P-tes-> Data String

ORX; ORZ; ORS -> Data Junes Jer
                Output lanes: 1701, 7-02 -> Duta lan Gueve

7-01 exit, 7-02 exit -> Bota Can

7-02 Duta Traves for

7-i Data Conquere
                 GRD& MAP
                  t-11: SP-an!=HULL 22 P-Xn odd ear
P-X1. add (P-an)
P-an!=HULL && P-Xn can mot add cans
O-Pn. Semd ("Fulle")
                  ! sauce for the & the
                t-e1: {(P-x1 have Can && P_+e1 == "gneen")}
P-x1-pop Element Without Tanger (p-h1)
P-t0: 10:
              ! Same for L-el & t-es
t-in P-bn!=HULL & & P-i can add cans
P-i. add Element (P-bn)
     ! Same: +-12 & +-13
        +-91: P-i. Have Canforte & & P-01 Candad Cans
P-i. Pop Element with Tanget To Greave (P-01)
! Same +-95
t-grexit P-01. Have Can

T-grexit P-01. Pot Element (P-01 Exit)

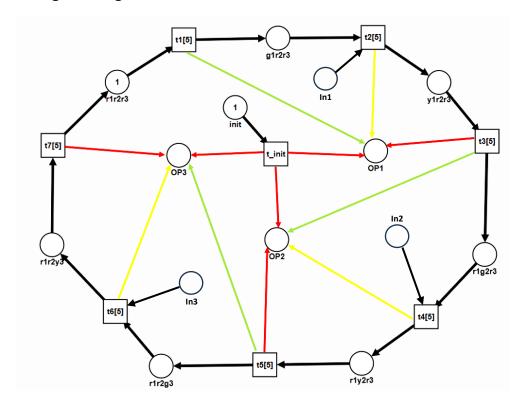
T-out: P-03 Exit! = Llow

P-08 Exit. Send

Over Wolwor (P03)
```

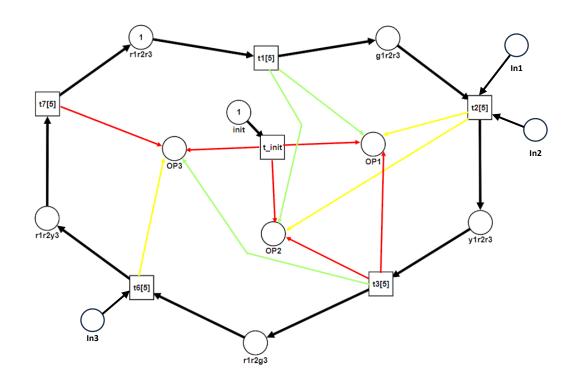
First Intersection

• The 3 traffic lights are green one at a time.



Second Intersection

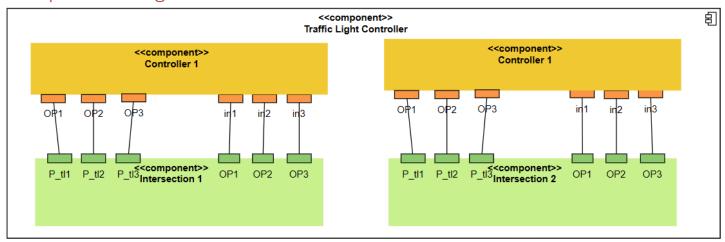
- OP3 traffic light is green: the other 2 must be red.
- OP3 TL is red: the other 2 can be green simultaneously.



Guards & Mappings for Controllers

```
PLACE TYPES
                                                     to: [ girars ] = 0 & & int == 0
   in 1 - Data String
                      ursus, girsus, girsus,
                                                             OPI. Send Over Network (" yellow")
   in 2: Bata String.
                                                             gresus = gresus
                      rigars, rigars, rirags,
                                                            Dynamiche Pay (" five ")
   in 3: Data String
                       rirzyz,: Data String
                                                            grars != 0 & & int! = 0
   OPI, OP2, OP3 Data Transfer
                                                            OPI. SendOverNetwork ("yeeRaw")
   t_init Data String
                                                             drisus = drisus.
  Guards & Mappings
                                                            Dynamic Delay (u tenu)
t_ini : ( init! = 0
                                                     t3: ( girzr3 ! = 0
             OPI. Send Over Network (int)
                                                            LY 3513 = AVLSUS
            OP2. Send Ovor Network (init)
                                                            OPI. Send Over Network ("red")
            003. Send Ovor Network (init)
                                                           ( OP 2. Send Over Network ("green")
            init. Make Null
                                                     tu: ( rigars != 0 88 in2 == 0
        111513 1 = 0
                                                             0P2. Send Over Network (" yellow")
         OPI. Send Over Wetwork (" green")
                                                            119213 = 119213
        Grisis = Linsus
                                                           (Dynamic Delay (" five")
 th: ( rigar3! = 0 &2 in2! = 0
                                                      to: ( rir2 93 ! = 0 88 in3! = 0
        OP2. Send Over Network (" yellow")
                                                            OP3. Send Ovor Network (" yeecow")
        riy213 = rig213
                                                            rx243 = rx293
       (Dynamic Delay (" ten")
                                                           ( Dynamic Delay ("ten")
 t5: ( rights ! = 0
                                                     tx: [ rirzy3 ! = 0
       r1293 = 14213
                                                            093. Send Over Network (u redu)
        OP2 SendOver Network (a reda)
                                                           Chusus = wusas
       093 SendOverNetwork ("green")
                                                     The same peace types, guards and
 t6: ( rir293! = 0. 88 in3 == 0
                                                      mappings apply for controller 2 too:)
       003. Send Over Network (" yellow")
       111293 = 111293
     ( Dynamic Delay ( u five u)
```

Component Diagram



Implementation

The entire system (plants + controllers) is implemented in Java using OERTPN Framework. The repository can be found on GitHub:

github.com/NTimea302/Traffic_Intersection_Controller

Testing

We tested the application for the following use cases:

- 1. Send a car from the 1st intersection, that should go through the middle street and exit from one of the exit lanes from the 2nd intersection.
- 2. Traffic jam: for each intersection, create a traffic jam case by sending the maximum number of cars to the input lane of the intersection, start the controller, then send the last car. The controller should receive a signal from the plant (intersection) and the transition that is responsible for sending a yellow light to that lane where you input the cars to, should have changed the delay to 10 sec. Let the controller OETPN run until it reaches the same transition (2 loops) to show that the delay is changed back to 5 sec.

Screen shots from the execution

