#### Show

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# A.2.1.2 Integer Type Logic System Variables - #L System Variables

Variable Name	Description	READ	WRITE		
Scan Time	•	•	•		
#L_ScanTime	The time from the start of step 0 of the next scan				
#L_AvgScanTime	The average of 64 #L_ScanTime cycles	4	-		
#L_MinScanTime	inScanTime The minimum scan time for #L_ScanTime				
#L_MaxScanTime	The maximum scan time for #L_ScanTime	1	-		
#L_ScanCount	Number of scans	✓	-		
#L_LogicTime	The time from the start of step 0 to the END instruction	1	-		
#L_AvgLogicTime	The average of 64 #L_LogicTime cycles	4	-		
#L_MinLogicTime	The minimum logic time for #L_LogicTime	4	-		
#L_MaxLogicTime	The maximum logic time for #L_LogicTime	4	-		
Status	·				
#L_Status	Logic status information	₩	-		
#L_Platform	Code number of the display unit platform	1	-		
#L_Version	Logic firmware version	₩	-		
#L_EditCount	Number of online edits	✓	-		
#L_ForceCount	Cumulative count of variables that are forcedly changed	✓	-		
#L_IOInfo	I/O driver information	4	-		
#L_LogicInfo	Logic Information	4	-		
#L_IOMasterDrv* <u>*1</u>	Master I/O Driver Extended Information	Depends on the I/O Driver	Depends on the I/O Driver		
#L_IOMasterDiag* <u>*4</u>	Master I/O Driver Self Diagnosis Information	Depends on the I/O Driver	Depends on the I/O Driver		

System Settings	<i>3</i>	,			
#L_ConstantScan	Logic startup frequency	₩	-		
#L_PercentScan	Logic operation rate	*	-		
#L_WatchdogTime	Logic WDT value	*	-		
#L_AddressRefreshTime	#L_AddressRefreshTime Connection device address refresh time				
Time					
#L_Time	Time information	₩	-		
Operation Information					
#L_Command	Changes the logic operation mode	₩	✓		
#L_LogicMonitor	The logic monitor startup switch	₩	₩		
#L_LogicMonStep	Indicates the steps for displaying the logic monitor	4	✓		
I/O Status			•		
#L_IOStatus	Statu so I/O Driver	₩	-		
Error information					
#L_CalcErrCode	Storage area for calculation error codes	✓	-		
#L_FaultStep	Storage area for the step Number of the calculation error				
#L_FaultLogicScreen	Storage area for the logic screen number of the calculation error	✓	-		
Logic Stop					
#L_StopScans	Number of logic stop scans	₩	✓		
Retentive Variable Backup		_	<b>'</b>		
#L_BackupCmd	Backup Command	₩	₩		
LT Common <sup>*2*3</sup>					
#L_ExIOFirmVer	Extended I/O board firmware version	₩	-		
#L_ExIOSpCtrI	Special I/O control	₩	✓		
#L_ExIOSpOut	Special output	₩	-		
#L_ExIOSpParmChg	Change Special I/O parameter	₩	₩.		
#L_ExIOSpParmErr	Special I/O parameter error	4	-		
#L_ExIOAcceIPIsTbI	Acceleration/deceleration pulse table	✓	₩		
#L_ExIOCntInCtrI	Counter input control	₩	✓		
#L_ExIOCntInExtCtrI	Counter input external control	₩	✓		
#L_ExIOCntInState	Counter input state	₩	✓		
#L_ExPWMExCtrl	PWM external input control	✓	₩		
#L_PWM*_WHZ	Ch* output frequency	- ✓	√		
#L_PWM*_DTY	CH* ON duty value	₩	✓		
<del> </del>	+	+			

#L_PLS*_LHZ	Ch* output frequency	✓	✓
#L_PLS*_NUM	CH* Output pulse count	₩	₩
#L_PLS*_SHZ	Ch* Initial Output Frequency	✓	₩
#L_PLS*_ACC	CH* acceleration/deceleration time or acceleration time	✓	✓
#L_PLS*_DEC	CH* Pulse deceleration time	₩	4
#L_PLS*_CHZ	CH* Current Frequency	₩	-
#L_PLS*_CPC	CH* Current Pulse Output Value	₩	-
#L_PLS*_EMG	Pulse emergency stop deceleration time	4	✓
#L_HSC*_MOD	Ch* count system	✓	₩
#L_HSC*_TB	CH* Time-Base when Sampling Mode is selected	✓	4
#L_HSC*_PLV	Ch* preload value	₩	4
#L_HSC*_PSV	Ch* pre-strobe value	₩	-
#L_HSC*_ONP	CH* ON Preset Value	✓	✓
#L_HSC*_OFP	CH* OFF Preset Value	✓	✓
#L_HSC*_HCV	Ch* count system	✓	-

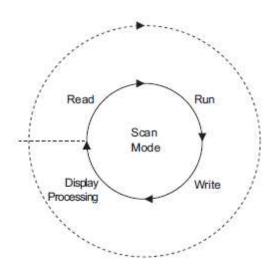
<sup>\*1 [\*] = 0</sup> to 255.

# #L\_ScanTime (The time from the start of step 0 of the current scan to the start of step 0 of the next scan)

Stores the scan time of the previous scan just before the execution of the next scan.

Scan time is the time required for I/O reading, execution of the logic program, I/O writing, and display processing.

The unit is 0.1 ms.



# **#L\_AvgScanTime (Average of 64 #L\_ScanTime cycles)**

<sup>\*2</sup> For information, please see 31.5 Controlling External I/O with LT.

 $<sup>^{*3}</sup>$  [\*] = Channel Number (1 to 4).

 $<sup>^{*4}</sup>$  [\*] = 0 to 31.

Stores the average scan time.

Average scan time is the average time required for I/O reading, execution of the logic program, I/O writing, and display processing in one scan.

The variable is updated with each completion of 64 scan cycles.

The unit is 0.1 ms.

# **#L\_MinScanTime (Minimum scan time of #L\_ScanTime)**

Stores the minimum scan time of the logic program.

When #L\_ScanTime is updated, the minimum scan check is performed and the variable is updated with every scan.

The unit is 0.1 ms.

#### **#L\_MaxScanTime (Maximum scan time of #L\_ScanTime)**

Stores the maximum scan time of the logic program.

When #L\_ScanTime is updated, the maximum scan check is performed and the variable is updated with every scan.

The unit is 0.1 ms.

# **#L\_ScanCount (Number of scans)**

Upon completion of each logic program scan, the counter increments the variable.

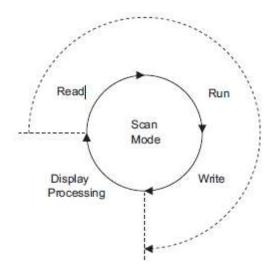
The value in #L\_ScanCount ranges from 0 to 16#FFFFFFF. When the maximum value (16#FFFFFFFF) is exceeded, the variable is incremented again from 0.

You can confirm whether the logic program is being executed by checking #L\_ScanCount.

#### **#L LogicTime (Time from the start of step 0 to the END instruction)**

Stores the logic time of the previous scan execution.

Logic time is the time required for I/O reading, execution of the logic program, and I/O writing in one scan. The display processing time is not included. The unit is 0.1 ms.



# **#L\_AvgLogicTime (Average of 64 #L\_ScanTime cycles)**

Stores the average logic time.

Average logic time is the average time required for I/O reading, execution of the logic program, and I/O writing in one scan.

The variable is updated with each completion of 64 scan cycles. The unit is 0.1 ms.

# **#L MinLogicTime (Minimum logic time of #L LogicTime)**

Stores the minimum logic time of the logic program.

When updating #L\_LogicTime, the minimum logic time is checked and the variable is updated with every scan.

The unit is 0.1 ms.

# **#L\_MaxLogicTime (Maximum logic time of #L\_LogicTime)**

Stores the maximum logic time of the logic program.

When updating #L\_LogicTime, the maximum logic time is checked and the variable is updated with every scan.

The unit is 0.1 ms.

# **#L\_Status** (Logic status information)

Displays the state of the display unit. Bytes and bits are defined as follows:

- Byte 0: Displays the current error state on the display unit.
- Byte 1: Displays the error state history. This byte resets to 0 only when the display unit is reset.
- Byte 2: Displays the current operation state.
- Byte 3: Reserved area.

Byte 0 (lato	h)				Byte 1 Error state history		Byte 0 Current error state	
Total Error	Scan Error	Reserved	Read Error	Overflow	I/O error	Minor Error	Major Error	
7	6	5	4	3	2	1	0	
Byte 1 (latc	h)							
Total Enor	Scan Error	Reserved	Read Error	Overflow	I/O error	Minor Error	Major Error	
15	14	13	12	11	10	9	8	
Byte 2 (latc		T-20 COACC	-	12000000	140000	Total	100 2000	
Reserved	Awaiting Communi- cation	Standby	Stopped	Pause	Force change Enable/ disable	I/O available	In RUN mode	
23	22	21	20	19	18	17	16	
Byte 3 (latc	h)							
Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserve	
31	30	29	28	27	26	25	24	

#### **#L Platform (display unit platform code number)**

Stores the display unit platform code number.

Н	0 0	38	, K	3	18 3			8 -		6	8 3	1	90 - 0		
.		Stores	the G	iP plat	form	code	numt	er.							2
<u></u>	34	36	85	1	f	1	Ĩ	ř.	ì	Ĭ	8 3	Ĩ	ř i	1	Ť

• AGP-3302B

0x00020404

• AGP-3301

0x00020504

• AGP-3300

0x00020514

• AGP-3310

0x00020B14

• AGP-3360

0x00020B34

• AGP-3400

0x00020614

• AGP-3500

0x00020714

• AGP-3600

0x00020814

• AGP-3450

0x00020634

• AGP-3550

0x00020734

• AGP-3650

0x00020834

• AGP-3750

0x00020934

• AGP-3510

0x00020A14

• AGP-3560

0x00020A34

• LT-3201A

0x00030204

• LT-3301

0x00030504

• LT-3300

0x00030514

GP4000 Series

0x00040000

LT4000 Series

0×00040000



For support for the logic program of your model, please refer to the following. 1.5 Supported Features

# **#L\_Version (Logic firmware version)**

Stores the logic firmware version.

# **#L\_EditCount (Number of online edits)**

Stores the number of online edits. (This variable cannot be executed while writing in RUN.)

# **#L\_ForceCount (Cumulative count of variables that are forcibly changed)**

Stores cumulative count of variables that are forcedly changed

# **#L IOInfo (I/O driver information)**

Stores information on the I/O driver.

#L IOInfo[0]: Internal Driver 2 #L\_IOInfo[1]: Internal Driver 1 #L\_IOInfo[2]: External Driver 1 #L\_IOInfo[3]: Reserved

# **#L\_LogicInfo (Logic Information)**

Reserved by the System.

#### **#L\_IOMasterDrv\*** (Master I/O Driver Extended Information)

[\*] = 0 to 255.

Stores Master I/O Driver Extended Information. Depending on the master I/O driver type, you may not be able to use some of the items.



31.7.4 Using I/O Driver Instructions - CANopen

#### **#L IOMasterDiag\* (Master I/O Driver Self Diagnosis)**

[\*] = 0 to 31.

Stores Master I/O Driver self diagnosis information. Depending on the master I/O driver type, you may not be able to use some of the items.

#### **#L ConstantScan (Logic startup frequency)**

Stores the total process time of the logic when the Fixed San Time is selected.

When logic time is constant, the display processing time can be extended by increasing the value of #L\_ConstantScan. By decreasing the value, the display processing time can be reduced. This is because most of the processing time is used by logic functions.

Set this as a default setting. The unit is 0.1 ms.

10 30.14.3.2 Logic Scan Time

# **#L\_PercentScan** (Logic operation rate)

Stores the usage percentage of the logic function compared to the total logic processing time in CPU Scan Percentage mode.

Set this as the default setting.

30.14.3.1 Logic Features

# **#L\_WatchdogTime (WDT value of the logic)**

Stores the WDT value (watch dog timer) with 0.1ms as a unit.

When #L\_ScanTime exceeds this value, a major error occurs.

Set this as a default setting. The unit is 0.1 ms.

# #L\_AddressRefreshTime (Connection device address refresh time)

Stores the address refresh time for the connection device addresses used in the logic program. The unit is 0.1 ms.

30.14.3.3 Address Refresh

# **#L\_Time** (Time information)

Indicates the "time" set in the logic in 4-digit BCD.

The time is stored in the following state:

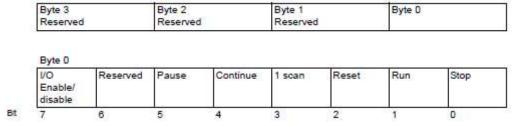
For example, 11:19 PM

	*	•	`	Minutes (one's column)
Value	2	3	1	9

#### **#L Command (Changes the logic operation mode)**

This is an integer variable used as a logic control command.

After the logic acknowledges #L\_Command, bits other than bit 7 are reset to 0. If multiple bits are ON, the least significant bit takes priority.



#### #L\_LogicMonitor (Startup switch of the logic monitor)

Starts up and operates the logic program monitor function on the display unit.

The following shows each operation.

Byte 3		Byte 2		Byte 1		Byte 0	
Byte 0		-å		t.		ati.	
Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Logic monitor Trigger: 1	Address Monitor Trigger: 1
7	6	5	4	3	2	1	0
Byte 1							
Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
15 2Byte 2	14	13	12	11	10	9	8
Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
23	22	21	20	19	18	17	16
3Byte 3	5.0	271	000	KE .	50	20	85
Starting: 1 Stopped: 0	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
31	30	20	28	27	26	25	24

# #L\_LogicMonStep (Indicates the steps for displaying the logic monitor)

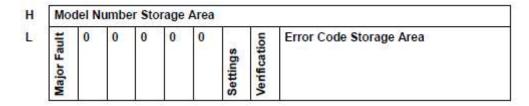
Stores the number of the rung to display in the logic monitor.

If the logic monitor is not running, write the rung number in  $\#L_LogicMonStep$  to start up the logic monitor with the specified rung number as the first rung when the logic monitor trigger bit (bit 0 of  $\#L_LogicMonitor$ ) turns OFF $\to$ ON.

This variable is used when the logic monitor function is enabled.

# **#L\_IOStatus (Status of the I/O driver)**

Stores I/O driver error information.



# • Model Number Storage Area

Model number of the unit where the error occurred is stored. This will be reserved bits depending on the I/O driver used.

### Identify

When the I/O attribute of the specified unit is the same as that of the actually connected unit, but the points differ, "1" is set. This will be reserved bits depending on the I/O driver used.

#### Settings

When the I/O attribute of the specified unit is different from that of the actually connected unit, "1" is set. This will be reserved bits depending on the I/O driver used.

#### Major Fault

When a failure that stops the logic such as an auxiliary board ID conflict or project data failure is found, "1" is set.

#### • Error Code

When an I/O driver error occurs, an error message and the following error code will display on the display unit screen.

RGEA\*\*\*: Internal driver 2 error code
RGEE\*\*\*: Internal driver 1 error code
RGEF\*\*\*: External driver 1 error code

#L\_IOStatus stores the number "\*\*\*" (0 to 255) of these error codes in the bottom 8 bits.

The following explains the classification of each error code.

Error Code	Description
001-049	Project data error
050-099	Hardware error
100-199	Application error
200-254	Internal Errors

For more information about the error code of each I/O driver, please refer to the following:

T.7 Display-related errors

T.8 Errors displayed with an expansion unit

#### • Array

The array of each element is as follows.

#L\_IOStatus[0] : Internal Driver 2
#L\_IOStatus[1] : Internal Driver 1
#L\_IOStatus[2] : External Driver 1

#L\_IOStatus[3] :Reserved

# **#L\_CalcErrCode**

The calculation error state can be identified in #L\_CalcErrCode. The area is cleared to 0 when resetting logic. You can reset logic using #L\_Command (change logic operation mode).

Н						59		3	9	1	3	95	99	J
i +	51	ores	tne c	calcul	ation	erro	cod	es.						10
_														

#### **Error Code List**

Error Code	Description	
0000	-	No error.
0001	Minor error (continue)	An overflow occurs when converting numbers, from real to integer, or 64-bit real to 32-bit real.
0002	Major error (stop)	A reference exceeded the array size.
0003		A reference exceeded the range of an integer.

5, 6, LL, 1.0 1 7 HV	/ lizitiz intoge	or type Logic Cyclem variables the Cyclem variables
0004		Stack overflow.
0005		An invalid instruction code is used.
0006		An error occurred during error handler processing.
0007		The scan time exceeded the WDT.
8000		The critical failure occurred on the I/O driver.
0009	Major error (stop)	Software error
0010		An invalid operand is used.
0011	-	Reserved
0012		BCD/BIN conversion error
0013	——Minor error (continue)	ENCO/DECO conversion error
0014	-	Reserved
0015		The SRAM data (user program) was destroyed
	Minor error (continue)	a destroyed FROM.
0016		The shift bit value exceeded the range.
0100	Major error (stop)	Major error occurred on I/O driver instruction
0105	Minor error (continue)	Minor error occurred on I/O driver instruction
6701	Continue on error	The CJ and CALL instructions have no jump destination.  An index modification caused the label to become undefined outside of P0 to P4095.  The P63 that ran the P63 for the CALL instruction cannot be used for a CALL instruction for a label that branches to END.
6702		The CALL instruction has a nesting level of 6 or higher.
6703		The interrupt has a nesting level of 3 or higher.
6704		The FOR and NEXT instructions have a nesting level of 6 or higher.
6705		The application instruction operand is outside the target device.
6706		The device Number and data value of the application instruction operand have exceeded the range.
6707		The final register was accessed without specifying the parameter

,	7.1.2 magai Typo Logio Cyclem variables 7/2 Cyclem variables					
	settings.					
6708	FROM/TO instruction error					
6709	Other (invalid branch)					
6710	Parameter incompatibility					
6730	The sampling time (Ts) is out of range (Ts <= 0).					
6731						
6732	The input filter constant (a) is out of range (a<100 or 100 <= a).					
6733	The proportion gain (Kp) is out of range (Kp<0).					
6734	The integral calculus time (Ti) is out of range (Ti<0).					
6735	The differential gain (Kd) is out of range (Kd<0 or 201 <= Kd).					
6736	The differential calculus time (Td) is out of range (Td<0).					
6740	Sampling time (Ts) <= sampling frequency					
6742	The change in measurement value is out of range (DPV < -32768 or 32767 < DPV)					
6743	The deviation is out of range (EV < -32768 or 32767 < EV).					
6744	The integral calculated value is out of range (other than -32768 to 32767).					
6745	The differential value is out of range because the differential gain (Kd) is out of range.					
6746	The differential calculated value is out of range (other than -32768 to 32767).					
6747	The PID calculation result is out of range (-32768 to 32767).					
6748	The PID output upper limit setting value < Output lower limit setting value					
6749	PID input/output change alarm settings error.					
6750	Step response method Auto-tuning result failure					
6751	Step response method Incompatibility of auto-tuning direction					
6752	Step response method Auto-tuning failure					
	Limit cycle method					
6753	Incompatibility of auto-tuning output settings values [ULV (upper limit) <= LLV (lower limit)]					
	Limit cycle method					
6754	Incompatibility of auto-tuning PV threshold (hysteresis) settings values (SHpv < 0)					
6755	Limit cycle method					
	Auto-tuning transition state error					

	(The data in the device managing the transition state was not successfully rewritten.)
	Limit cycle method
6756	Result error caused by exceeding the auto-tuning measurement time. (t  on  > t, t  on  < t, t < 0)
	Limit cycle method
6757	The proportion gain of the auto-tuning result is out of range. $(Kp = other than 0 to 32767)$
	Limit cycle method
6758	The integral calculus time of the auto-tuning result is out of range. (Ti = other than 0 to 32767)
	Limit cycle method
6759	The differential calculus time of the auto-tuning result is out of range. (Td = other than 0 to 32767)
6760	The sum of ABS data from the server is inconsistent.
6762	The port specified by the inverter communication instruction is already being used.
6765	Application instruction usage time error
6770	Writing to the FLASH memory board failed
6771	The FLASH memory board is disconnected.
6772	The write error that occurs when writing to the FLASH memory board is prohibited.

# #L\_FaultStep

Stores the program step Number when abnormal processing occurs.

## #L FaultLogicScreen

Stores the logic number when abnormal processing occurs.

INIT: 1

MAIN: 2

ERRH: 3

SUB-01:32 to SUB-32: 63

#### **#L\_StopScans**

Enter a numeric value to execute scanning for the specified number of times. The logic scan continues until the setting reaches 0. Meanwhile, the #L\_StopPending bit is ON. When this bit turns OFF, the logic stops.

# #L\_BackupCmd

When you backup the data of the variables specified to be kept, it triggers backup and restore.

Bit 0: When backup is executing, the lower 16 bits are turned OFF automatically.

Bit 1: When restore in executing, the lower 16 bits are turned OFF automatically.

Bit 8: When backup is complete (normal completion), it is turned ON. When an error occurs, it is turned OFF.

Bit 9: When restore is complete (normal completion), it is turned ON. When an error occurs, it is turned OFF.

The other bits are reserved.

l	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	0	0	0	0	Bad	ckup	riable		0	0	0	0	6.55	ep Va ckup		

#### • Keep Variable Backup Request Bit

0 0	Restore Execution	Backup Execution
-----	-------------------	------------------

Bit	OFF	ON		
Backup Execution	INANA	Request (Backup of variable)		
Restore Execution	INONE	Request (Restore of variable)		

After execution, it is turned OFF automatically.

When the request bits are simultaneously turned ON, restore is executed after the backup.

#### • Keep Variable Backup Completion Bit

0		0	Restore	Execution	Backup	Execution	
---	--	---	---------	-----------	--------	-----------	--

Bit	OFF	ON
Backup Execution	None	Completion Notice
Restore Execution	None	Completion Notice

# NOTE

- You cannot backup in offline mode or transfer mode.
- You can only restore the same project that you backed up. If it is a different project, restore will not execute.
- When you execute backup and restore sequentially, the screen display speed may slow, or the online logic monitor may discontinue. Also, depending on the project, communication may be affected. When you allocate #L\_BackupCmd to the switch parts, specify the [Bit Set] and do not touch continuously. Do not backup or restore on the D-Script using #L\_BackupCmd.
- You can back up only when Logic is stopped.