The 7th IEEE Non-Volatile Memory Systems and Applications Symposium

IEEE NVMSA 2018
CALL FOR PAPERS

Hakodate, Japan, August 28-31, 2018

Organizing committee

General Co-Chairs:
Toshio Endo (Tokyo Institute of Technology, Japan)
Yuan-Hao Chang (Academia Sinica, Taiwan)

Program Co-Chairs:
Soramichi Akiyama (AIST, Japan)
Tao Li (University of Florida, US)

Workshop Chair:
Ryota Shioya (Nagoya University, Japan)

Publicity Co-Chairs:
Duo Liu (Chongqing University, China)
Guillaume Prenat (CEA/CNRS, France)
Youjip Won (Hanyang University, Korea)

Finance Chair:
Hideyuki Jitsumoto (Tokyo Institute of Technology, Japan)

Publication Chair:
Takashi Nakada (NAIST, Japan)

Local Chair:
Hiroshi Inamura (Future University Hakodate, Japan)

Web Chair:
Eishi Arima (The University of Tokyo, Japan)

Steering committee

Steering Committee Co-Chairs
Yiran Chen (Duke University, US)
Tei-Wei Kuo (National Taiwan University, Taiwan)

Steering Committee Members
Nikil Dutt (University of California, Irvine, US)
Hiroshi Nakamura (The University of Tokyo, Japan)
Edwin Sha (Chongqing University, China)
Zili Shao (Hong Kong Polytechnic University, Hong Kong)
Jiwu Shu (Tsinghua University, China)
Lionel Torres (University of Montpellier 2, France)
Chun (Jason) Xue (City University of Hong Kong, Hong Kong)
Sungjoo Yoo (Seoul National University, Korea)

NVMSA is a premier conference in the area of non-volatile memory systems and emerging memory technologies. It provides a fantastic opportunity for global non-volatile memory researchers from different communities to discuss and exchange knowledge, ideas and insights, and to facilitate the establishment of potential collaborations that can speed up the progress in the design and application of NVMs. An expanded technical program will be offered in NVMSA 2018 for the audience from academia and industry.

Scope of Conference

Device/Circuit Design of NVM Systems:
- Emerging NVM Circuit Design
- NVM Device Design
- Error Correction for NVMs
- Non-volatile Logic Circuit
- Low Power NVM Circuit

NVM Storage:
- Operating System Support for NVM
- Compiler Optimization for NVM
- NVM-based Storage Software
- NVM-based Databases
- NVM Controller Design

NVM Architecture and Systems:
- Non-volatile Registers
- Non-volatile Memory Architectures
- Non-volatile Cache Design
- NVM Neuromorphic Architectures
- NVDIMM

NVM Applications:
- In-memory Computing
- NVM for Big Data Analytics
- NVM in Mobile Healthcare Applications
- NVM in Wearable Applications
- NVM in the Internet of Things

Paper Submission

Both research and industry track papers are solicited. All papers must be submitted electronically in PDF format. The submitted manuscripts must describe original work not previously published and not concurrently submitted elsewhere. Submissions must be no more than 6 pages in the IEEE conference proceedings format (two-column, single-space). Each paper is peer-reviewed by the program committee members, and the authors are notified of either "accepted for a long presentation", "accepted for a short presentation", or "reject". A long presentation will be given a 30-mins slot (including Q&As) and a short presentation will be given a 20-mins slot (including Q&As) at the conference. Note that authors of a short presentation do not need to shorten the paper itself. Accepted papers will be included in the IEEE Xplore and will be EI indexed. Authors of non-accepted papers are encouraged to submit a poster descriptions.
The authors should submit their papers through the submission web page at https://easychair.org/conferences/?conf=ieeenvmsa2018.

Call For Posters

We solicit poster submissions of early stage work, especially from students and young researchers to promote fruitful discussions with other participants and speed up the research. A poster submission must describe the content of the poster, such as the motivation of the work, the idea to approach the problem, and preliminary results (if any). Authors of non-accepted papers are encouraged to submit poster descriptions.

Important Dates

Abstract Submission: April 13, 2018
Paper Submission: April 20, 2018
Acceptance Notification: June 1, 2018
Camera-ready Deadline: June 15, 2018
Poster Submission: TBD
Conference Date: August 29-31, 2018 (Workshop on August 28)

Program committee

Name	Country
Eishi Arima	Japan
Hyokyung Bahn	Korea
Jalil Boukhobza	France
Li-Pin Chang	Taiwan
Meng-Fan Chang	Taiwan
Yuan-Hao Chang	Taiwan
Che-Wei Chang	Taiwan
Zhuan Chen	US
Feng Chen	US
Ronald DeMara	US
Shinobu Fujita	Japan
Jose Ignacio Gomez	Spain
Takahiro Hirofuchi	Japan
Chien-Chung Ho	Taiwan
Jingtong Hu	US
Yu Hua	China
Po-Chun Huang	Taiwan
Jinho Hwang	US
Arpit Joshi	UK
Lei Ju	China
Patric P.C. Lee	Hong Kong
Kyoungwoo Lee	Korea
Yongpan Liu	China
Duo Liu	China
Jay Lofstead	US
Youyou Lu	China
Hiroko Midorikawa	Japan
Onur Mutlu	Switzerland
Takashi Nakada	Japan
Beomseok Nam	Korea
Sam H. Noh	Korea
Preeti Panda	India
Pradeep Ramachandran	India
Dongkun Shin	Korea
Guangyu Sun	China
Ken Takeuchi	Japan
Hiroyuki Tomiyama	Japan
Yu Wang	China
Chundong Wang	Singapore
Youjip Won	Korea
Ming-Chang Yang	Hong Kong
Chia-Lin Yang	Taiwan
Sungjoo Yoo	Korea
Youtao Zhang	US

Sponsored by

National Institute of Information and Communications Technology (NICT)

IEEE

https://nvmsa18.github.io/index.html