CSE 141: Introduction to Computer Architecture	Summer II 2019
Lab 1	
Instructor: Devon Merrill	
Name:	Date:
tructions	
Answer each question in the boxes provided. Any writing outside of the turn in responses recorded on separate sheets.	boxes will NOT be graded. Do no
Handwritten or typed responses are accepted. In either case, make sure boxes.	e all answers are in the appropriat
Graphs must be appropriately titled and labeled. Units must be incluminimums and maximums.	uded. Axes must have appropriat
All responses must be neat and legible. Illegible answers will result in ze	ero points.
You will need data gathered on the reference processor to complete this uctions.	lab. See the course web page for
Big O (3 point):	
(a) Plot the matrix size vs. execution time for the 1000 MHz run of a graphing guidelines in the instructions for full credit.	code.exe. Make sure to follow th
(b) This algorithm has a Big O run time of $O(n^3)$ to multiply two $n \times agree$ with this Big O time? Why or why not?	n matrices. Do your measurement

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2. Clock Scaling (3 point):

Pow	er (4 point):
(a)	Calculate the package power for each data point. Plot the power vs. clock rate using the data for largest inputs at each clock rate. Power should be normalized to the lowest value. Make sure to fol the graphing guidelines.
(b)	What is the R value of a linear trend line to this power vs. clock rate data?
(c)	From the power equation we expect power to scale linearly with frequency. Does this match what measured? Why or why not?

4. *CPI* (4 point):