REVISION HISTORY

REVISION	DESCRIPTION OF CHANGE	DATE	Approved
А	Release of Final Schematic	10/22/2024	lain Galloway
A1	Changed BT1 to DNP, Li-Ion batteries cannot be shipped with baords.	10/22/2025	lain Galloway
В	Completely removed BT1 from shcematic. Li-lon batteries soldered or loose cannot be shipped with NXP boards.	10/22/2025	lain Galloway
С	Several changes made to schematic per document PCML-94468_C.xlsx. New USB Connector J1 added. USB Conn changed to Full SMT.	1/22/2025	lain Galloway
D	J1 Connector made SBU1 SBU2 NC. Updated footprint on U13. Added 47uF to input U13.	6/4/2025	lain Galloway

X-MR-VMU-TROPIC TABLE OF CONTENTS

	Table of Contents	
1	COVER PAGE	
2	POWER DELIVERY	
3	I.MXRT1064_GPIO	
4	CONNECTORS_COMMUNICATION	Þ١
5	ETHERNET	
6	IMU	
7	LED	

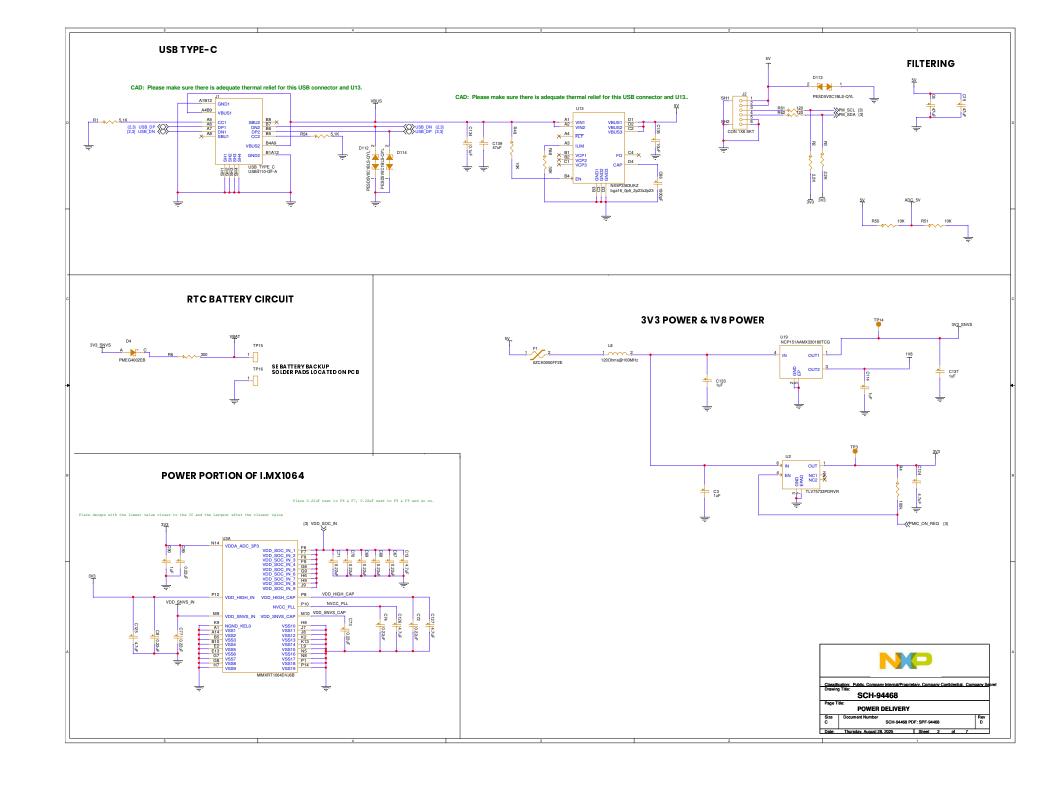
SILKSCREEN

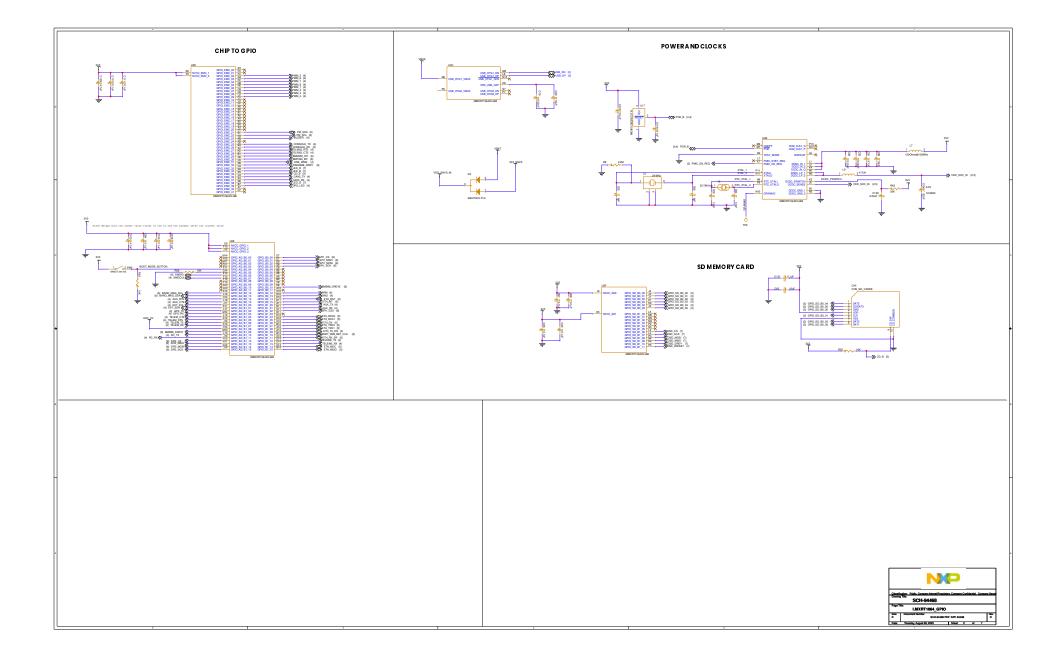
Silkscreen text is this COLOR in the schematic.

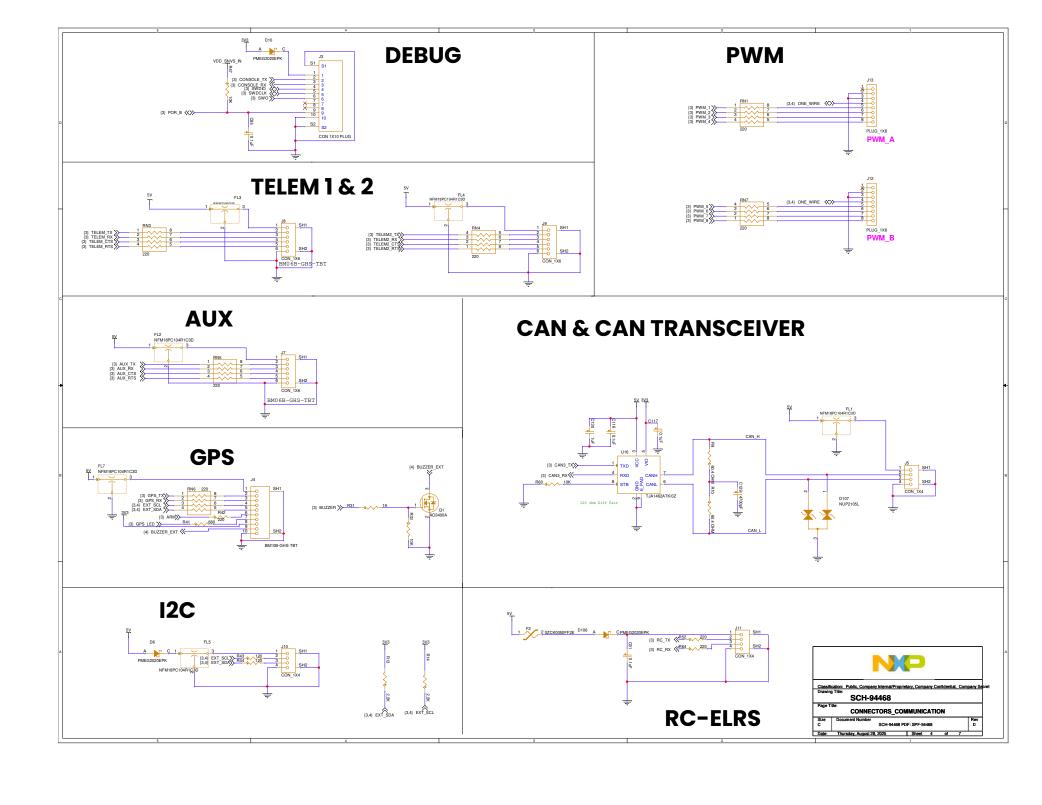
CAD NOTES:

CAD Notes are this COLOR in the schematic.

N. 1		Micro	cont	roller Pro	duct Gro	up		
		6501 William Cannon Drive West Austin, TX 78735-8598						
		proprietary to NXP and le or in part without the ICAP Classific	express			Semic		
Designer: Michael Steffen	Drawin			01.	100.		100.	
Drawn by: Michael Steffen	Page Title: Cover							
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ETHERNET 100BASE-T1 2 600 OHM 10.11 VREGA_OUT {3} ETH_RST 36 MDIO {3} ETH_MDIO <>> C50 1uF 1 MDC {3} ETH_MDC <>> VREGA_IN VREGA_IN 17 TMS/ GPI00/CONFIG0 TRX_P 2 FL6 3 TRX_FL_P DIFF FAIR TRX_N 1 4 TRX_FL_N DIFF FAIR 2000H@100KHZ 18 TDI/GPIO1 C52 0.1uF {3} ETH_RX_ER LAYOUT NOTE: low 29 TXC RXC/GPIO8/TX_TCLK 26 X (3) ENET_RMII_REF_CLK <>> impedance connection to this pin (either directly placed or connected via Power island) 31 TXD0 TXD1 TXD1 TXD1 TXD2/GPIO10 TXD3/GPIO11 RXD0/CONFIG5 RXD1/CONFIG4 21 RXD2/GPIO4/CONFIG3 RXD3/GPIO3/CONFIG2 {3} ETH_TXD0 {3} ETH_TXD1 30 TX_CTL (3) ETH_TX_EN <>> RX CTL/CONFIG6 TJA1103AHN PHYAD = 18 Move D8, D9 LED to top side of PCB. SCH-94468 ETHERNET Date: Thursday, August 28, 2025

