

Table of Contents	
P01	Cover
P02	Notes
P03	ETH_B2B
P04	TJA1103BHN
P05	Changelog

Revision History			
Rev.	Date	By	Description
X0-X8	2025-09-12	YT (CTO SI)	Initial schematic development and format changes
A	2025-09-16	YT (CTO SI)	Schematic is released


A detailed changelog is available on the last page



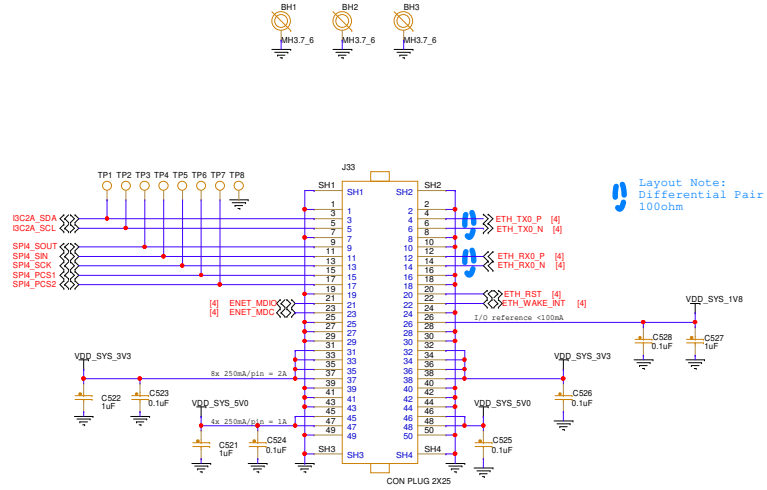
X-MR-NAVQ95E-T1P

NOTES:

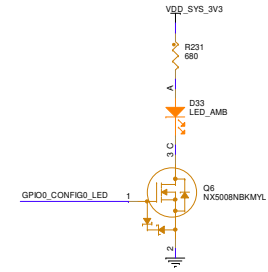
1. Interrupted lines coded with the same letter or letter combinations are electrically connected.
2. Device type number is for reference only. The number varies with the manufacturer.
3. Greyed out components indicate Do Not Populate (DNP) during assembly.
4. Special signal usage:
 _B denotes an active-low signal.
 _P and _N denote a differential signal.
5. Interpret diagram in accordance with American National Standards Institute specifications, current revision, with the exception of logic block symbology.
6. All SILK display properties will be in this color
7. All schematic annotations will be in this color

		System Innovation High Tech Campus 90 5656 AG Eindhoven, The Netherlands	
This document contains information proprietary to NXP and shall not be used for engineering design, procurement or manufacture in whole or in part without the express written permission of NXP Semiconductors.			
© NXP SEMICONDUCTORS		Classification: <u>Company Internal/Proprietary</u>	
Designer: Youri Tils	Drawing Title: X-MR-NAVQ95E-T1P		
Drawn by: Youri Tils	Page Title: Cover		
Approved: Jari van Ewijk	Size C	Document Number SCH-90098 PDF: SPF-90098	Rev A
Date: Monday, September 29, 2025		Sheet 1 of 5	

5					4					3					2					1				
D																								
C																								
B																								
A																								
																				</				



Classification: Company Internal			
Drawing Title: X-MR-NAVQ95E-T1P			
Page Title: ETH_B2B			
Size C	Document Number SCH-96098 PDF: SPF-96098	Rev A	
Date: Monday, September 29, 2025	Sheet 3 of 5		



Classification: Company Internal			
Drawing Title: X-MR-NAVQ95E-T1P			
Page Title: TJA1103BHN			
Size C	Document Number SCH-96098 PDF: SPF-96098	Rev A	
Date:	Monday, September 29, 2025	Sheet	4 of 5

Changelog

Rev.	Date	By	Description
X0-X5	2025-09-12	YT (CTO SI)	Initial schematic development and format changes
X6	2025-09-16	YT (CTO SI)	Added note for VDD_CORE and MOSFET circuit to toggle LEDs
X7	2025-09-18	YT (CTO SI)	Fixed GPIO0_CONFIG0_LED net connection
X8	2025-09-24	YT (CTO SI)	Swapped SGMII TX and RX to be correct. Fixed LED driver circuit TJA1103.



Classification: Company Internal

Drawing Title:

X-MR-NAVQ95E-T1P

Page Title:

Changelog

Size B	Document Number SCH-96098 PDF: SPF-96098	Rev A
Date: Monday, September 29, 2025	Sheet 5 of 5	