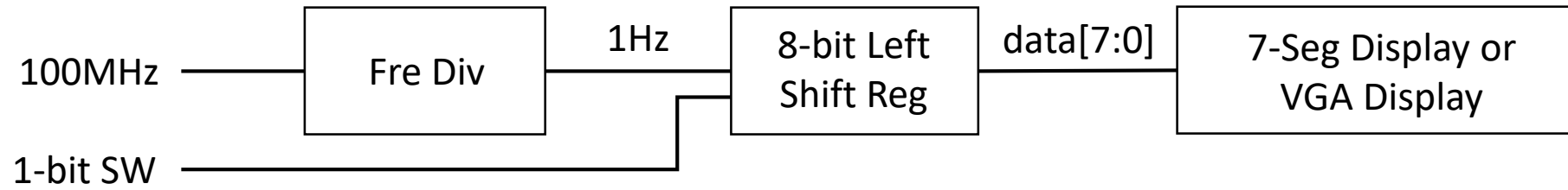# LAB ASSIGNMENT4

- Use frequency divider, 8-bit left shift register, and 7-segment-tube display(or VGA display) modules to complete the design.

- Each module accounts for 25% of the score, and the constraints file accounts for 25%

- Enter a digital number 0 or 1 every second, and store 8-bit data in the left shift register (serial-parallel converter)

- When data is 0x01, show your SID on the 7 segment tube or VGA and maintain until the next valid data arrives

- When data is 0xFF, show your Family name and maintain until the next valid data comes

- Demonstrate before class ends