University of Cape Town ~~ Department of Computer Science Computer Science 3002F/3023F ~~ 2017

Class Test 2

** Solutions **

Enter the following details AND shade in the corresponding blocks to the right with your Student Number.

Student Number:

Name (optional)

Marks : 40 for CSC3002F

24 for CSC3023F

Time : 45 minutes for CSC3002F

27 minutes for CSC3023F

Instructions:

a) CSC3002F: Answer both questions.

b) CSC3023F: Answer question 2 **ONLY**.

- c) Write your answers in PEN in the spaces provided.
- d) Show all calculations where applicable.

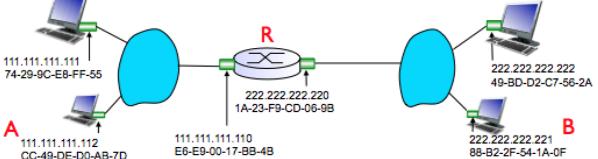
$A \square$					0
В 🛛					1
C					2
$D \ \square$					3
\mathbf{E} \square					4
\mathbf{F} \square					5
$G\ \mathbb{I}$					6
$H \square$					7
$\mathbf{I} \square$					8
J \square					9
\mathbf{K}					
L \square					
\mathbf{M} \square					
N					
Ο 🛮					
$P \square$					
$Q\ \mathbb{I}$					
R \square					
S					
T					
U 🛮					
$V \square$					
$W \square$					
$X \square$					
\mathbf{Y}					

FOR OFFICIAL USE ONLY:

Question		1		2	3	4	5	6	7	8
Max		16	:	24						
Marks	0									
	1									
	2									
	3									
	4									
	5									
	6									
	7									
	8									
	9									
Marker										

Question 1 [16] This question is for CSC3002F students only.

channel	partitioning, random access, and 'taking turns'	
What is CSI	MA/CD and how does it work?	[
starts transr	e, multiple access, with collision detection. Device checks if cable is idle; if nitting else waits till idle. If NIC transmits entire frame without detecting done; else, it aborts, sends a jam signal, and does a binary (exponential) bat again.	
starts transr another, it's off and tries	mitting else waits till idle. If NIC transmits entire frame without detecting done; else, it aborts, sends a jam signal, and does a binary (exponential) ba	
starts transr another, it's off and tries	nitting else waits till idle. If NIC transmits entire frame without detecting done; else, it aborts, sends a jam signal, and does a binary (exponential) ba again.	ck-



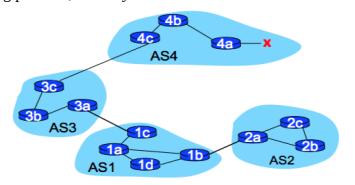
CC-49-DE-D0-AB-7D

	address of the packet that A will transmit?
	nation IP: 222.222.222.221. Destination MAC: E6-E9-00-17-BB-4B (i.e., that of the r, not the MAC addy of B)
В.	In the grand process of A sending something to B, is it ever possible in any ARP so that the device with IP address 111.111.111.111 will receive a packet from A?
out. In	A doesn't know the MAC address of the router, it will have to use ARP to figure that a so doing, A will send a broadcast message on its subnet first. This message will
arrive	e at 111.111.111, for it is on the same subnet.
Switcl differe table	
Switcl differe table	thes and routers are both based on store-and-forward of packets, but there are ences. Compare where a forwarding table in a router comes from with where a forwarding table in a switch comes from. Include in your comparison an explanation of table comparison and explanation of table comparison.
Switcl differe table	thes and routers are both based on store-and-forward of packets, but there are ences. Compare where a forwarding table in a router comes from with where a forwarding table in a switch comes from. Include in your comparison an explanation of table comparison and explanation of table comparison.
Switcl differe table	thes and routers are both based on store-and-forward of packets, but there are ences. Compare where a forwarding table in a router comes from with where a forwarding table in a switch comes from. Include in your comparison an explanation of table comparison and explanation of table comparison.

(vi)Consider the network in the figure below. Suppose AS2 and AS3 are running OSPF for the intra-AS routing protocol, and AS1 and AS4 are running enhanced-RIP for their intra-AS routing protocol. BGP is used for the inter-AS routing protocol. Router 3c learns about prefix **x** from which routing protocol, *and why that one*? [2]

is, it will flood all ports except x, arriving also at B. when B sends a message back to A, it is

then also stored in the table (port y for B). And so on. (1)



eBGP (1). x is in another AS than where 3C is, and 3c is a area border router, so between AS, 'exterior' (1). [Marking note: just BGP is not enough, because there's iBGP and eBGP.]

Question 2 [24] This question is for both CSC3002F and CSC3023F.

(i)	Explain what an operating system does from a system perspective.	[1]
	It's a resource allocator. (1)	
(ii)	Describe the responsibilities of the operating system when it comes to process management	ı t. [5]
		· - -
	 Scheduling processes on the CPU. 2) Creating and deleting processes. 3) Suspending a resuming processes. 4) Provide mechanisms for synchronisation. 5) Provide mechanism communication. 	
(iii)	Why do operating systems make use of an interrupt vector for handling interrupts?	[2]
	To be able to handle interrupts very quickly. (1) There is a predefined number of interrupts so a table of pointers (an interrupt vector) is used to transfer control to the appropriate interrupt service routine. (1)	
` '	Explain what the microkernel approach to operating system structure is, and why it is prefet to using a simple structure such as that employed by MS-DOS.	erable [4]
	Remove all non-essential components from the kernel (1) – implement them as system and user-level programs. (1) Interfaces and levels of functionality are well separated, compared to a simple structure such as MS-DOS. (1) Easier to extend an OS, and easier to port to other hardware. (1)	1

(v) What is a bootstrap loader and what is its function?	[2]
Small piece of code in ROM (1) that locates the kernel, loads it into main memory, and starts execution. (1)	
(vi) What is the difference between the stack and the heap of a process?	[2]
The stack stores temporary data used as part of the process (function parameters, return addresses, local variables). (1) The heap is memory that is dynamically allocated during process runtime. (1)	
(vii) What does a medium-term scheduler do, and what is its main purpose?	[2]
Removes a process from memory (and therefore from active contention for the CPU). (1) Purpose is to modify the degree of multiprogramming. (1)	
(viii) Explain what is a zombie process and how do deal with it.	[2]
A process that has terminated (called exit()), but whose parent has not yet called wait(). (1) All processes exist as zombie processes at some point, but generally only briefly. Nothing to be done. Once the parent calls wait(), it ceases to be a zombie.(1)	
(ix) In message-passing systems, describe the information contained in the communica primitives, and the properties of the communication link, for direct message passing.	atic [4]

·	
send(P,msg) contains the message and the details of the process to which the mess	age is
sent. receive(Q,msg) contains the message and the process which is to receive the	message.
(1) The link is established automatically. (1) A link is associated with exactly two	
(1). There is exactly one link between each pair of processes.(1)	7.000000