




Report Generated by Test Manager

Title: Test
Author: pass
Date: 07-Aug-2024 18:17:44

Test Environment

Platform: PCWIN64
MATLAB: (R2022b)

Summary

Name	Outcome	Duration (Seconds)
Results: 2024-Aug-07 18:14:42	✓	20.352
 TEST	✓	20.325
 New Test Suite 1	✓	20.326
 New Test Case 1	✓	20.325

Results: 2024-Aug-07 18:14:42

Result Type: Result Set
Parent: None
Start Time: 2024-08-07 18:14:43
End Time: 2024-08-07 18:15:03
Outcome: Total: 1, Passed: 1

[Back to Report Summary](#)

TEST

Test Result Information

Result Type: Test File Result
Parent: [Results: 2024-Aug-07 18:14:42](#)
Start Time: 2024-08-07 18:14:43
End Time: 2024-08-07 18:15:03
Outcome: Total: 1, Passed: 1

Test Suite Information

Name: TEST

[Back to Report Summary](#)

New Test Suite 1

Test Result Information

Result Type: Test Suite Result
Parent: [TEST](#)
Start Time: 2024-08-07 18:14:43
End Time: 2024-08-07 18:15:03
Outcome: Total: 1, Passed: 1

Test Suite Information

Name: New Test Suite 1

[Back to Report Summary](#)

New Test Case 1










Test Result Information

Result Type: Test Case Result
Parent: [New Test Suite 1](#)
Start Time: 2024-08-07 18:14:43
End Time: 2024-08-07 18:15:03
Outcome: Passed

Test Case Information

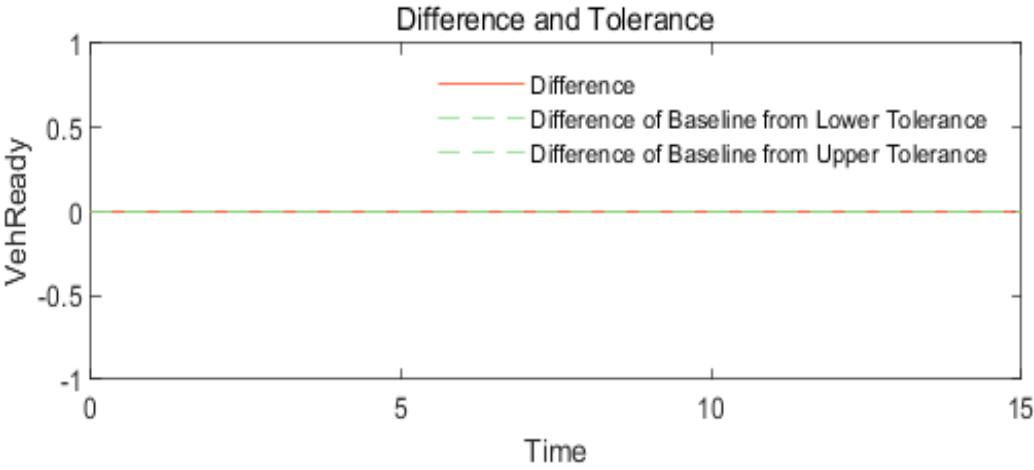
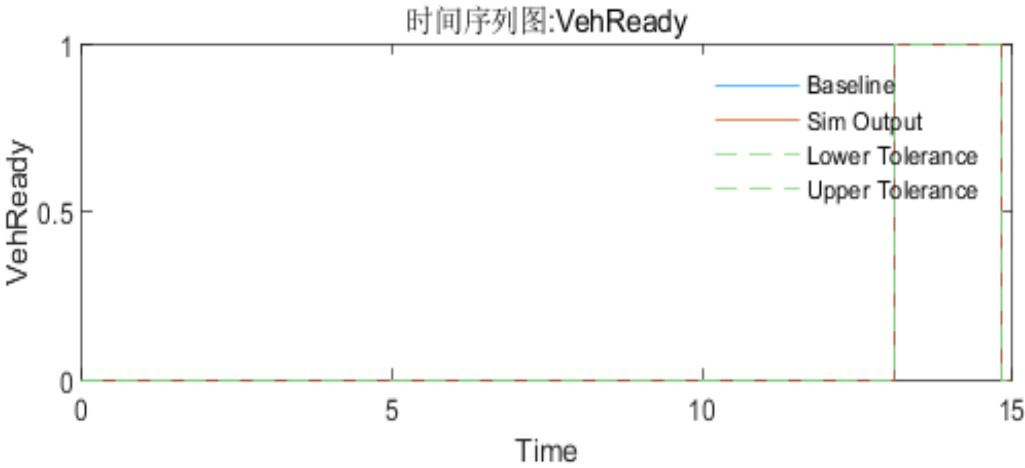
Name: New Test Case 1
Type: Baseline Test
Baseline Name: Scenario1
Baseline File: C:\Users\12045\Desktop\Vehicle_control_model\Drive_ready\TESTSHEET.xls

Baseline Comparison

Name	Abs Tol	Rel Tol	Lead Tol	Lag Tol	Max Diff	Data Type 1	Units 1	Sample Time 1	Data Type 2	Units 2	Sample Time 2	Interp	Sync	Link to Plot
 VehReady	0	0	0	0	0	boolean			boolean		0.01	zoh	union	Link
 BeeperSt Out	0	0	0	0	0	boolean			boolean		0.01	zoh	union	Link
 VehStandby	0	0	0	0	0	boolean			boolean		0.01	zoh	union	Link
 AMKCANready	0	0	0	0	0	boolean			boolean		0.01	zoh	union	Link
 MCFL_DC On_setpoints	0	0	0	0	0	boolean			boolean		0.01	zoh	union	Link
 MCFR_DC On_setpoints	0	0	0	0	0	boolean			boolean		0.01	zoh	union	Link
 MCFL_DC Enable	0	0	0	0	0	boolean			boolean		0.01	zoh	union	Link
 MCFR_DC Enable	0	0	0	0	0	boolean			boolean		0.01	zoh	union	Link
 MCFL_InverterOn	0	0	0	0	0	boolean			boolean		0.01	zoh	union	Link

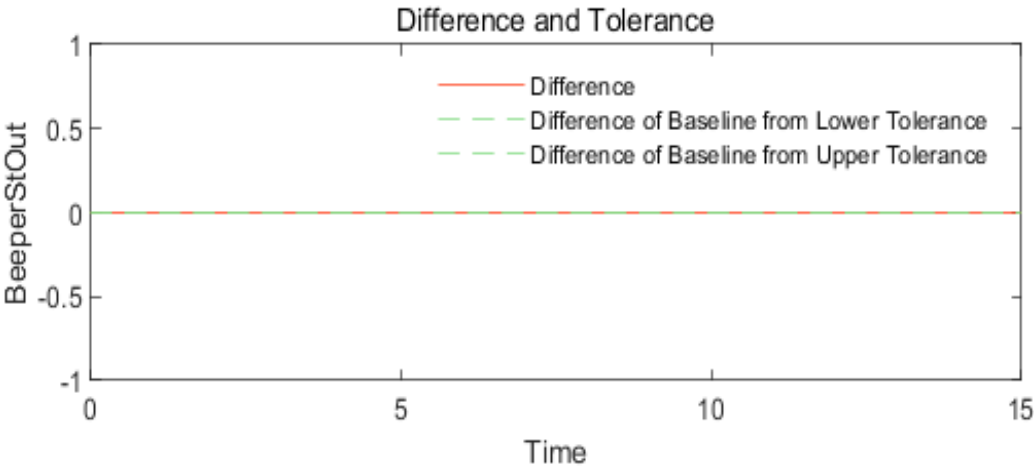
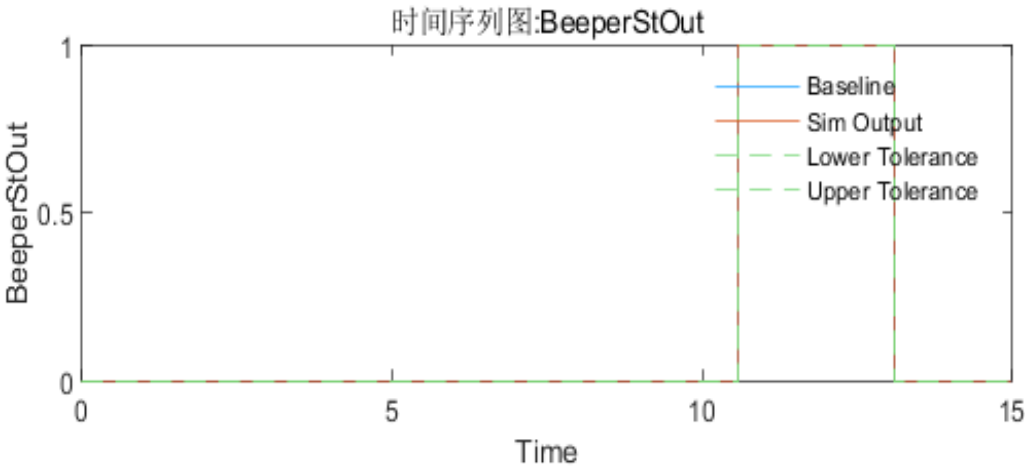
✔ MCFR_InverterOn	0	0	0	0	0	boolean			boolean		0.01	zoh	union	Link
✔ MC_DConError	0	0	0	0	0	boolean			boolean		0.01	zoh	union	Link
✔ errorReset	0	0	0	0	0	double			double		0.01	zoh	union	Link

Name	Abs Tol	Rel Tol	Lead Tol	Lag Tol	Max Diff	Data Type 1	Units 1	Sample Time 1	Data Type 2	Units 2	Sample Time 2	Interp	Sync
✔ VehReady	0	0	0	0	0	boolean			boolean		0.01	zoh	union



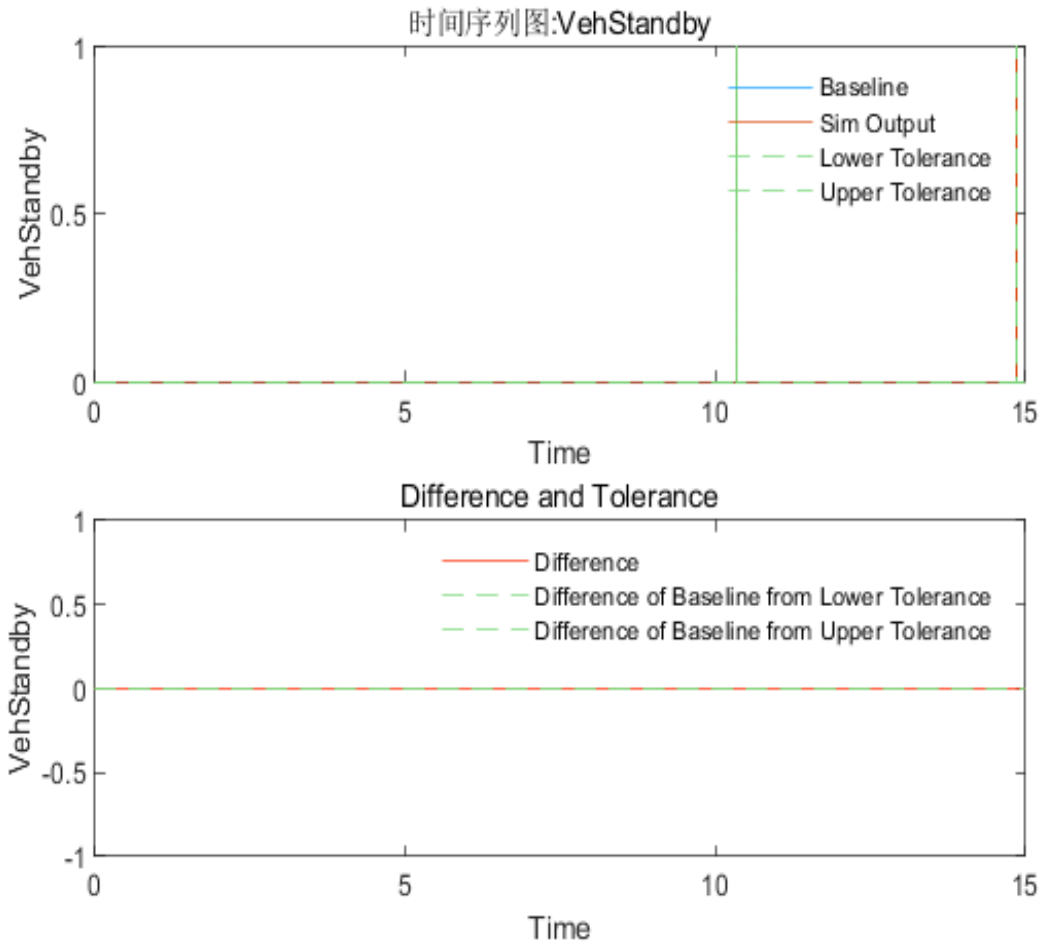
[Back to Report Summary](#)[Back to Criteria Results](#)

Name	Abs Tol	Rel Tol	Lead Tol	Lag Tol	Max Diff	Data Type 1	Units 1	Sample Time 1	Data Type 2	Units 2	Sample Time 2	Interp	Sync
<div><div></div><div>BeeperStOut</div></div>	0	0	0	0	0	boolean			boolean		0.01	zoh	union



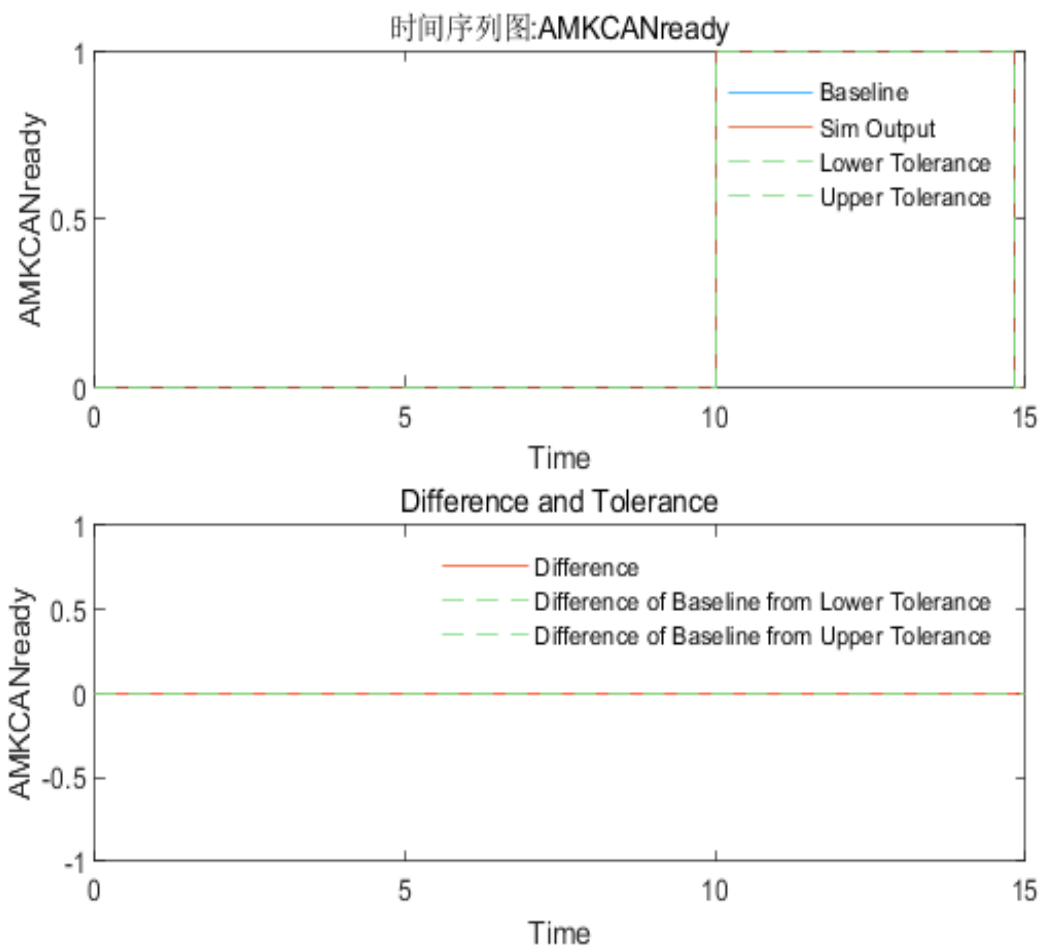
[Back to Report Summary](#)[Back to Criteria Results](#)

Name	Abs Tol	Rel Tol	Lead Tol	Lag Tol	Max Diff	Data Type 1	Units 1	Sample Time 1	Data Type 2	Units 2	Sample Time 2	Interp	Sync
✓ VehStandby	0	0	0	0	0	boolean			boolean		0.01	zoh	union



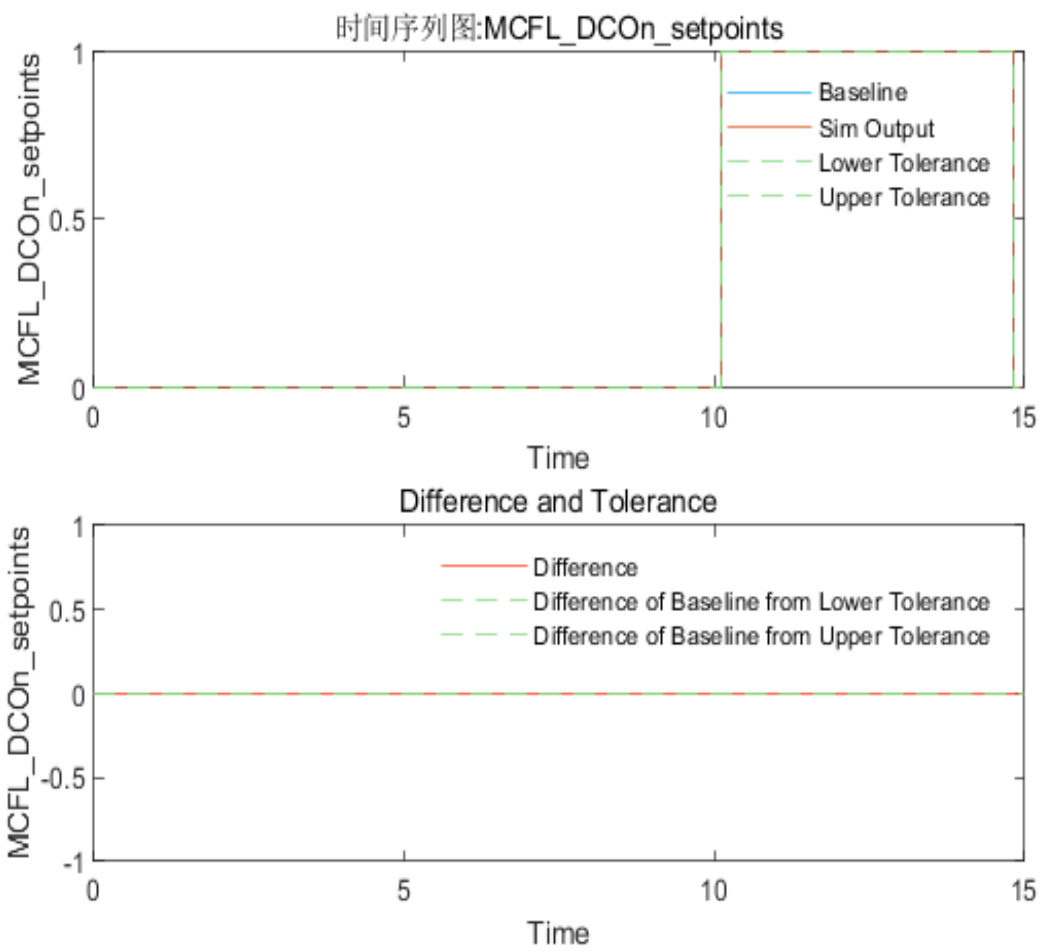
[Back to Report Summary](#)[Back to Criteria Results](#)

Name	Abs Tol	Rel Tol	Lead Tol	Lag Tol	Max Diff	Data Type 1	Units 1	Sample Time 1	Data Type 2	Units 2	Sample Time 2	Interp	Sync
✓ AMKCANready	0	0	0	0	0	boolean			boolean		0.01	zoh	union



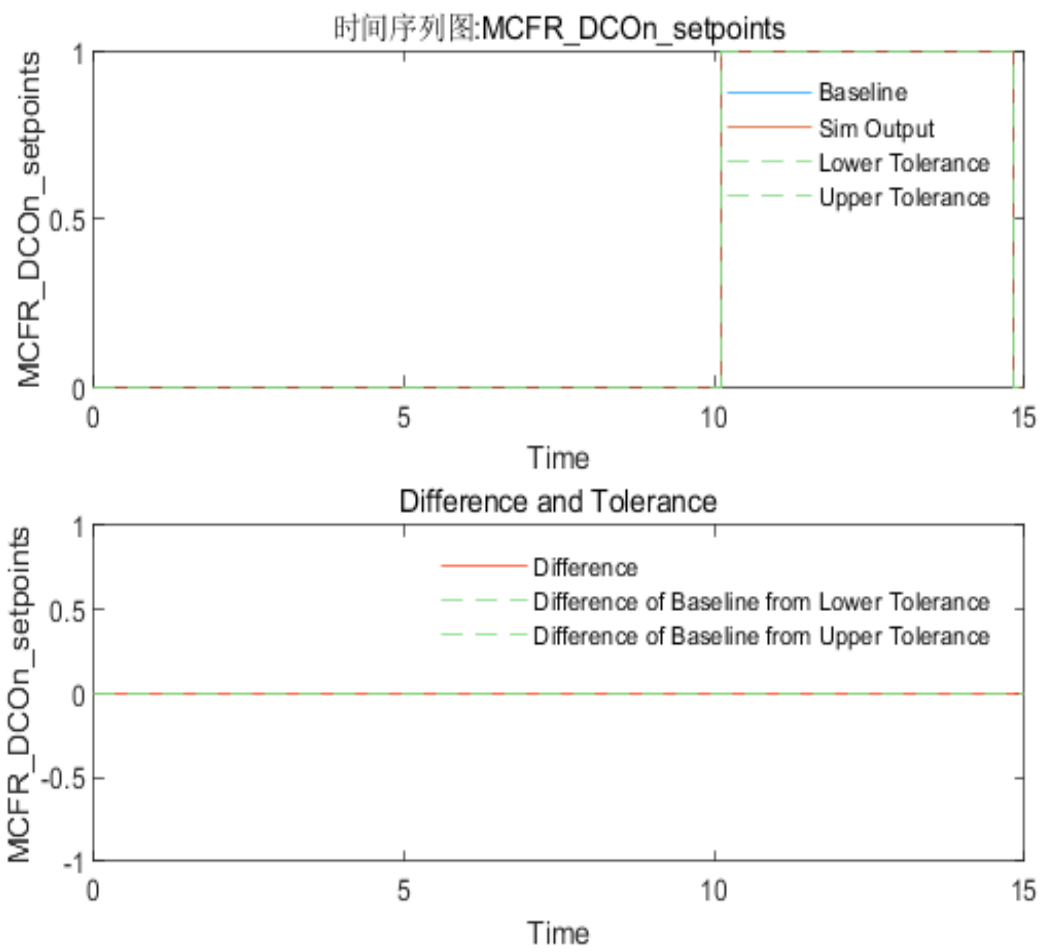
[Back to Report Summary](#)[Back to Criteria Results](#)

Name	Abs Tol	Rel Tol	Lead Tol	Lag Tol	Max Diff	Data Type 1	Units 1	Sample Time 1	Data Type 2	Units 2	Sample Time 2	Interp	Sync
✓ MCFL_DC On_setpoints	0	0	0	0	0	boolean			boolean		0.01	zoh	union



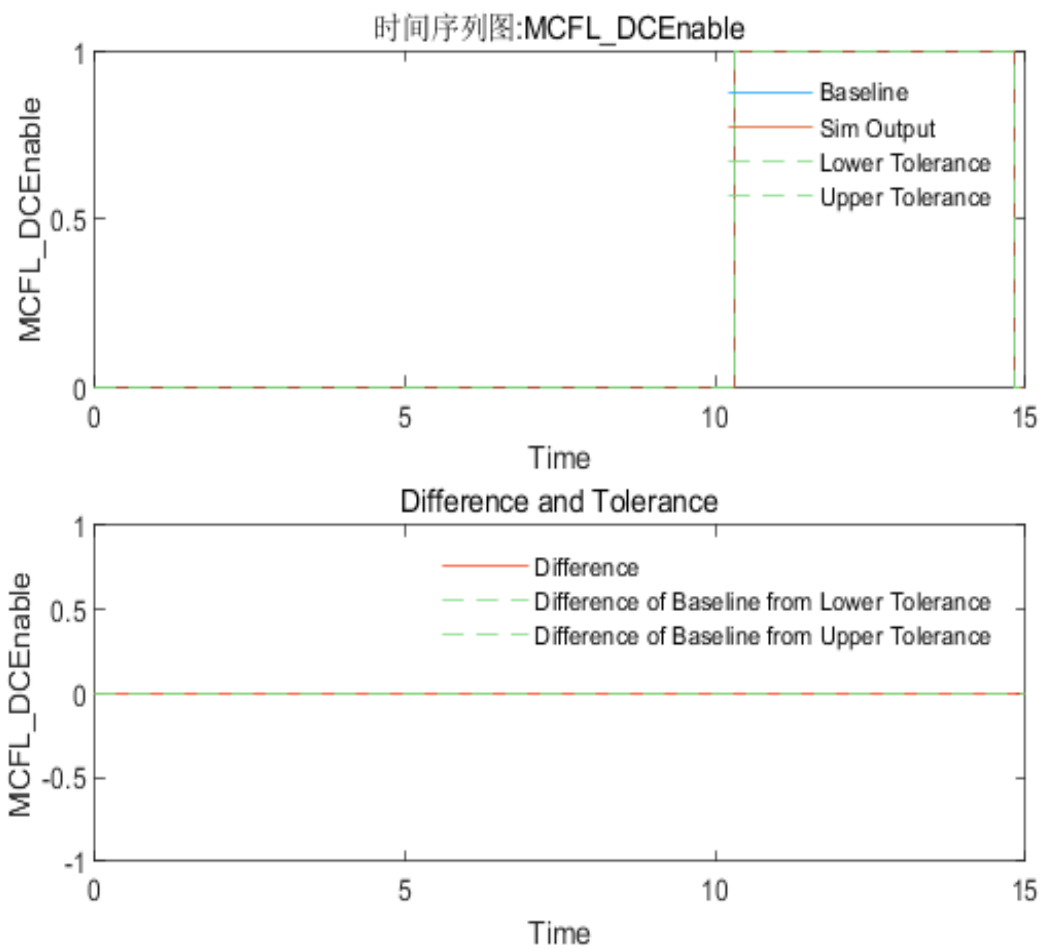
[Back to Report Summary](#)[Back to Criteria Results](#)

Name	Abs Tol	Rel Tol	Lead Tol	Lag Tol	Max Diff	Data Type 1	Units 1	Sample Time 1	Data Type 2	Units 2	Sample Time 2	Interp	Sync
✓ MCFR_DCOOn_setpoints	0	0	0	0	0	boolean			boolean		0.01	zoh	union



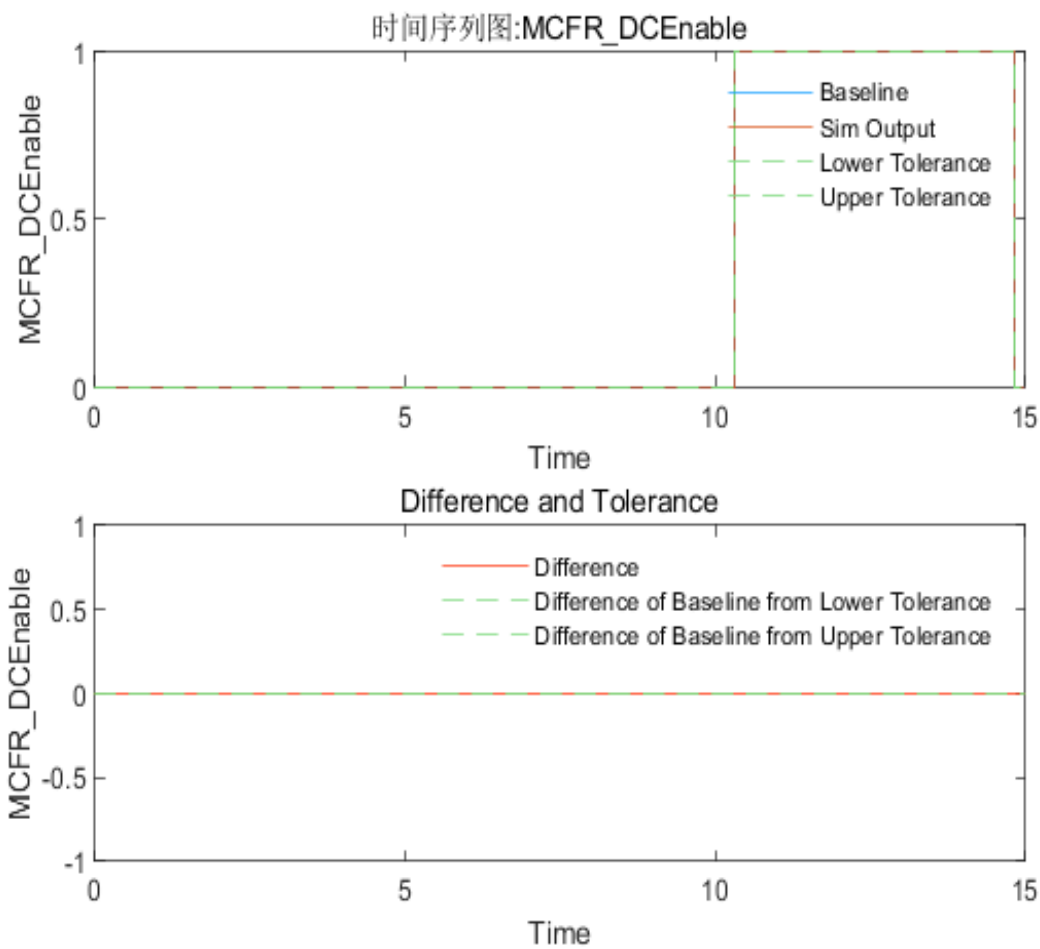
[Back to Report Summary](#)[Back to Criteria Results](#)

Name	Abs T ol	Rel To l	Lead T ol	Lag T ol	Max Di ff	Data Type 1	Units 1	Sample Ti me 1	Data Type 2	Units 2	Sample Tim e 2	Interp	Sync
✓ MCFL_DC Enable	0	0	0	0	0	boolean			boolean		0.01	zoh	union



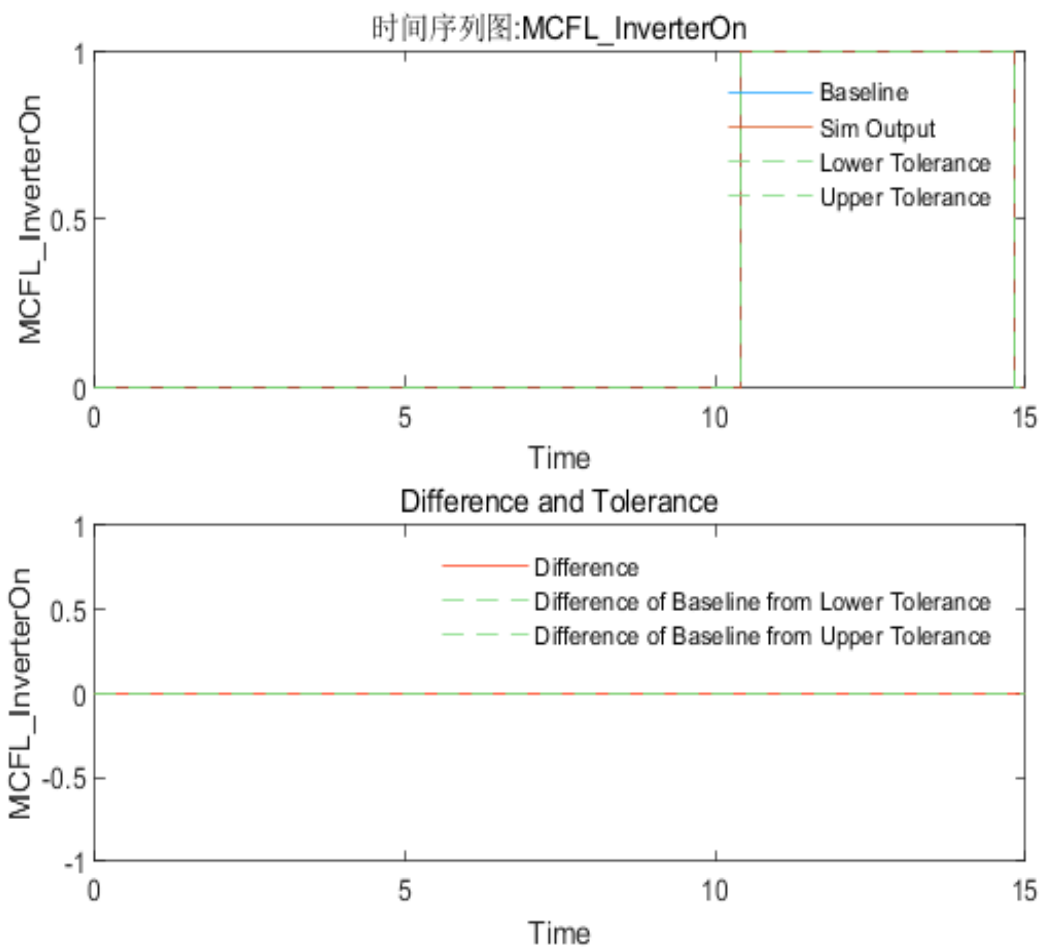
[Back to Report Summary](#)[Back to Criteria Results](#)

Name	Abs Tol	Rel Tol	Lead Tol	Lag Tol	Max Diff	Data Type 1	Units 1	Sample Time 1	Data Type 2	Units 2	Sample Time 2	Interp	Sync
✓ MCFR_DC Enable	0	0	0	0	0	boolean			boolean		0.01	zoh	union



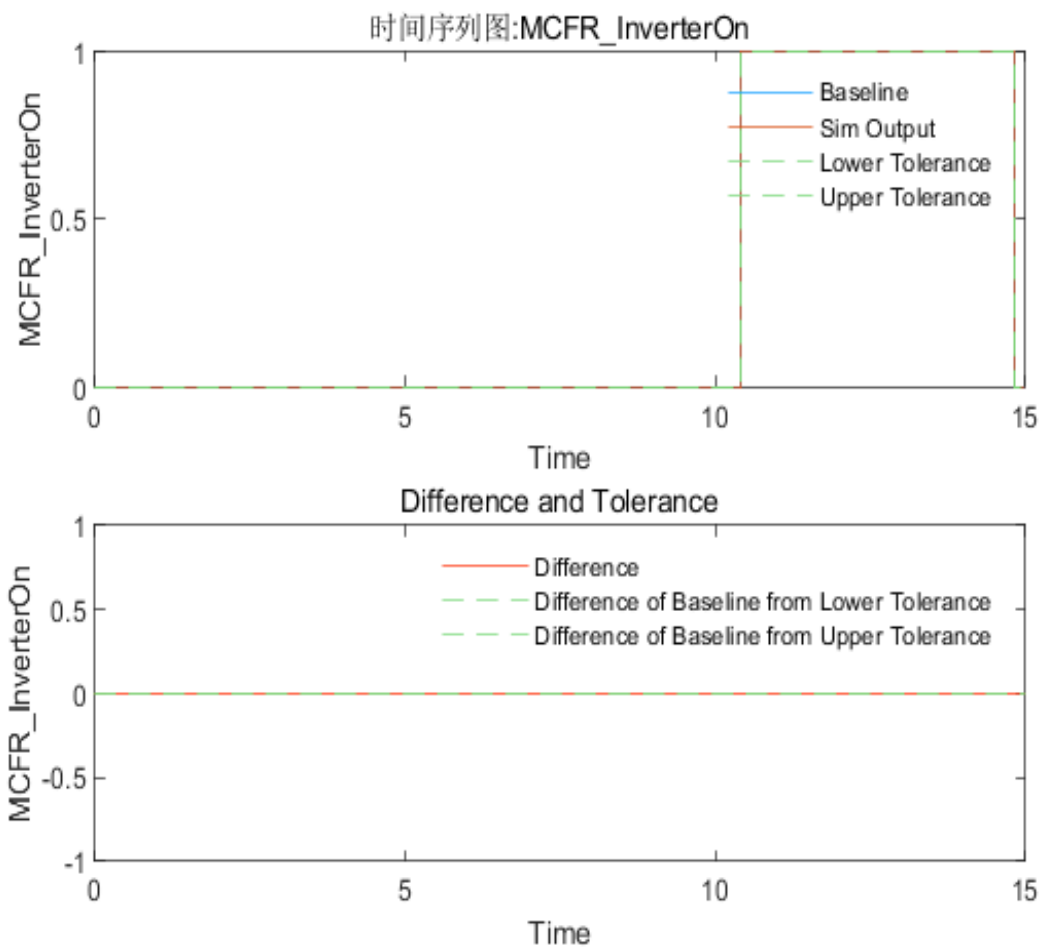
[Back to Report Summary](#)[Back to Criteria Results](#)

Name	Abs Tol	Rel Tol	Lead Tol	Lag Tol	Max Diff	Data Type 1	Units 1	Sample Time 1	Data Type 2	Units 2	Sample Time 2	Interp	Sync
✓ MCFL_InverterOn	0	0	0	0	0	boolean			boolean		0.01	zoh	union



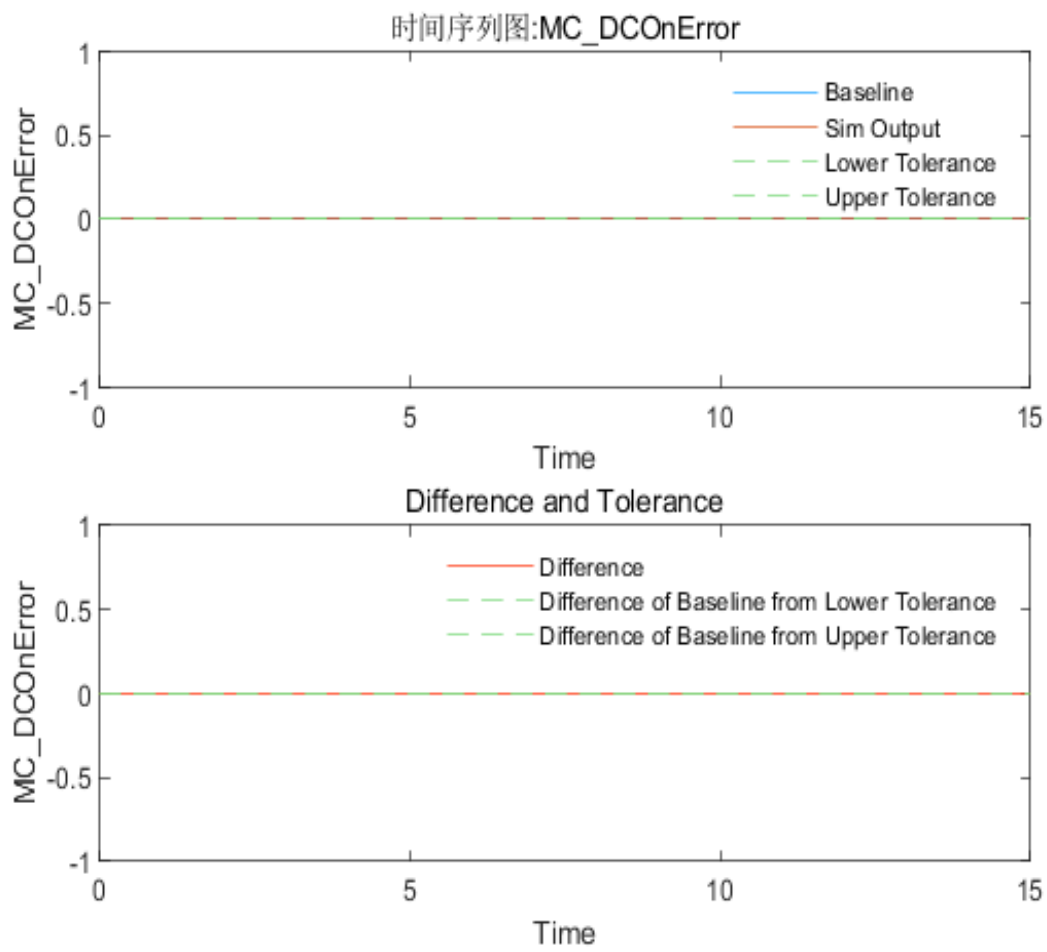
[Back to Report Summary](#)[Back to Criteria Results](#)

Name	Abs Tol	Rel Tol	Lead Tol	Lag Tol	Max Diff	Data Type 1	Units 1	Sample Time 1	Data Type 2	Units 2	Sample Time 2	Interp	Sync
✓ MCFR_InverterOn	0	0	0	0	0	boolean			boolean		0.01	zoh	union



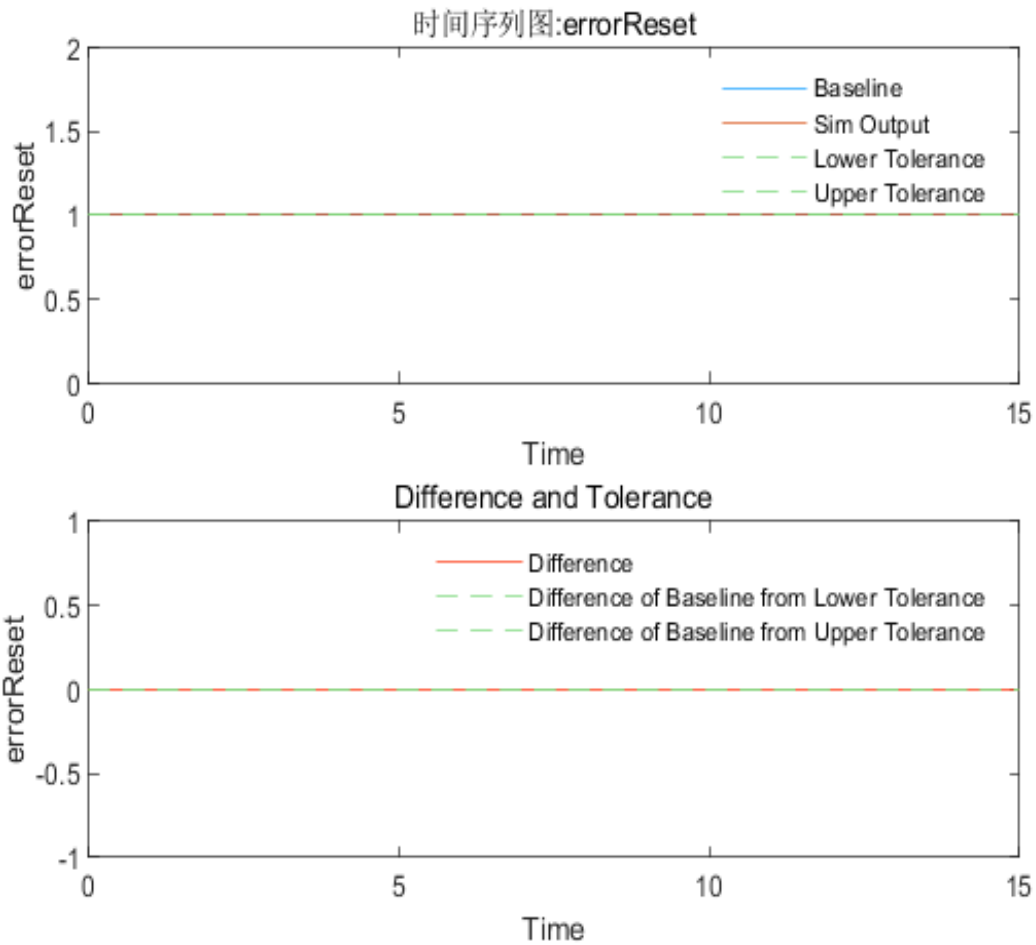
[Back to Report Summary](#)[Back to Criteria Results](#)

Name	Abs Tol	Rel Tol	Lead Tol	Lag Tol	Max Diff	Data Type 1	Units 1	Sample Time 1	Data Type 2	Units 2	Sample Time 2	Interp	Sync
✔ MC_DCon Error	0	0	0	0	0	boolean			boolean		0.01	zoh	union



[Back to Report Summary](#)[Back to Criteria Results](#)

Name	Abs Tol	Rel Tol	Lead Tol	Lag Tol	Max Diff	Data Type 1	Units 1	Sample Time 1	Data Type 2	Units 2	Sample Time 2	Interp	Sync
✔ errorReset	0	0	0	0	0	double			double		0.01	zoh	union



[Back to Report Summary](#)[Back to Criteria Results](#)

Simulation

System Under Test Information

Model:	Drive_ready
Harness:	Drive_ready_Harness11
Harness Owner:	Drive_ready
Release:	Current
Simulation Mode:	normal

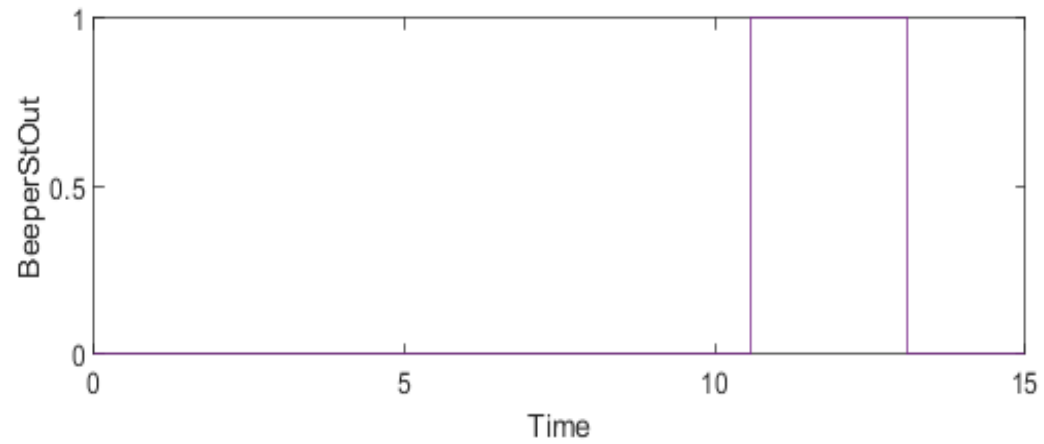
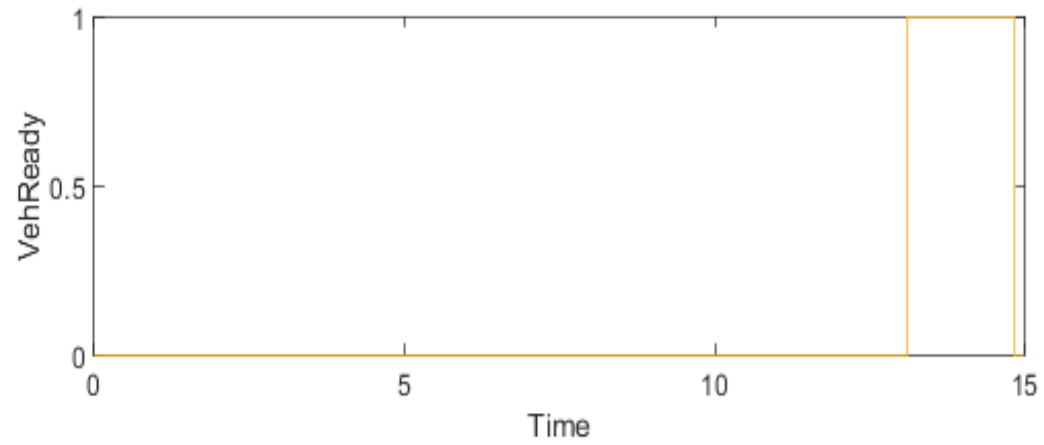
Override SIL or PIL 0
 Mode:
 Configuration Set: Configuration1
 External Input Name: Scenario1
 External Input File: C:\Users\12045\Desktop\Vehicle_control_model\Drive_ready\TESTSHEET.xls
 Start Time: 0
 Stop Time: 15
 Checksum: 3919448138 3354916996 1612025063 2831917993
 Simulink Version: 10.6
 Model Version: 1.0
 User ID: 12045
 Model Path: C:\Users\12045\Desktop\Vehicle_control_model\Drive_ready\Drive_ready.slx
 Machine Name: YJR
 Solver Name: FixedStepDiscrete
 Solver Type: Fixed-Step
 Fixed Step Size: 0.01
 Simulation Start Time: 2024-08-07 18:14:43
 Simulation Stop Time: 2024-08-07 18:15:03
 Platform: PCWIN64

Simulation Output

Name	Data Type	Units	Sample Time	Interp	Sync	Link to Plot
VehReady	boolean		0.01	zoh	union	Link
BeeperStOut	boolean		0.01	zoh	union	Link
MC_DCONerror	boolean		0.01	zoh	union	Link
errorReset	double		0.01	zoh	union	Link
VehStandby	boolean		0.01	zoh	union	Link
AMKCANready	boolean		0.01	zoh	union	Link
MCFL_DCON_setpoints	boolean		0.01	zoh	union	Link
MCFR_DCON_setpoints	boolean		0.01	zoh	union	Link
MCFL_DCEnable	boolean		0.01	zoh	union	Link
MCFR_DCEnable	boolean		0.01	zoh	union	Link
MCFL_InverterOn	boolean		0.01	zoh	union	Link

MCFR_InverterOn	boolean		0.01	zoh	union	Link
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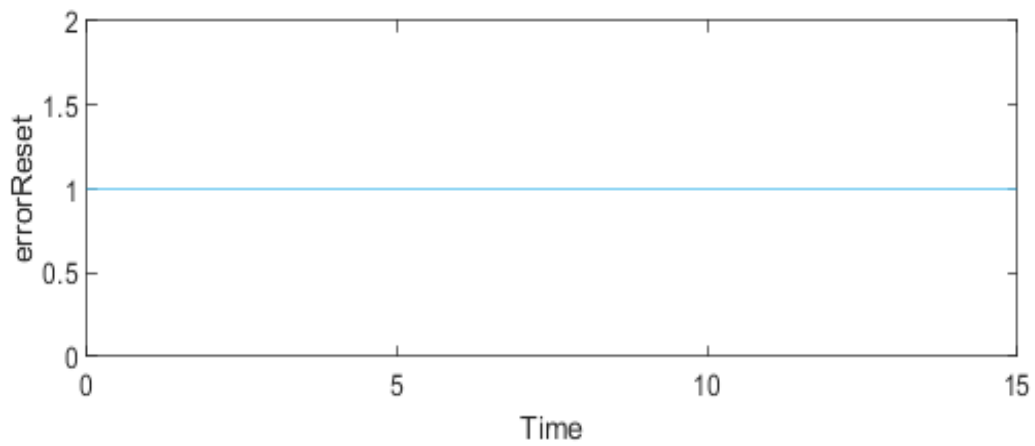
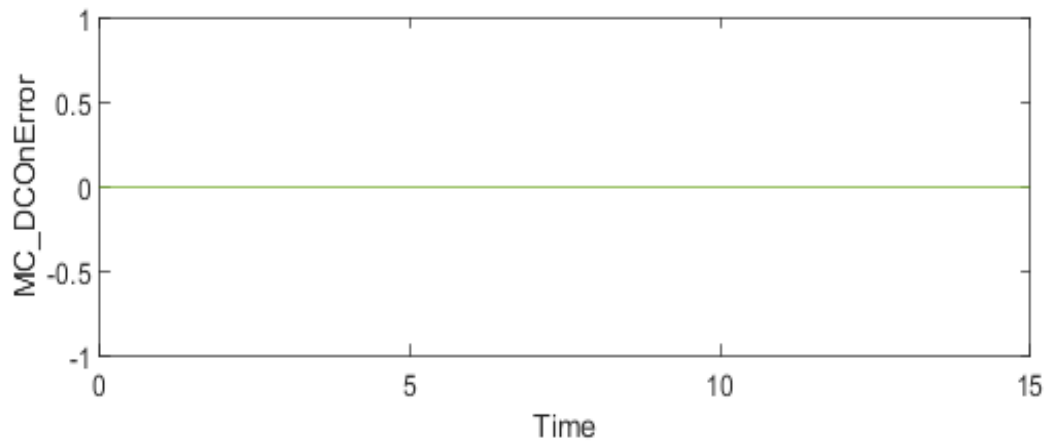
Name	Data Type	Units	Sample Time	Interp	Sync
VehReady	boolean		0.01	zoh	union
BeeperStOut	boolean		0.01	zoh	union



[Back to Report Summary](#)[Back to Signal Summary](#)

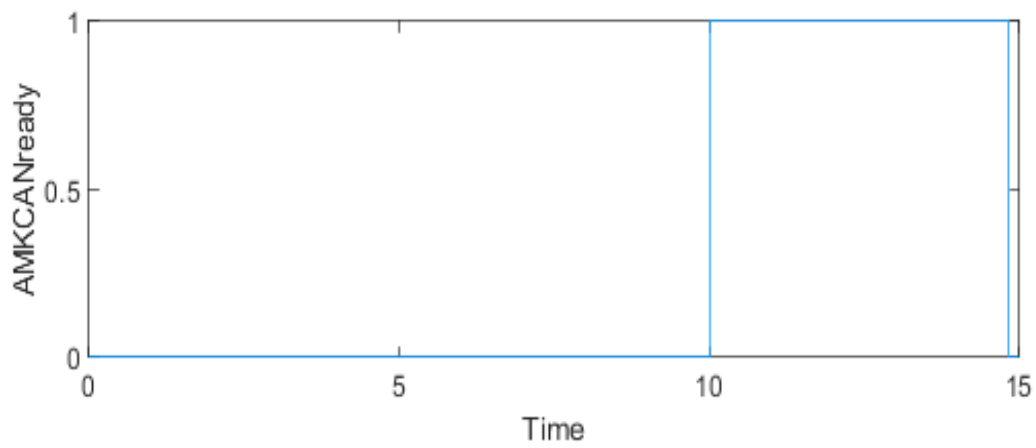
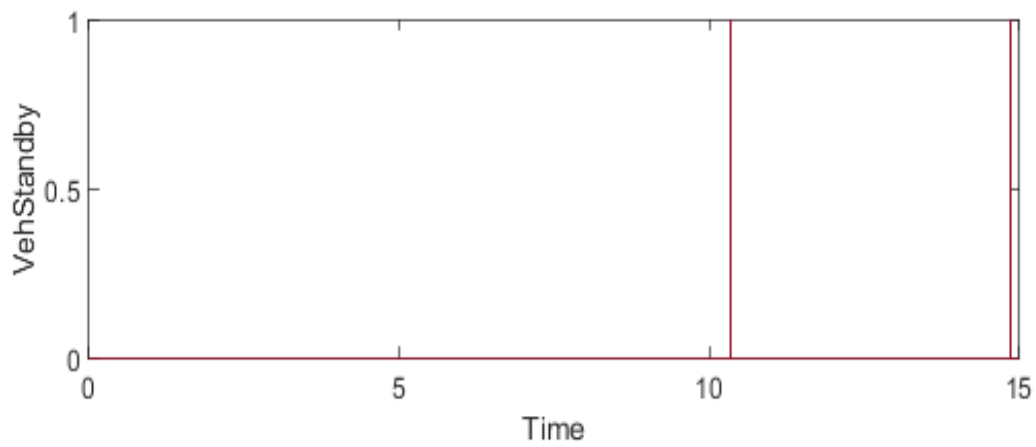
Name	Data Type	Units	Sample Time	Interp	Sync
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MC_DCONError	boolean		0.01	zoh	union
errorReset	double		0.01	zoh	union



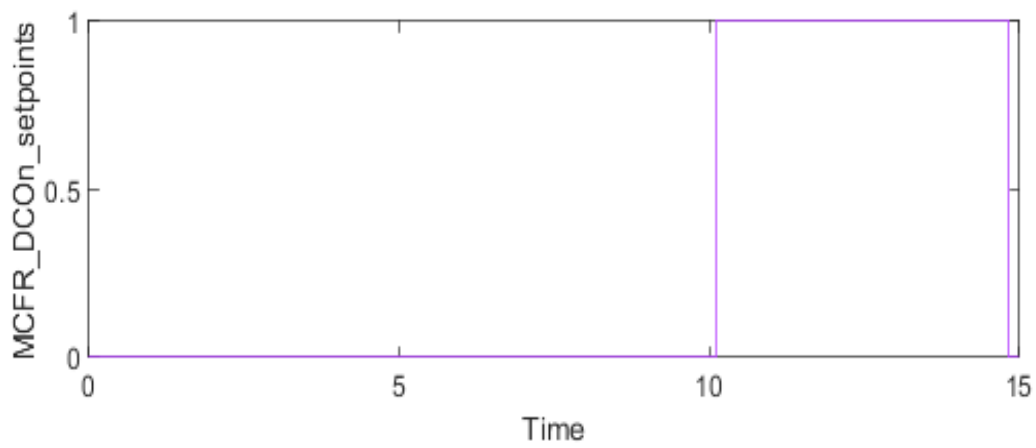
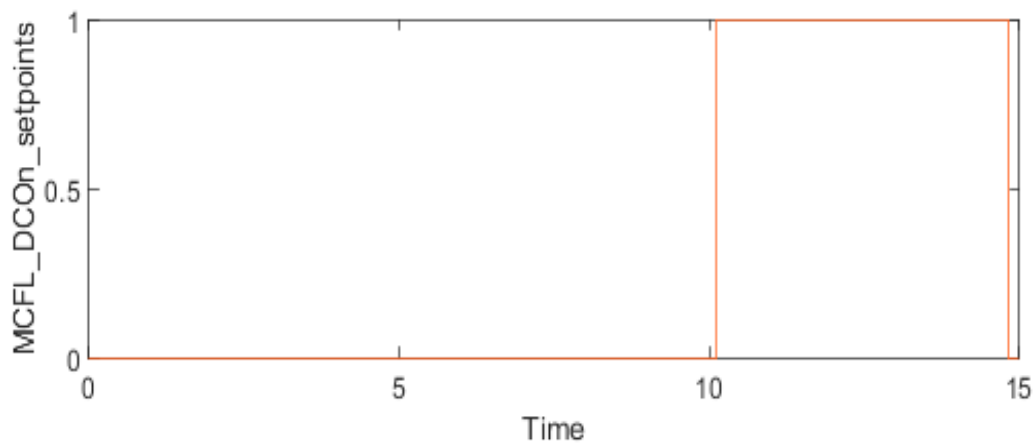
[Back to Report Summary](#)[Back to Signal Summary](#)

Name	Data Type	Units	Sample Time	Interp	Sync
VehStandby	boolean		0.01	zoh	union
AMKCANready	boolean		0.01	zoh	union



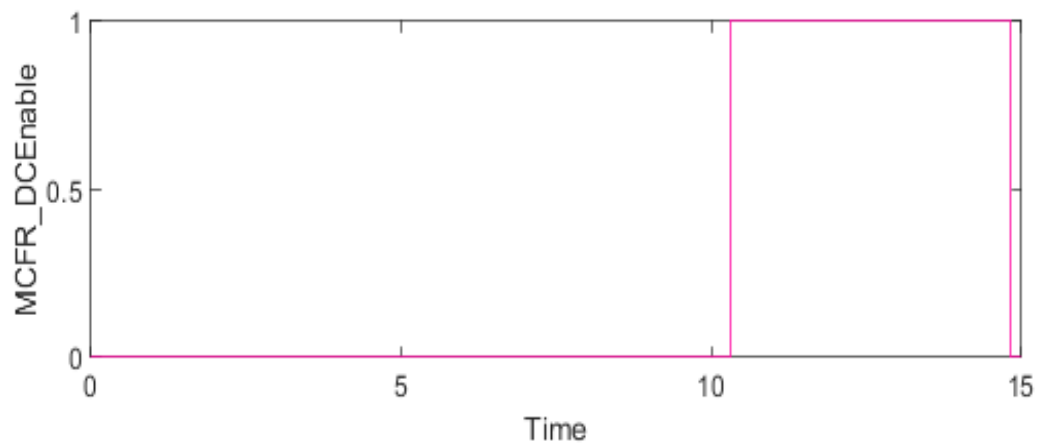
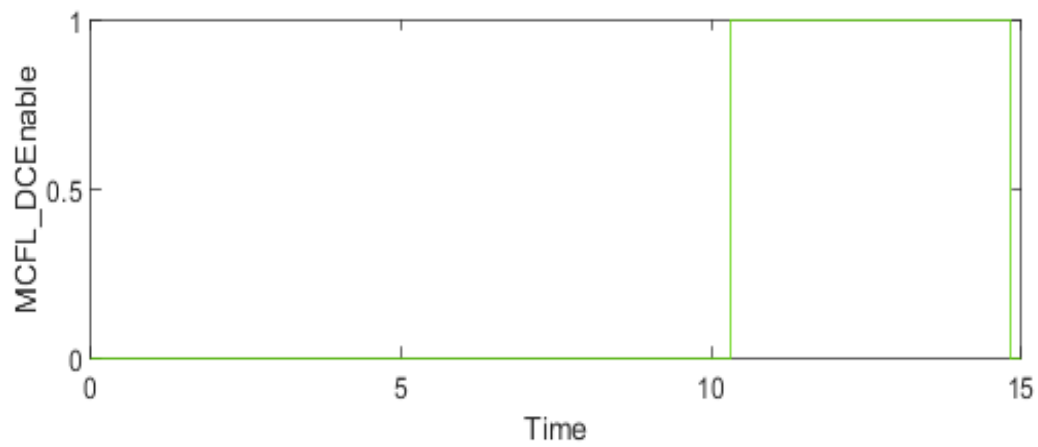
[Back to Report Summary](#)[Back to Signal Summary](#)

Name	Data Type	Units	Sample Time	Interp	Sync
MCFL_DCON_setpoints	boolean		0.01	zoh	union
MCFR_DCON_setpoints	boolean		0.01	zoh	union



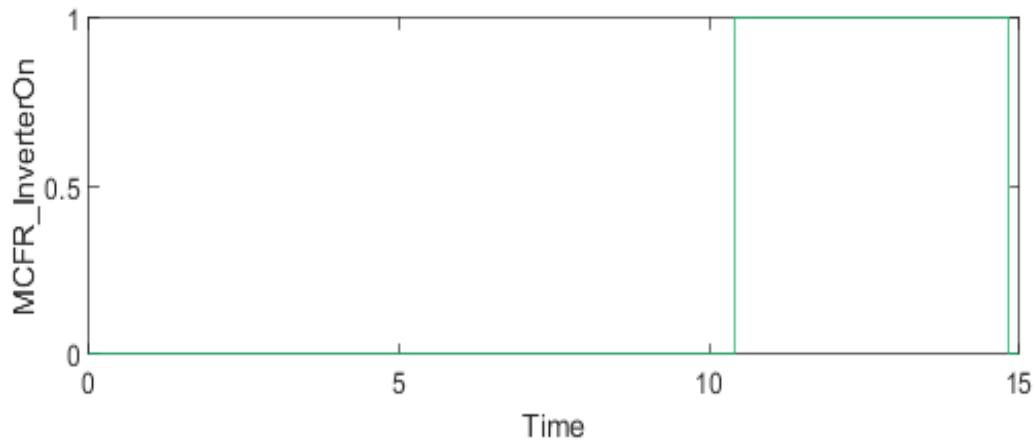
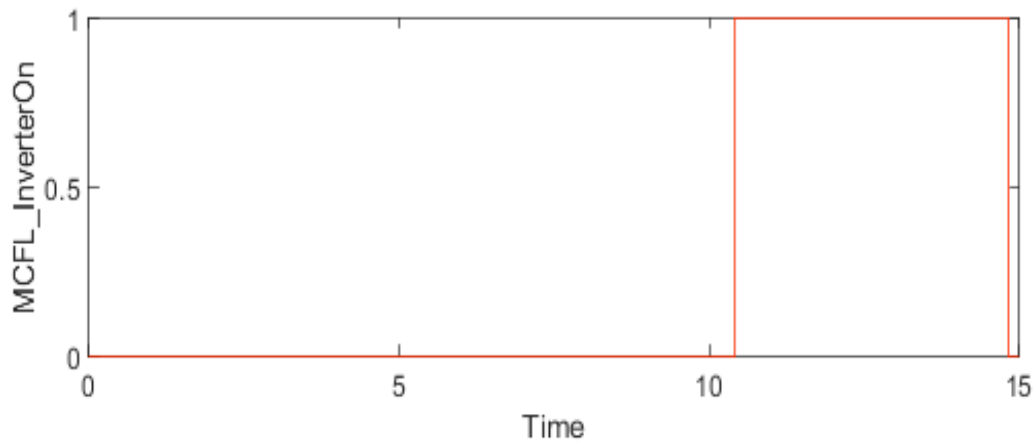
[Back to Report Summary](#)[Back to Signal Summary](#)

Name	Data Type	Units	Sample Time	Interp	Sync
MCFL_DCEnable	boolean		0.01	zoh	union
MCFR_DCEnable	boolean		0.01	zoh	union



[Back to Report Summary](#)[Back to Signal Summary](#)

Name	Data Type	Units	Sample Time	Interp	Sync
MCFL_InverterOn	boolean		0.01	zoh	union
MCFR_InverterOn	boolean		0.01	zoh	union



[Back to Report Summary](#)[Back to Signal Summary](#)

Simulation Logs:

执行为类 Simulink.BlockDiagram 定义的事件

SLExecEvent::POST_TERMINATE_OF_SIM_MODEL_EVENT 的侦听程序回调时出现

错误:

对象无效或已删除。

出错 ModelSlicer/refreshDynamicSliceForStepFromExistingData

出错 ModelSlicer>@(~,~)obj.refreshDynamicSliceForStepFromExistingData

出错 Simulink.Simulation.internal.DesktopSimHelper

出错 Simulink.Simulation.internal.DesktopSimHelper.sim

出错 Simulink.SimulationInput/sim

出错 [sltest.harness.SimulationInput/sim](#) (第 91 行)

out = sim@Simulink.SimulationInput(obj, varargin{:});

出错 MultiSim.internal.runSingleSim

出错 MultiSim.internal.SimulationRunnerSerial/executeImplSingle

出错 MultiSim.internal.SimulationRunnerSerial/executeImpl

出错 Simulink.SimulationManager/executeSims

出错 Simulink.SimulationManager/execute

出错 Simulink.SimulationManager/run

出错 stm.internal.SimulationInput.simulateAndEvaluate>simulate

出错 stm.internal.SimulationInput.simulateAndEvaluate

出错 stm.internal.util.simInEvaluateAndSimulate

[Back to Report Summary](#)