

workspace - Synergy Configuration - S7G2_With_USB/configuration.xml - e² studio

File Edit Navigate Search Project Renesas Views Run Window Help

Debug S7G2_With_USB.elf

Project Explorer

- NetXDuo_MQTT_Client_MG_AP
- S7G2_Host_Device_No_LEDs
- S7G2_Link
- S7G2_No_USB
- S7G2_USB_Device_Only
- S7G2_With_USB
 - Binaries
 - Includes
 - src
 - synergy
 - Debug
 - script
 - synergy_cfg
 - buildinfo.xml
 - configuration.xml
 - HelixControllerS7G2.elf.jlink
 - R7F57G27H3A01CFC.pincfg
 - S7G2_With_USB.elf.jlink
 - S7G2_With_USB.elf.launch
 - S7G2-SK.pincfg
 - synergy_cfg.txt

[S7G2_With_USB] Synergy Configuration

Pins Configuration

Generate Project Content

[Pins Tutorial](#)

Select pin configuration

S7G2-SK.pincfg ☒ Generate data: g_bsp_pin_cfg

Pin Selection

type filter text

- Ports
 - P0
 - P1
 - P2
 - P3
 - P4
 - P5
 - P6
 - P7
 - P8
 - P9
 - PA
 - PB
- Peripherals
 - Monitoring:CAC
 - Analog:ADC
 - ADC
 - ADC0
 - ADC1
 - Analog:COMP
 - Analog:DAC12
 - DAC120
 - DAC121
 - Connectivity:CAN
 - CAN0
 - CAN1
 - Connectivity:ETHERC
 - ETHERC0.RMII
 - ETHERC1.RMII
 - Connectivity:ILC
 - Connectivity:SCI
 - serin

Pin Configuration

Module name: ADC0

Usage: The below signal list may contain dedicated pins that cannot be configured and are shown for clarity

Operation Mode: Custom

Input/Output

ADTRG:	None	
AN00:	None	
AN01:	P001	
AN02:	P002	
AN03:	P008	
AN04:	P009	
AN05:	None	
AN06:	P015	
AN16:	None	
AN17:	None	
AN18:	None	
AN19:	None	
PGAVSS0:	None	

Summary | BSP | Clocks | Pins | Threads | Messaging | Components

Properties Problems Smart Browser

Properties are not available.

Pin Conflicts Console Debugger Console Error Log Progress

Synergy

workspace - Synergy Configuration - S7G2_With_USB/configuration.xml - e² studio

File Edit Navigate Search Project Renesas Views Run Window Help

Debug

S7G2_With_USB.elf

Quick Access

Project Explorer

NetXDuo_MQTT_Client_MG_AP

S7G2_Host_Device_No_LEDs

S7G2_Link

S7G2_No_USB

S7G2_USB_Device_Only

S7G2_With_USB

- Binaries
- Includes
- src
- synergy
- Debug
- script
- synergy_cfg
 - buildinfo.xml
 - configuration.xml
 - HelixControllerS7G2.elf.jlink
 - R7F57G27H3A01CFC.pincfg
 - S7G2_With_USB.elf.jlink
 - S7G2_With_USB.elf.launch
 - S7G2-SK.pincfg
 - synergy_cfg.bt

[S7G2_With_USB] Synergy Configuration

Generate Project Content

[Pins Tutorial](#)

Select pin configuration

S7G2-SK.pincfg

Generate data: g_bsp_pin_cfg

Pin Selection

type filter text

Ports

- P0
- P1
- P2
- P3
- P4
- P5
- P6
- P7
- P8
- P9
- PA
- PB

Peripherals

- Monitoring:CAC
- Analog:ADC
 - ADC0
 - ADC1
- Analog:COMP
- Analog:DAC12
 - DAC120
 - DAC121
- Connectivity:CAN
 - CAN0
 - CAN1
- Connectivity:ETHERC
 - ETHERC0.RMII
 - ETHERC1.RMII
- Connectivity:IIC
- Connectivity:SCI

Pin Configuration

Module name: ADC0

Usage: The below signal list may contain dedicated pins that cannot be configured and are shown for clarity

Operation Mode: Custom

Input/Output

ADTRG:	None	
AN00:	None	
AN01:	P001	
AN02:	P002	
AN03:	P008	
AN04:	P009	
AN05:	None	
AN06:	P015	
AN16:	None	
AN17:	None	
AN18:	None	
AN19:	None	
PGAVSS0:	None	

Summary

BSP

Clocks

Pins

Threads

Messaging

Components

Properties

Problems

Smart Browser

Properties are not available.

Pin Conflicts

Console

Debugger Console

Error Log

Progress

Synergy

workspace - Synergy Configuration - S7G2_With_USB/configuration.xml - e² studio

File Edit Navigate Search Project Renesas Views Run Window Help

Debug S7G2_With_USB.elf

Project Explorer

- NetXDuo_MQTT_Client_MG_AP
- S7G2_Host_Device_No_LEDs
- S7G2_JLink
- S7G2_No_USB
- S7G2_USB_Device_Only
- S7G2_With_USB
 - Binaries
 - Includes
 - src
 - synergy
 - Debug
 - script
 - synergy_cfg
 - buildinfo.xml
 - configuration.xml
 - HelixControllerS7G2.elf.jlink
 - R7F57G27H3A01CFC.pincfg
 - S7G2_With_USB.elf.jlink
 - S7G2_With_USB.elf.launch
 - S7G2-SK.pincfg
 - synergy_cfg.txt

[S7G2_With_USB] Synergy Configuration

Pins Configuration

Generate Project Content

[Pins Tutorial](#)

Select pin configuration

S7G2-SK.pincfg ☒ Generate data: g_bsp_pin_cfg

Pin Selection

type filter text

- Ports
 - P0
 - P1
 - P2
 - P3
 - P4
 - P5
 - P6
 - P7
 - P8
 - P9
 - PA
 - PB
- Peripherals
 - Monitoring:CAC
 - Analog:ADC
 - ADC0
 - ADC1
 - Analog:COMP
 - Analog:DAC12
 - DAC120
 - DAC121
 - Connectivity:CAN
 - CAN0
 - CAN1
 - Connectivity:ETHERC
 - ETHERC0.RMII
 - ETHERC1.RMII
 - Connectivity:ILIC
 - Connectivity:SCI

Pin Configuration

Module name: DAC120

Operation Mode: Enabled

Input/Output

DA: P014

Summary | BSP | Clocks | Pins | Threads | Messaging | Components

Properties Problems Smart Browser

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Pin Conflicts Console Debugger Console Error Log Progress

Synergy

workspace - Synergy Configuration - S7G2_With_USB/configuration.xml - e² studio

File Edit Navigate Search Project Renesas Views Run Window Help

Debug

S7G2_With_USB.elf

Project Explorer

NetXDuo_MQTT_Client_MG_AP

S7G2_Host_Device_No_LEDs

S7G2_JLink

S7G2_No_USB

S7G2_USB_Device_Only

S7G2_With_USB

- Binaries
- Includes
- src
- synergy
- Debug
- script
- synergy_cfg
 - buildinfo.xml
 - configuration.xml
 - HelixControllerS7G2.elf.jlink
 - R7FS7G27H3A01CFC.pincfg
 - S7G2_With_USB.elf.jlink
 - S7G2_With_USB.elf.launch
 - S7G2-SK.pincfg
 - synergy_cfg.txt

[S7G2_With_USB] Synergy Configuration

Pins Configuration

Generate Project Content

Select pin configuration

S7G2-SK.pincfg

☒ Generate data: g_bsp_pin_cfg

Pin Selection

type filter text

Ports

- P0
- P1
- P2
- P3
- P4
- P5
- P6
- P7
- P8
- P9
- PA
- PB

Peripherals

- Monitoring:CAC
- Analog:ADC
 - ADC0
 - ADC1
- Analog:CMP
- Analog:DAC12
 - DAC120
 - DAC121
- Connectivity:CAN
 - CAN0
 - CAN1
- Connectivity:ETHERC
 - ETHERC0.RMII
 - ETHERC1.RMII
- Connectivity:ILIC
- Connectivity:SCI

Pin Configuration

Module name: CAN0

Pin Group Selection: Mixed

Operation Mode: Enabled

Input/Output

CRX: ☒ P402

CTX: ☒ P401

Summary

BSP

Clocks

Pins

Threads

Messaging

Components

Properties Problems Smart Browser

Properties are not available.

Pin Conflicts Console Debugger Console Error Log Progress

Synergy

workspace - Synergy Configuration - S7G2_With_USB/configuration.xml - e² studio

File Edit Navigate Search Project Renesas Views Run Window Help

Debug S7G2_With_USB.elf

Project Explorer

- NetXDuo_MQTT_Client_MG_AP
- S7G2_Host_Device_No_LEDs
- S7G2_Link
- S7G2_No_USB
- S7G2_USB_Device_Only
- S7G2_With_USB
 - Binaries
 - Includes
 - src
 - synergy
 - Debug
 - script
 - synergy_cfg
 - buildinfo.xml
 - configuration.xml
 - HelixControllerS7G2.elf.jlink
 - R7F57G27H3A01CFC.pincfg
 - S7G2_With_USB.elf.jlink
 - S7G2_With_USB.elf.launch
 - S7G2-SK.pincfg
 - synergy_cfg.bit

[S7G2_With_USB] Synergy Configuration

Pins Configuration

Select pin configuration

S7G2-SK.pincfg ☒ Generate data: g_bsp_pin_cfg

Pin Selection

type filter text

- Ports
 - P0
 - P1
 - P2
 - P3
 - P4
 - P5
 - P6
 - P7
 - P8
 - P9
 - PA
 - PB
- Peripherals
 - Monitoring:CAC
 - Analog:ADC
 - ADC0
 - ADC1
 - Analog:CMP
 - Analog:DAC12
 - DAC120
 - DAC121
 - Connectivity:CAN
 - CAN0
 - CAN1
 - Connectivity:ETHERC
 - ETHERC0.RMII
 - ETHERC1.RMII**
 - Connectivity:ILC
 - Connectivity:SCI
 - crin

Pin Configuration

Module name: ETHERC1.RMII

Usage: For ETHERC, use same Pin Group for signals
-Please refer to the MCU User's Manual.
Selection of MII and RMII are mutually exclusive.
Operation Mode will only allow 'Disabled' if RMII is enabled.

Pin Group Selection: .A only

Operation Mode:

Input/Output

REF50CK:	P701	
TXD0:	P700	
TXD1:	P406	
TXD_EN:	P405	
RXD0:	P702	
RXD1:	P703	
RX_ER:	P704	
CRS_DV:	P705	
MDC:	P403	
MDIO:	P404	

Summary BSP Clocks Pins Threads Messaging Components

Properties Problems Smart Browser

Properties are not available.

Pin Conflicts Console Debugger Console Error Log Progress

Synergy

workspace - Synergy Configuration - S7G2_With_USB/configuration.xml - e² studio

File Edit Navigate Search Project Renesas Views Run Window Help

Debug S7G2_With_USB.elf

Project Explorer

- NetXDuo_MQTT_Client_MG_AP
- S7G2_Host_Device_No_LEDs
- S7G2_Link
- S7G2_No_USB
- S7G2_USB_Device_Only
- S7G2_With_USB
 - Binaries
 - Includes
 - src
 - synergy
 - Debug
 - script
 - synergy_cfg
 - buildinfo.xml
 - configuration.xml
 - HelixControllerS7G2.elf.jlink
 - R7FS7G27H3A01CFC.pincfg
 - S7G2_With_USB.elf.jlink
 - S7G2_With_USB.elf.launch
 - S7G2-SK.pincfg
 - synergy_cfg.txt

[S7G2_With_USB] Synergy Configuration

Pins Configuration

Generate Project Content

[Pins Tutorial](#)

Select pin configuration

S7G2-SK.pincfg ☒ Generate data: g_bsp_pin_cfg

Pin Selection

type filter text

- > Connectivity:ILIC
- > ✓ Connectivity:SCI
 - SCI0
 - SCI1
 - SCI2
 - SCI3
 - SCI4
 - SCI5
 - SCI6
 - SCI7
 - ✓ SCI8
 - SCI9
- > Connectivity:SPI
- > Connectivity:SSI
- > ✓ Connectivity:USBFS
 - ✓ USBFS0
- > ✓ Connectivity:USBHS
 - ✓ USBHS0
- > Input:CTSU
- > ✓ Input:IRQ
 - ✓ IRQ0
- > Input:KINT
- > Graphics:GLCDC
- > Graphics:PDC
- > ✓ Storage:QSPI
 - ✓ QSPI0
- > Storage:SDHI
- > System:BUS
- > ✓ System:CGC
- > ✓ System:DEBUG
 - ✓ DEBUG0
- > Time:ACT

Pin Configuration

Module name: SCIO

Usage: When using Simple I2C mode, ensure port pins output type is n-ch open drain. When switching between I2C and other modes, first disable.

Pin Group Selection: Mixed

Operation Mode: Simple SPI

Input/Output

TXD_MOSI:	✓ P411	
RXD_MISO:	✓ P410	
SCK:	✓ P412	
CTS_RTS_SS:	None	
SDA:	None	
SCL:	None	

Summary BSP Clocks Pins Threads Messaging Components

Properties Problems Smart Browser

Properties are not available.

Pin Conflicts Console Debugger Console Error Log Progress

Synergy

workspace - Synergy Configuration - S7G2_With_USB/configuration.xml - e² studio

File Edit Navigate Search Project Renesas Views Run Window Help

Debug S7G2_With_USB.elf

Quick Access

Project Explorer

NetXDuo_MQTT_Client_MG_AP

S7G2_Host_Device_No_LEDs

S7G2_Link

S7G2_No_USB

S7G2_USB_Device_Only

S7G2_With_USB

- Binaries
- Includes
- src
- synergy
- Debug
- script
- synergy_cfg
 - buildinfo.xml
 - configuration.xml
 - HelixControllerS7G2.elf.jlink
 - R7FS7G27H3A01CFC.pincfg
 - S7G2_With_USB.elf.jlink
 - S7G2_With_USB.elf.launch
 - S7G2-SK.pincfg
 - synergy_cfg.txt

[S7G2_With_USB] Synergy Configuration

Pins Configuration

Generate Project Content

Select pin configuration

S7G2-SK.pincfg ☒ Generate data: g_bsp_pin_cfg

Pin Selection

type filter text

Connectivity:IIC

- Connectivity:SCI
 - SCI0
 - SCI1
 - SCI2
 - SCI3
 - SCI4
 - SCI5
 - SCI6
 - SCI7
 - SCI8
 - SCI9

Connectivity:SPI

Connectivity:SSI

Connectivity:USBFS

- USBFS0

Connectivity:USBHS

- USBHS0

Input:CTS0

Input:IRQ

- IRQ0

Input:KINT

Graphics:GLCDC

Graphics:PDC

Storage:QSPI

- QSPI0

Storage:SDHI

System:BUS

System:CGC

- DEBUG0

System:DEB00

System:ACT

Pin Configuration

Module name: SCI8

Usage: When using Simple I2C mode, ensure port pins output type is n-ch open drain. When switching between I2C and other modes, first disable.

Pin Group Selection: Mixed

Operation Mode: Simple SPI

Input/Output

TXD_MOSI: P105

RXD_MISO: P104

SCK: P106

CTS_RTS_SS: None

SDA: None

SCL: None

Summary

BSP

Clocks

Pins

Threads

Messaging

Components

Properties Problems Smart Browser

Properties are not available.

Pin Conflicts Console Debugger Console Error Log Progress

Synergy

workspace - Synergy Configuration - S7G2_With_USB/configuration.xml - e² studio

File Edit Navigate Search Project Renesas Views Run Window Help

Debug

S7G2_With_USB.elf

Quick Access

Project Explorer

NetXDuo_MQTT_Client_MG_AP

S7G2_Host_Device_No_LEDs

S7G2_Link

S7G2_No_USB

S7G2_USB_Device_Only

S7G2_With_USB

- Binaries
- Includes
- src
- synergy
- Debug
- script
- synergy_cfg
- buildinfo.xml
- configuration.xml
- HelixControllerS7G2.elf.jlink
- R7FS7G27H3A01CFC.pincfg
- S7G2_With_USB.elf.jlink
- S7G2_With_USB.elf.launch
- S7G2-SK.pincfg
- synergy_cfg.txt

[S7G2_With_USB] Synergy Configuration

Pins Configuration

Generate Project Content

Select pin configuration

S7G2-SK.pincfg

Generate data: g_bsp_pin_cfg

Pin Selection

type filter text

Connectivity:IIC

Connectivity:SCI

- SCI0
- SCI1
- SCI2
- SCI3
- SCI4
- SCI5
- SCI6
- SCI7
- SCI8
- SCI9

Connectivity:SPI

Connectivity:SSI

Connectivity:USBFS

- USBFS0

Connectivity:USBHS

- USBHS0

Input:CTSU

Input:IRQ

- IRQ0

Input:KINT

Graphics:GLCDC

Graphics:PDC

Storage:QSPI

- QSPI0

Storage:SDHI

System:BUS

System:CGC

System:DEBUG

- DEBUG0

Timer:ACT

Pin Configuration

Module name: USBFS0

Usage: The below signal list may contain dedicated pins that cannot be configured and are shown for clarity

Operation Mode: Device

Input/Output

USBDP: USBDP

USBDM: USBDM

OVRCLRB: None

OVRCLRA: None

VBUSEN: None

VBUS: P407

EXICEN: None

ID: None

VCCUSB: VCCUSB

VSSUSB: VSSUSB

Summary

BSP

Clocks

Pins

Threads

Messaging

Components

Properties Problems Smart Browser

Properties are not available.

Pin Conflicts Console Debugger Console Error Log Progress

Synergy



Project Explorer

- NetXDuo_MQTT_Client_MG_AP
- S7G2_Host_Device_No_LEDs
- S7G2_Link
- S7G2_No_USB
- S7G2_USB_Device_Only
- S7G2_With_USB
 - Binaries
 - Includes
 - src
 - synergy
 - Debug
 - script
 - synergy_cfg
 - buildinfo.xml
 - configuration.xml
 - HelixControllerS7G2.elf.jlink
 - R7FS7G27H3A01CFC.pincfg
 - S7G2_With_USB.elf.jlink
 - S7G2_With_USB.elf.launch
 - S7G2-SK.pincfg
 - synergy_cfg.txt

[S7G2_With_USB] Synergy Configuration

Pins Configuration

Generate Project Content

[Pins Tutorial](#)

Select pin configuration

S7G2-SK.pincfg

☒ Generate data: g_bsp_pin_cfg

Pin Selection

type filter text

- > Connectivity: IIC
 - ✓ SCIO
 - SC11
 - SC12
 - SC13
 - SC14
 - SC15
 - SC16
 - SC17
 - ✓ SC18
 - SC19
- > Connectivity: SPI
- > Connectivity: SSI
- ✓ Connectivity: USBFS
 - ✓ USBFS0
- ✓ Connectivity: USBHS
 - ✓ USBHS0
- > Input: CTSU
- ✓ Input: IRQ
 - ✓ IRQ0
- > Input: KINT
- > Graphics: GLCDC
- > Graphics: PDC
- ✓ Storage: QSPI
 - ✓ QSPI0
- > Storage: SDHI
- > System: BUS
- > System: CGC
- ✓ System: DEBUG
 - ✓ DEBUG0
- > System: AGT

Pin Configuration

Module name: USBHS0

Usage: The below signal list may contain dedicated pins that cannot be configured and are shown for clarity

Operation Mode: Host

Input/Output

USBHSDP:	USBHSDP	
USBHSDM:	USBHSDM	
OVRCURB:	None	
OVRCURA:	P707	
VBUSEN:	PB00	
VBUS:	None	
EXICEN:	None	
ID:	None	
USBHSRREF:	USBHSRREF	
AVCCUSBHS:	AVCCUSBHS	
AVSSUSBHS:	AVSSUSBHS	
PVSSUSBHS:	PVSSUSBHS	
VCCUSBHS:	VCCUSBHS	
VSS1USBHS:	VSS1USBHS	
VSS2USBHS:	VSS2USBHS	

Properties Problems Smart Browser

Properties are not available.

Pin Conflicts Console Debugger Console Error Log Progress

Synergy

workspace - Synergy Configuration - S7G2_With_USB/configuration.xml - e² studio

File Edit Navigate Search Project Renesas Views Run Window Help

Debug S7G2_With_USB.elf

Project Explorer

- NetXDuo_MQTT_Client_MG_AP
- S7G2_Host_Device_No_LEDs
- S7G2_JLink
- S7G2_No_USB
- S7G2_USB_Device_Only
- S7G2_With_USB
 - Binaries
 - Includes
 - src
 - synergy
 - Debug
 - script
 - synergy_cfg
 - buildinfo.xml
 - configuration.xml
 - HelixControllerS7G2.elf.jlink
 - R7F57G27H3A01CFC.pincfg
 - S7G2_With_USB.elf.jlink
 - S7G2_With_USB.elf.launch
 - S7G2-SK.pincfg
 - synergy_cfg.txt

[S7G2_With_USB] Synergy Configuration

Pins Configuration

Select pin configuration

S7G2-SK.pincfg ☒ Generate data: g_bsp_pin_cfg

Pin Selection

type filter text

- > Connectivity:ILC
- > ✓ Connectivity:SCI
 - ✓ SCI0
 - SCI1
 - SCI2
 - SCI3
 - SCI4
 - SCI5
 - SCI6
 - SCI7
 - ✓ SCI8
 - SCI9
- > Connectivity:SPI
- > Connectivity:SSI
- > ✓ Connectivity:USBFS
 - ✓ USBFS0
- > ✓ Connectivity:USBHS
 - ✓ USBHS0
- > Input:CTSU
- > ✓ Input:IRQ
 - ✓ IRQ0
- > Input:KINT
- > Graphics:GLCDC
- > Graphics:PDC
- > ✓ Storage:QSPI
 - ✓ QSPI0
- > Storage:SDHI
- > System:BUS
- > ✓ System:CGC
- > ✓ System:DEBUG
 - ✓ DEBUG0
- > Time:ACT

Pin Configuration

usage: to use IRQ function with output or peripheral modes, change directly in port dialog

Operation Mode: Enabled

Input/Output

NMI:	None	
IRQ00:	None	
IRQ01:	None	
IRQ02:	None	
IRQ03:	None	
IRQ04:	None	
IRQ05:	None	
IRQ06:	✓ P000	
IRQ07:	None	
IRQ08:	None	
IRQ09:	None	
IRQ10:	✓ P005	
IRQ11:	None	
IRQ12:	None	
IRQ13:	None	
IRQ14:	✓ P512	
IRQ15:	None	

Summary | BSP | Clocks | Pins | Threads | Messaging | Components

Properties Problems Smart Browser

Properties are not available.

Pin Conflicts Console Debugger Console Error Log Progress

Synergy

workspace - Synergy Configuration - S7G2_With_USB/configuration.xml - e² studio

File Edit Navigate Search Project Renesas Views Run Window Help

Debug S7G2_With_USB.elf

Project Explorer

- NetXDuo_MQTT_Client_MG_AP
- S7G2_Host_Device_No_LEDs
- S7G2_Link
- S7G2_No_USB
- S7G2_USB_Device_Only
- S7G2_With_USB
 - Binaries
 - Includes
 - src
 - synergy
 - Debug
 - script
 - synergy_cfg
 - buildinfo.xml
 - configuration.xml
 - HelixControllerS7G2.elf.jlink
 - R7FS7G27H3A01CFC.pincfg
 - S7G2_With_USB.elf.jlink
 - S7G2_With_USB.elf.launch
 - S7G2-SK.pincfg
 - synergy_cfg.txt

[S7G2_With_USB] Synergy Configuration

Pins Configuration

Generate Project Content

[Pins Tutorial](#)

Select pin configuration

S7G2-SK.pincfg ☒ Generate data: g_bsp_pin_cfg

Pin Selection

type filter text

- ✓ USBHS0
- > Input:CTS0
- ✓ Input:IRQ
- ✓ IRQ0
- > Input:KINT
- > Graphics:GLCDC
- > Graphics:PDC
- ✓ Storage:QSPI
- ✓ QSPI0
- > Storage:SDHI
- > System:BUS
- ✓ System:CGC
- ✓ System:DEBUG
- ✓ DEBUG0
- > Timer:AGT
- ✓ Timer:GPT
 - GPT0
 - GPT1
 - GPT2
 - GPT3
 - GPT4
 - GPT5
 - GPT6
 - GPT7
 - GPT8
 - GPT9
 - ✓ GPT10
 - GPT11
 - GPT12
 - GPT13
- > Timer:OPS
- > Timer:ONEC

Pin Configuration

Module name: QSPI0

Usage: For QSPI, same Pin Group Recommended

Pin Group Selection: _A only

Operation Mode: Custom

Input/Output

QSPCLK:	✓ P500	
QSSL:	✓ P501	
OIO0:	✓ P502	
QIO1:	✓ P503	
QIO2:	✓ P504	
QIO3:	✓ P505	

Summary | BSP | Clocks | Pins | Threads | Messaging | Components

Properties Problems Smart Browser

Properties are not available.

Pin Conflicts Console Debugger Console Error Log Progress

Synergy

workspace - Synergy Configuration - S7G2_With_USB/configuration.xml - e² studio

File Edit Navigate Search Project Renesas Views Run Window Help

Debug S7G2_With_USB.elf

Quick Access

Project Explorer

NetXDuo_MQTT_Client_MG_AP
S7G2_Host_Device_No_LEDs
S7G2_Link
S7G2_No_USB
S7G2_USB_Device_Only
S7G2_With_USB
 Binaries
 Includes
 src
 synergy
 Debug
 script
 synergy_cfg
 buildinfo.xml
 configuration.xml
 HelixControllerS7G2.elf.jlink
 R7F57G27H3A01CFC.pincfg
 S7G2_With_USB.elf.jlink
 S7G2_With_USB.elf.launch
 S7G2-SK.pincfg
 synergy_cfg.bit

[S7G2_With_USB] Synergy Configuration

Pins Configuration

Generate Project Content

[Pins Tutorial](#)

Select pin configuration

S7G2-SK.pincfg ☒ Generate data: g_bsp_pin_cfg

Pin Selection

type filter text

✓ USBHS0

Input:CTSU

✓ Input:IRQ

✓ IRQ0

Input:KINT

Graphics:GLCDC

Graphics:PDC

✓ Storage:QSPI

✓ QSPI0

Storage:SDHI

System:BUS

✓ System:CGC

CGC0

✓ System:DEBUG

DEBUG0

Timer:AGT

✓ Timer:GPT

GPT0

GPT1

GPT2

GPT3

GPT4

GPT5

GPT6

GPT7

GPT8

GPT9

✓ GPT10

GPT11

GPT12

GPT13

Timer:QPC

Pin Configuration

Module name: CGC0

Usage: The below signal list may contain dedicated pins that cannot be configured and are shown for clarity

Operation Mode: Main+Sub Osc

Input/Output

EXTAL: ✓ P212

XTAL: ✓ P213

XCIN: XCIN

XCOUT: XCOUT

CLKOUT: None

Summary BSP Clocks Pins Threads Messaging Components

Properties Problems Smart Browser

Properties are not available.

Pin Conflicts Console Debugger Console Error Log Progress

Synergy

workspace - Synergy Configuration - S7G2_With_USB/configuration.xml - e² studio

File Edit Navigate Search Project Renesas Views Run Window Help

Debug S7G2_With_USB.elf

Project Explorer

- NetXDuo_MQTT_Client_MG_AP
- S7G2_Host_Device_No_LEDs
- S7G2_Link
- S7G2_No_USB
- S7G2_USB_Device_Only
- S7G2_With_USB
 - Binaries
 - Includes
 - src
 - synergy
 - Debug
 - script
 - synergy_cfg
 - buildinfo.xml
 - configuration.xml
 - HelixControllerS7G2.elf.jlink
 - R7FS7G27H3A01CFC.pincfg
 - S7G2_With_USB.elf.jlink
 - S7G2_With_USB.elf.launch
 - S7G2-SK.pincfg
 - synergy_cfg.txt

[S7G2_With_USB] Synergy Configuration

Pins Configuration

Generate Project Content

[Pins Tutorial](#)

Select pin configuration

S7G2-SK.pincfg ☒ Generate data: g_bsp_pin_cfg

Pin Selection

type filter text

- ✓ USBHS0
- > Input:CTS0
- ✓ Input:IRQ
- ✓ IRQ0
- > Input:KINT
- > Graphics:GLCDC
- > Graphics:PDC
- ✓ Storage:QSPI
- ✓ QSPI0
- > Storage:SDHI
- > System:BUS
- ✓ System:CGC
- ✓ CGC0
- ✓ System:DEBUG
- ✓ DEBUG0
- > Timer:AGT
- ✓ Timer:GPT
 - GPT0
 - GPT1
 - GPT2
 - GPT3
 - GPT4
 - GPT5
 - GPT6
 - GPT7
 - GPT8
 - GPT9
 - ✓ GPT10
 - GPT11
 - GPT12
 - GPT13
- > Timer:ONE

Pin Configuration

Module name: DEBUG0

Usage: When switching between modes, first disable.

Operation Mode: JTAG

Input/Output

TCK:	✓ P300	→
TDI:	✓ P110	→
TDO:	✓ P109	→
TMS:	✓ P108	→
SWCLK:	None	→
SWDIO:	None	→
SWO:	None	→

Summary BSP Clocks Pins Threads Messaging Components

Properties Problems Smart Browser

Properties are not available.

Pin Conflicts Console Debugger Console Error Log Progress

Synergy

workspace - Synergy Configuration - S7G2_With_USB/configuration.xml - e² studio

File Edit Navigate Search Project Renesas Views Run Window Help

Debug S7G2_With_USB.elf

Project Explorer

- NetXDuo_MQTT_Client_MG_AP
- S7G2_Host_Device_No_LEDs
- S7G2_JLink
- S7G2_No_USB
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 - configuration.xml
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 - R7FS7G2H3A01CFC.pincfg
 - S7G2_With_USB.elf.jlink
 - S7G2_With_USB.elf.launch
 - S7G2-SK.pincfg
 - synergy_cfg.txt

[S7G2_With_USB] Synergy Configuration

Pins Configuration

Generate Project Content

[Pins Tutorial](#)

Select pin configuration

S7G2-SK.pincfg ☒ Generate data: g_bsp_pin_cfg

Pin Selection

type filter text

- ✓ USBHS0
- > Input:CTSU
- ✓ Input:IRQ
- ✓ IRQ0
- > Input:KINT
- > Graphics:GLCDC
- > Graphics:PDC
- ✓ Storage:QSPI
- ✓ QSPI0
- > Storage:SDHI
- > System:BUS
- ✓ System:CGC
- ✓ CGC0
- ✓ System:DEBUG
- ✓ DEBUG0
- > Timer:AGT
- ✓ Timer:GPT
 - GPT0
 - GPT1
 - GPT2
 - GPT3
 - GPT4
 - GPT5
 - GPT6
 - GPT7
 - GPT8
 - GPT9
 - ✓ GPT10
 - GPT11
 - GPT12
 - GPT13
- > Timer:APB

Pin Configuration

Module name: GPT10

Pin Group Selection: Mixed

Operation Mode: GTIOCA or GTIOCB

Input/Output

GTIOCA: ✓ P409

GTIOCB: None

Summary BSP Clocks Pins Threads Messaging Components

Properties Problems Smart Browser

Properties are not available.

Pin Conflicts Console Debugger Console Error Log Progress

Synergy