

Parallel and Distributed Computing – 1st Project Report

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1 Introduction

The goal of this project was to develop a series of algorithms that perform matrix multiplication and analyze their performance using various techniques, namely:

- Normal Matrix Multiplication;
- "Line by Line" Matrix Multiplication;
- Block Matrix Multiplication;

2 Algorithm Analysis

2.1 Normal Matrix Multiplication

This algorithm is the *naïve* implementation of the normal matrix multiplication procedure.

It iterates through each line of the first matrix and each column of the second matrix to perform the dot product between each vector, accumulating the results in the destination matrix.

The algorithm can be (grossly) coded as the following:

```
for (i = 0; i < mx_size; ++i)
    for (j = 0; j < mx_size; ++j)
        for (k = 0; k < mx_size; ++k)
            phc[i * mx_size + j] += pha[i * mx_size + k] * phb[k *
                mx_size + j];
```

where `mx_size` is the side length of each matrix `pha`, `phb` and `phc`.

2.2 "Line by Line" Matrix Multiplication

This algorithm is an improvement over the normal matrix multiplication algorithm.

It exploits the properties of cache locality to speed up the matrix product computations.

The algorithm can be (grossly) coded as the following:

```
for (i = 0; i < mx_size; ++i)
    for (k = 0; k < mx_size; ++k)
        for (j = 0; j < mx_size; ++j)
            phc[i * mx_size + j] += pha[i * mx_size + k] * phb[k *
                mx_size + j];
```

where `mx_size` is the side length of each matrix `pha`, `phb` and `phc`.

The main difference between this algorithm and the previous one is that, by changing the order of iteration of `j` and `k`, the processor does not need to load a new set of data into cache due to the multiplication order following the second matrix's lines, thus reducing drastically the number of cache misses resulting from the *naïve* implementation.

2.3 Block Matrix Multiplication

This reasoning behind this algorithm is to "divide and conquer": by dividing the original matrix into many sub-matrices and performing the multiplication on these, the process is sped up.

The algorithm used with each sub-matrix is the "Line by Line" one since it offers better performance over the *naïve* implementation.

Since the code for this algorithm is pretty big, it is not included in the report.

This algorithm also has another input parameter: the block size. This value controls the size of the resulting sub-matrices, which affects the performance of the algorithm, as can be seen in the following section.

3 Performance Analysis

The performance metrics collected to analyze the developed algorithms were:

- The execution time;
- The Level 1 Data Cache misses;
- The Level 2 Data Cache misses;

These metrics give us an overview of the behavior of the algorithms and allow us to derive another important metric: Gflops/s. This value indicates how many floating point operations were performed per second: (typically) the higher the value the more operations were performed and the faster the algorithm was.

3.1 Data graphics

The following graphics

4 Conclusions

All of the project's goals were achieved -

A Appendix

A.1 Code

Versions of the algorithms in C and Java are available in folder `src/`.