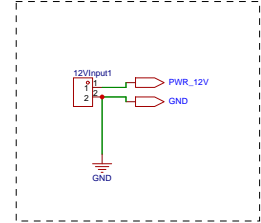
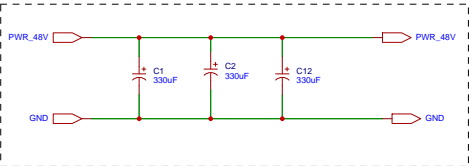


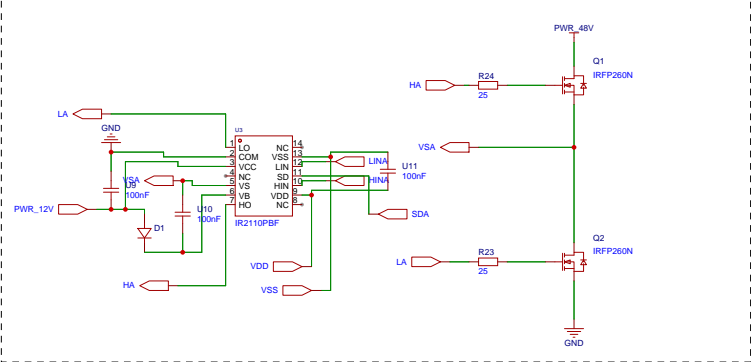
Input Voltage 12V



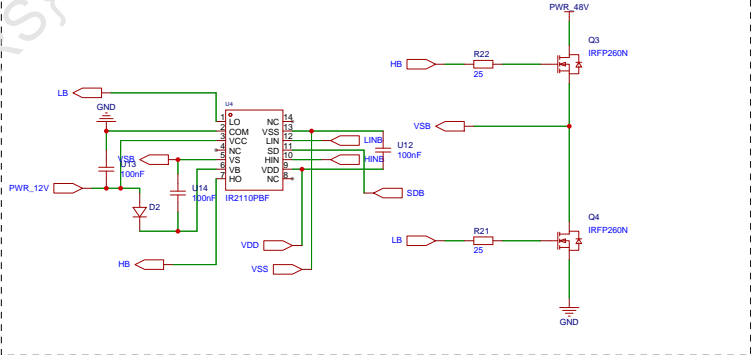
Capacitors Bank



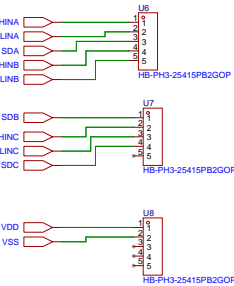
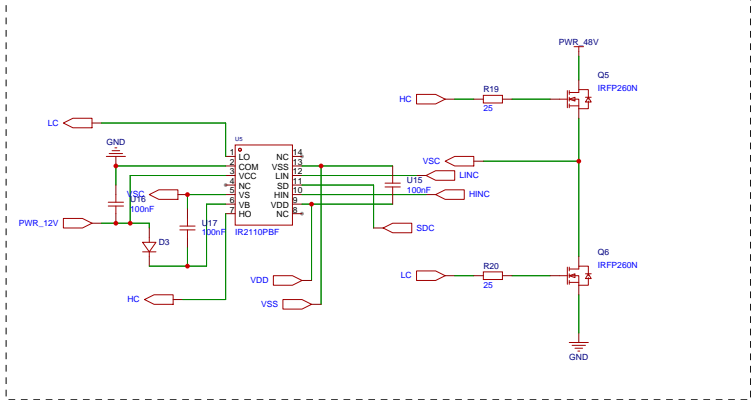
Phase A




Phase B



Phase C



Schematic	Schematic1			Update Date	2024-11-14
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