Important Note: We worked together continuously on Friday and Saturday to finish the project, therefore, we've done the same things together.

Friday:
November 24
- Drew pipelined datapath

Saturday:
November 25 5-10pm:
- Created all the registers needed between the stages and passed all the required signals
- Tested to see that the pipelined datapath is working but without hazard handling
- Attempted to make single memory

Sunday:
November 26 2-6pm:
- Added all the required units for hazard handling
- Tested the the pipeline was working with two memories fully

November 26 7-12pm:
- Implemented single memory
- Fixed Compression module
- Tested thoroughly
- Fixed datapath