



三级

研发

电子档

10年

CONFIDENTIAL					
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意见	所属部门	签名+日期			

版本：03

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# E1784AC63.A\_RM69090\_XM\_V01

## 1. Power On Sequence

W/R	Type	Register	Parameter	Description
				Turn on VCI
				Delay ( No Limit )
				Turn on VDDIO
				Delay ( No Limit )
				Turn on VBAT
				Delay >10ms
				Reset pin high
				Delay >10ms
				MIPI初始化
				Delay >1ms
w	0x15	0xFE	0x00	User Command
w	0x15	0x35	0x00	Enable TE
w	0x15	0x51	0xFF	
w	0x15	0x2A	0x00	
w			0x10	
w			0x01	
w			0x7F	
w	0x15	0x2B	0x00	
w			0x00	
w			0x01	
w			0xBF	
w	0x05	0x11		Sleep Out
				Delay >120ms
w	0x05	0x29		Display On

HFP	20	VFP	20
HBP	40	VBP	12
HPW	20	VPW	4
HPX	368	VPX	448
HAA	HBP+HPX	VAA	VBP+VPX
HAL	HFP+HBP+HPX	VAL	VFP+VBP+VPX

## 2. Power Off Sequence

R/W	Type	Register	Parameter	Description
w	0x05	0x28		Display off
w	0x05	0x10		Sleep in
				Delay >120ms
				Reset pin low
				Dealy >0ms
				Turn off VBAT
				Delay >10ms
				Turn off VC& VDDIO

3. Sleep In				
R/W	Type	Register	Parameter	Description
w	0x05	0x28		Display off
w	0x05	0x10		Sleep in
Delay > 120ms				

4. Sleep Out				
R/W	Type	Register	Parameter	Description
w	0x05	0x11		Sleep out
Delay > 120ms				
w	0x05	0x29		Display on

9. Idle mode In				
R/W	Type	Register	Parameter	Description
w	0x15	0xFE	0x00	user command
w	0x05	0x39		Idle in

10. Idle mode Out				
R/W	Type	Register	Parameter	Description
w	0x15	0xFE	0x00	user command
w	0x05	0x38		Idle out

11. Deep standby In				
R/W	Type	Register	Parameter	Description
w	0x15	0xFE	0x00	user command
w	0x15	0x4F	0x01	

12. Deep standby Out				
R/W	Type	Register	Parameter	Description
				Reset pin low
Delay > 3ms				
				Reset pin high
				Power on Sequence