

Figure 5.1: Logic Diagram for 5x8x4 PLA

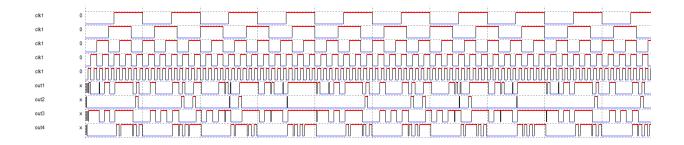


Figure 5.2: Output Waveform for 5x8x4 PLA

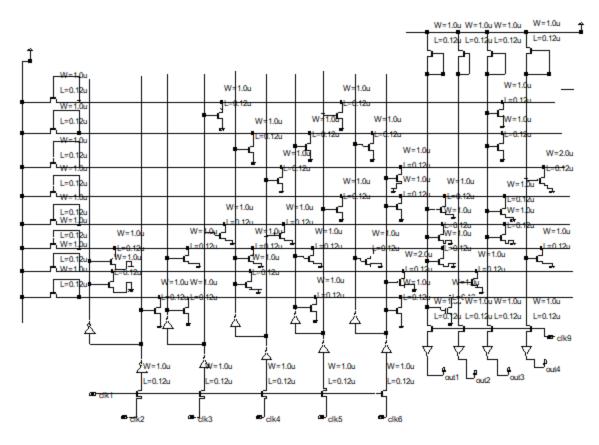


Figure 5.3: Circuit Diagram for 5x8x4 PLA

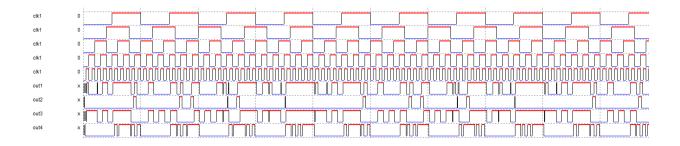


Figure 5.4: Output Waveform for 5x8x4 PLA Circuit