

1. (I) b
- II. a
- III. c
- IV. b
- V. a
- VI. c
- VII. d
- VIII. a
- IX. a

$$\begin{array}{r}
 0100 \\
 0400 \\
 \hline
 1000 \\
 z=0
 \end{array}
 \quad
 \begin{array}{l}
 e=0 \\
 s=0 \\
 s=1
 \end{array}$$

3. (a) We can use negative numbers as exponent in floating point addition using bias. So, we use bias for the benefit of comparison.

	Exponent	Fraction
(b) for zero:	0	0
for infinity:	255 (single precision)	0
	2047 (double precision)	0

4. (a) largest:
 exponent 1110, actual exponent = $14 - 7 = 7$
 fraction 111111111 (11 bit)
 significand 1

$$\text{largest} = 1.0 \times 2^7$$

smallest:
 exponent 0001, actual = $1 - 7 = -6$
 fraction 000...00 (11 bit)
 significand 1

$$\text{smallest} = 1.0 \times 2^{-6}$$

(b) largest:
 exponent 11110, actual = $30 - 15 = 15$
 fraction 11...11 (10 bit)
 significand 1

$$\text{largest} = 1.0 \times 2^{15}$$

smallest:
 exponent 00001, actual = $1 - 15 = -14$
 fraction, 00...00 (10 bit)
 significand 1

smallest number = 1.0×2^{-14}

6. $91 \% 3 = 1$

\therefore instruction = sw

wires: a, b, e, f, h, q, d, c, o, j,
l, m, p, r

Control signals:

ALUSrc
MemWrite
~~ALUSrc~~

$m_2 = 0$

$m_1 = 10$

$m_3 = 1$

$m_4 = 0$

5. (b) if (~~WB~~.reg write
 and (~~WB~~.registerid \neq 0)
 and (~~WB~~.registerid = ID.registerid))

Forward A = 10

if (WB.reg write
 and (WB.registerid \neq 0)
 and (WB.registerid = ID.registerid))

Forward B = 10

