

1. Line diagram and solving based on line diagram- Spring25 Q1
2. Comparator Spring25 Q1,fall24 Q2
3. VTC of inverting and non-inverting amplifier
4. Theory- saturation reason,virtual ground-Spring25 Q4, fall24 Q1
5. VTC and equation inverse design- Spring25 Q6,Q5
6. Comparator as not gate-Spring25 Q7
7. In and out Signal plot given, draw VTC- Summer24 Q1
8. Mid Summer 24 Q3
9. Fall24- 2,3

Topic wise must solve problems:

Line Diagram: 2,3,7,10

Open Loop: either of problem 2 or 3,5,6(a,b),8,12,13,15

Close Loop: 1(III,V,VI),2,4,5,9,11,13,17,19,20,22,24,26