Department of Computer Science and Engineering (CSE) BRAC University

Practice Problem Set 1

CSE251 - Electronic Devices and Circuits

ALTERNATIVE REPRESENTATION OF CIRCUITS

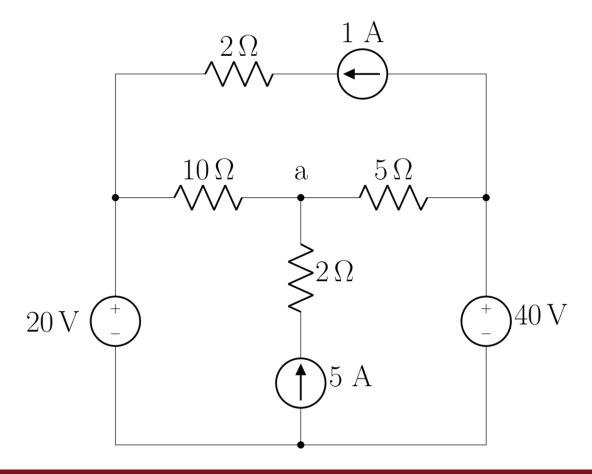
Circuit drawing, KCL, and KVL

Course Description, COs, and Policies



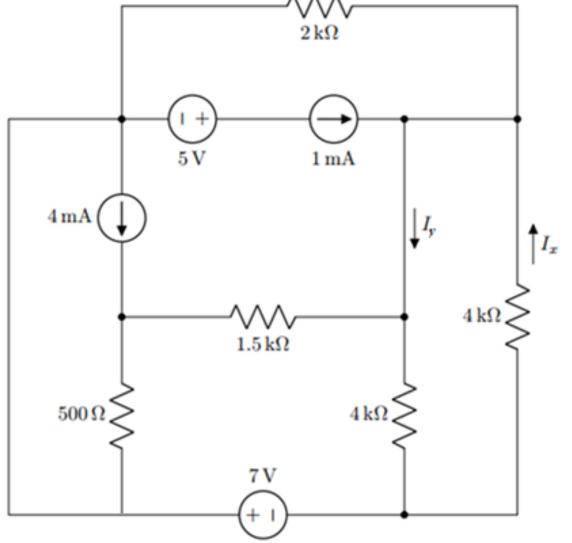
Midterm and Final Questions

 Draw an alternative representation of the following circuit minimizing the number of floating voltage sources.



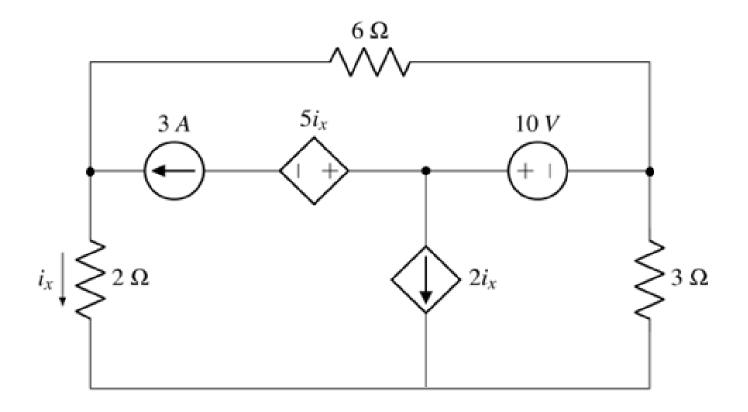


- Draw an alternative representation of the following circuit minimizing the number of floating voltage sources.
- Using KCL, solve for I_x and I_y .



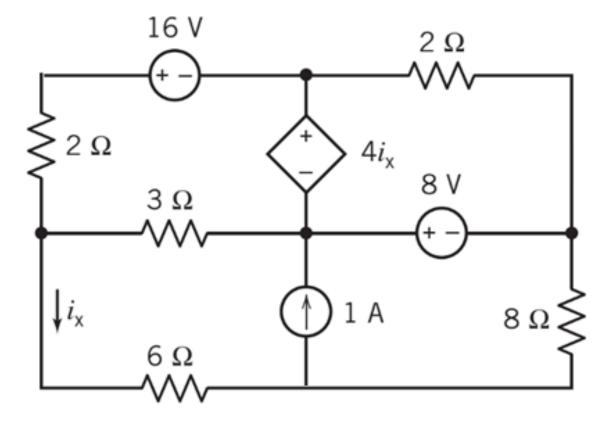


• Draw an alternative representation of the following circuit minimizing the number of floating voltage sources. Using KCL, determine i_{χ} .



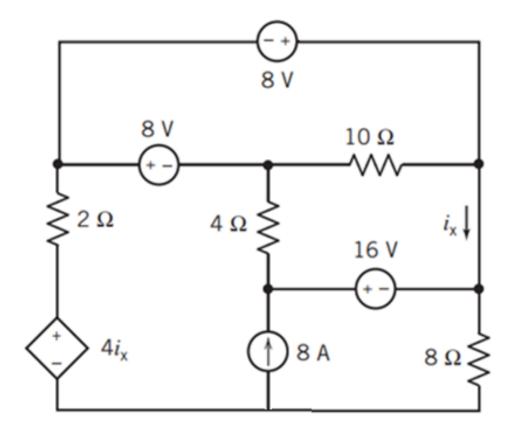


 Draw an alternative representation of the following circuit minimizing the number of floating voltage sources. Write all the KCL equations needed to solve this circuit.



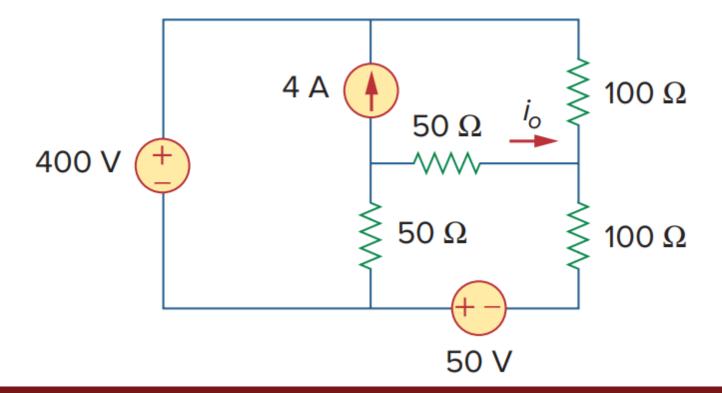


• Draw an alternative representation of the following circuit minimizing the number of floating voltage sources. Using KVL, solve for i_{χ} .



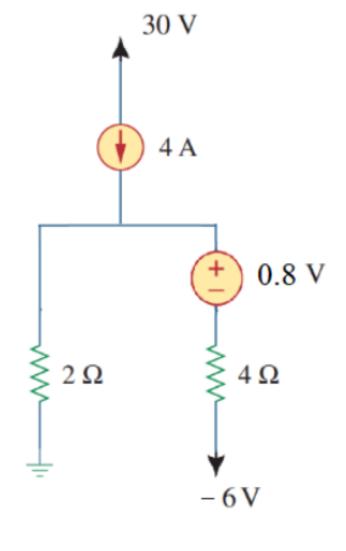


• How many KVL equations we can derive from the following circuit that are solvable? Draw an alternative representation minimizing the number of floating voltage sources. Solve for i_o using circuit laws as necessary.



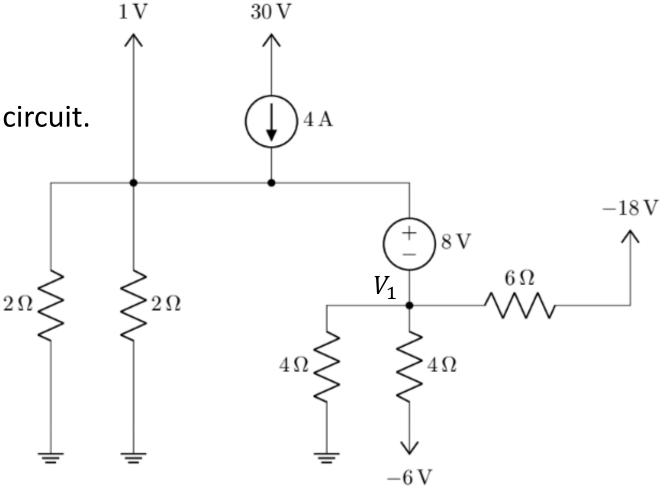


Draw a loop representation of the adjacent circuit.



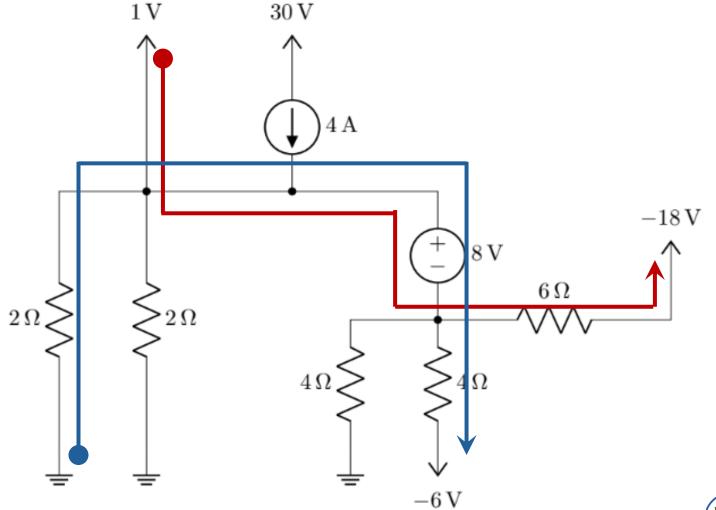


- Determine V_1 using KCL.
- Draw a loop representation of the circuit.



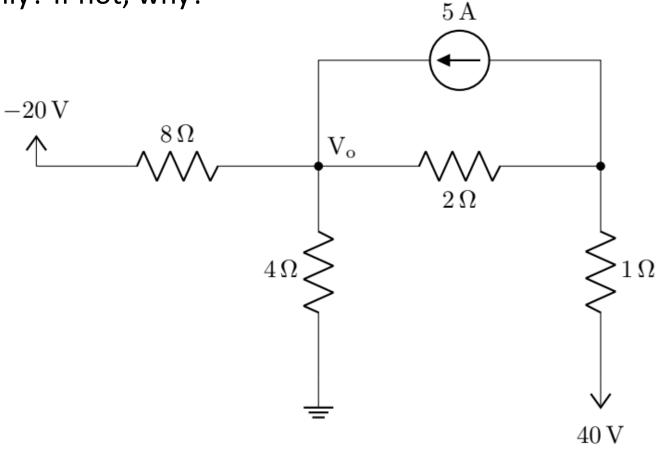


 Write KVL equations along the indicated arrows: maroon and blue.



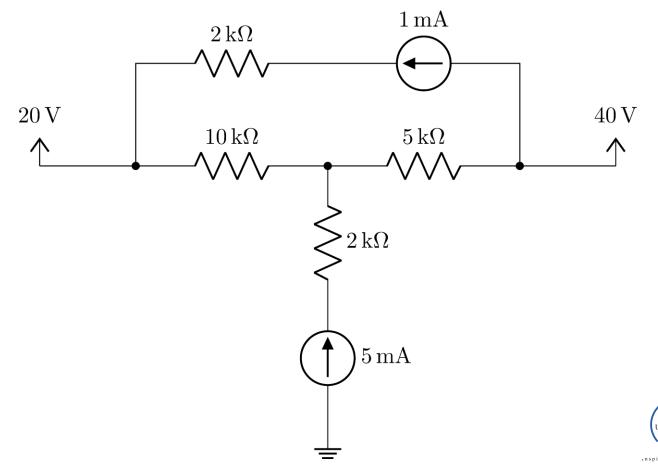


- Determine the voltage V_o and the voltage across the 5 A source using KCL.
- Can you solve it using KVL only? If not, why?



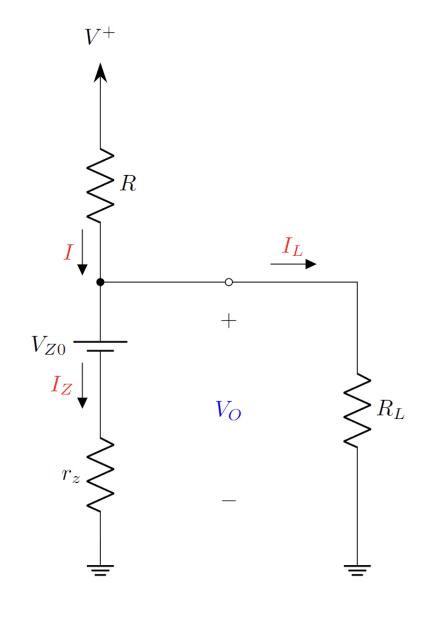


• For the circuit shown below, determine the voltage across the $1\,mA$ current source using KVL. Do not use KCL.





• For ${\it R}=100~\Omega$, ${\it R}_{\it L}=10~{\rm k}\Omega$, ${\it r}_{\it z}=20~\Omega$, ${\it V}_{\it Z0}=3$ V, and ${\it I}_{\it Z}=1$ mA, determine ${\it V}_{o}$, ${\it I}_{\it L}$, ${\it I}$, and ${\it V}^{+}$.





Acknowledgement and References

Some of the problems in this set are taken or adapted from the following sources:

- 1. Sadiku, M. N. O., Fundamentals of Electric Circuits, McGraw-Hill
- 2. Dorf, R. C., & Svoboda, J. A., Introduction to Electric Circuits, Wiley

