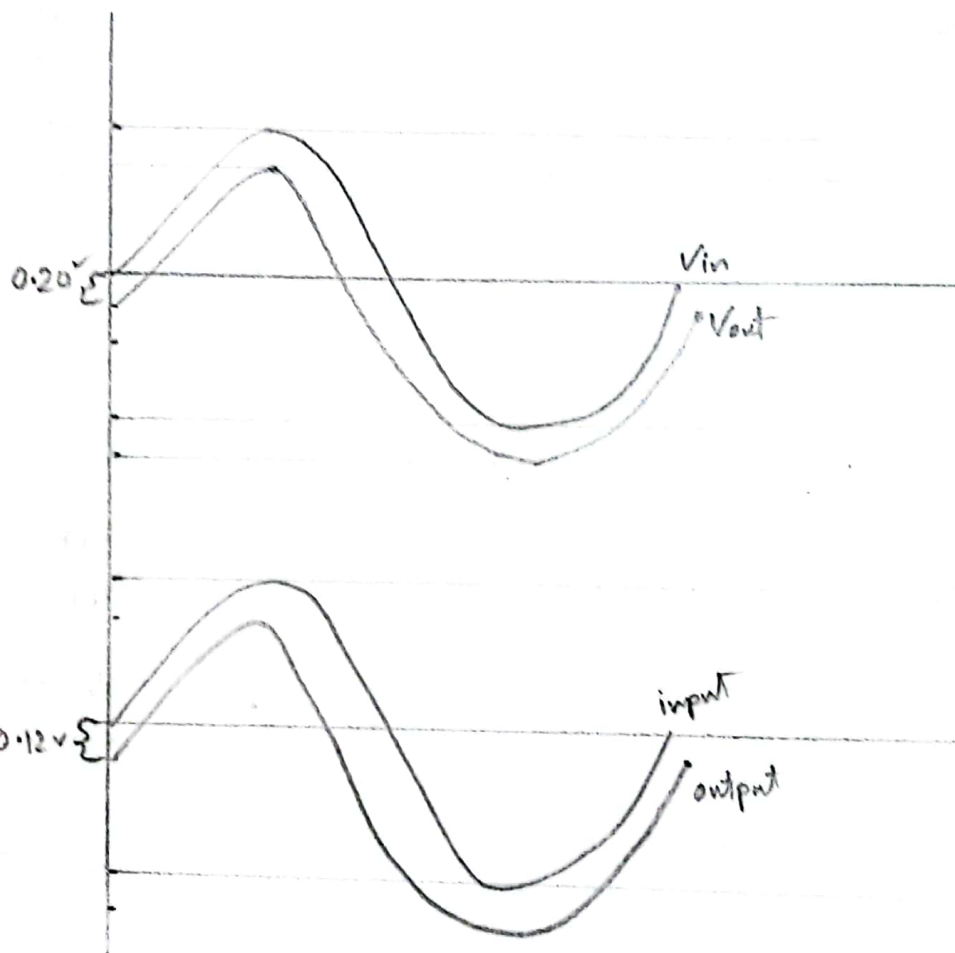
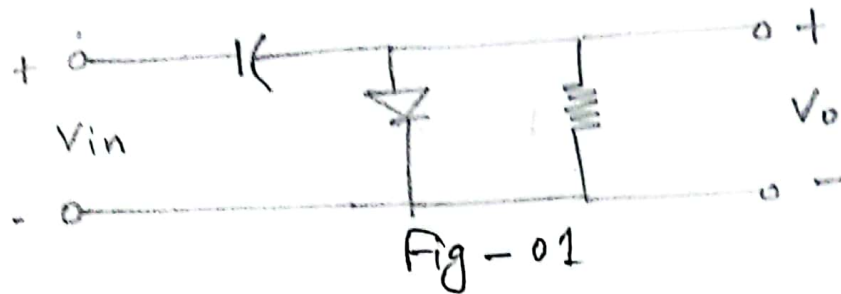


Exp no: 04

Exp name: Study of diode clamping circuits

Diagram:



$$V_{PP} = 8.56 \text{ V (input)}$$

$$V_{PP} = 8.16 \text{ V (output)}$$

$$V_{max} = 4.16 \text{ V (input)}$$

$$V_{max} = 720 \text{ mV (output)}$$

$$V_{min} = -4.40 \text{ V (input)}$$

$$V_{min} = -7.44 \text{ V (output)}$$

$$V_{PP} = 6 \text{ V (input)}$$

$$V_{PP} = 5.76 \text{ V (output)}$$

$$V_{max} = 2.80 \text{ V (input)}$$

$$V_{max} = 720 \text{ mV (output)}$$

$$V_{min} = -3.12 \text{ V (input)}$$

$$V_{min} = -5.04 \text{ V (output)}$$

Definit

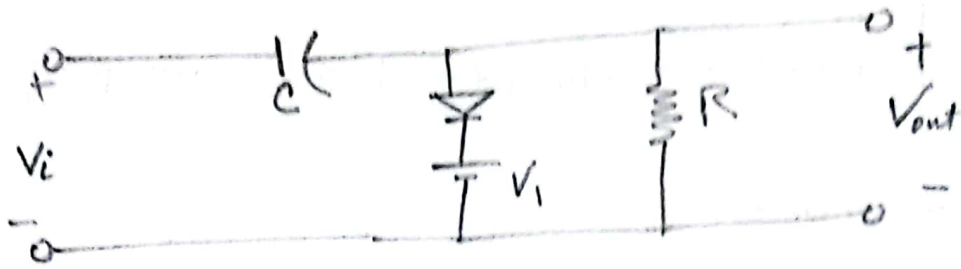
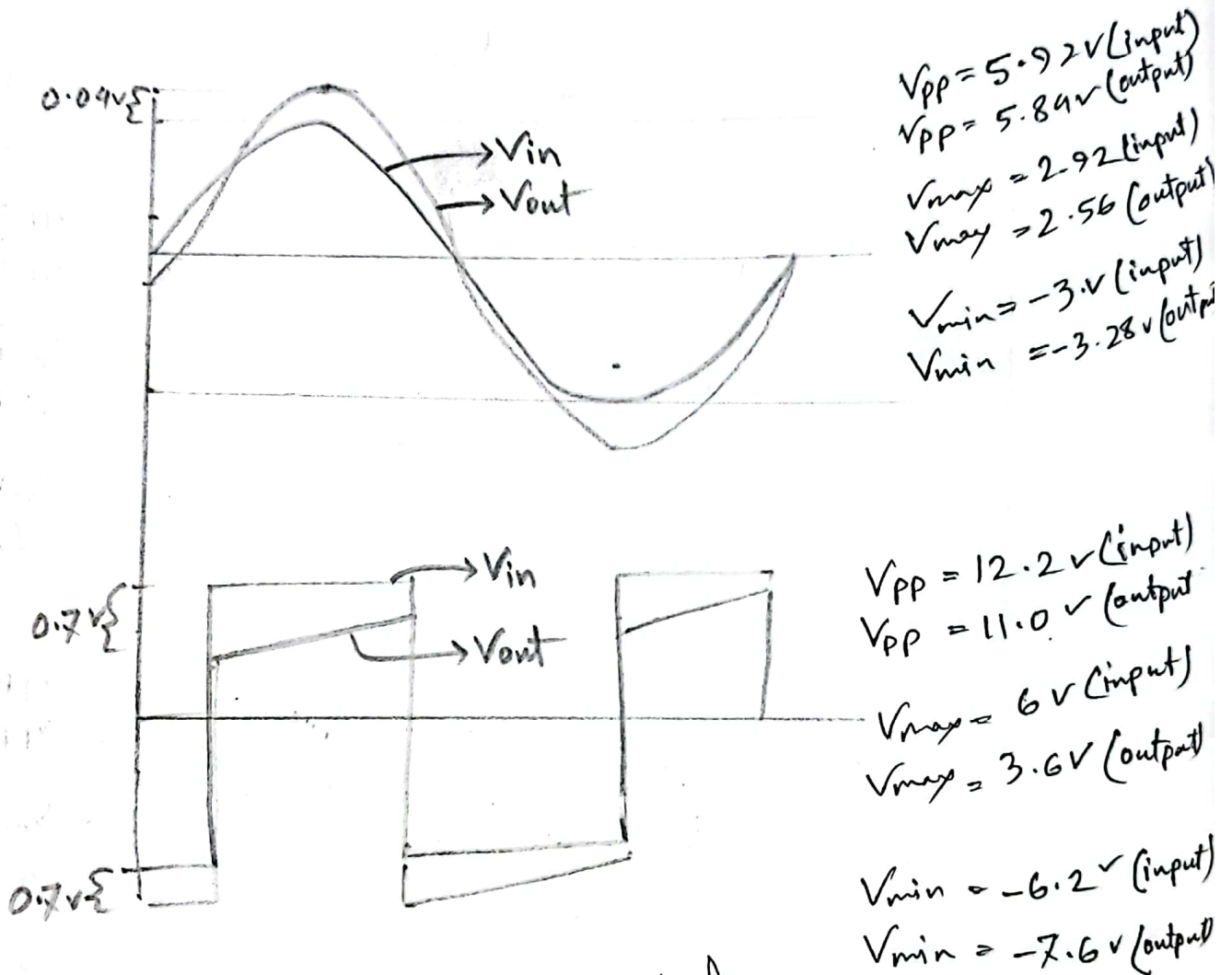


Fig - 2



*Amir*